SPEC. NO.	TQ3C-8EAF0-E1DDM04-00
DATE	January 24, 2006

S P E C

FOR : _____

TYPE : TCG062HV1AE-G00

C O N T E N T S

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Issued
Date: FEB. 03. 2006
KYOCERA
Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 24, 2006	H. Nishina	S. Oshita	M. Fujitani	Y. Matsumoto	S. Hayashi

Caution

1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.

2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.

3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

Designed by: Engineering Dept.   Confirmed by: QA Dept.

1. Application

   This data sheet defines the specification for a (640×R.G.B)×240 dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight.
   『RoHS Compliance』
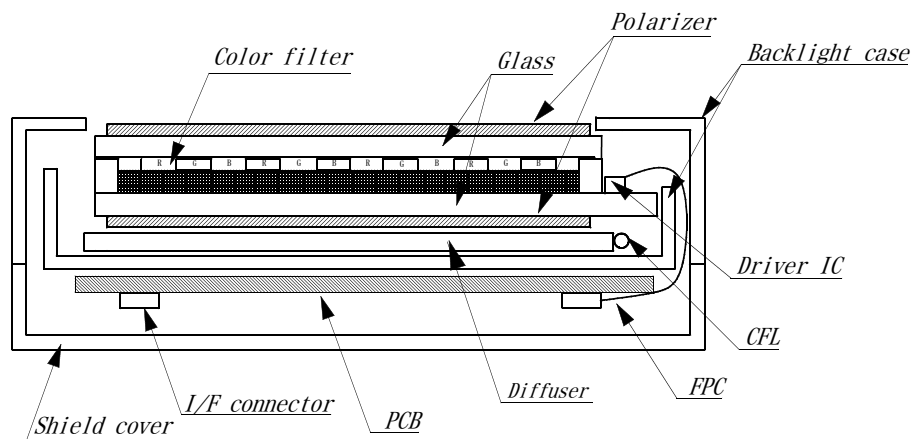
2. Construction and Outline

   (640×R.G.B)×240 dots, COG type LCD with CFL backlight.

   Backlight system   : Side-edge type CFL (1 tube).

   Inverter   : CXA-L0612A-VJL(TDK)
                           or equivalent

   Polarizer   : Glare treatment

   Additional circuits : Timing controller, Power supply (3.3V input)

This drawing is showing conception only.

3. Mechanical Specifications
 3-1. Mechanical specification of LCD panel

ITEM	SPECIFICATION	UNIT
Outline dimensions	174.2 (W) × 73.4 (H) × 12.2 (D)	mm
Effective viewing area	149.8 (W) × 57.4 (H)	mm
Dot number	(640×R.G.B) (W) × 240 (H)	Dots
Dot pitch	0.077 (W) × 0.231 (H)	mm
Display mode *1	Normally white	—
Mass	170	g

*1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage *1	Vin	−0.3	6.0	V

*1 Input signals : CK, R0～R5, G0～G5, B0～B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Operating temperature *1	Top	−10	70	℃
Storage temperature *2	Tsto	−30	80	℃
Operating humidity *3	Hop	10	*4	%RH
Storage humidity *3	Hsto	10	*4	%RH
Vibration	—	*5	*5	—
Shock	—	*6	*6	—

*1 Operating temperature means a temperature which operation shall be guaranteed.
   Since display performance is evaluated at 25℃, another temperature range should be confirmed.
*2 Temp. = −30 ℃ < 48 h , Temp = 80 ℃ < 168 h
   Store LCD panel at normal temperature/humidity.
   Keep it free from vibration and shock.
   LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.
   (Please refers to 12. Precautions for use as detail).
*3 Non-condensation.
*4 Temp. ≦ 40℃, 85%RH Max.
   Temp. > 40℃, Absolute Humidity shall be less than 85% RH at 40℃.
*5

Frequency	10～55 Hz	Converted to acceleration value : (0.3～9 m/s^2)
Vibration width	0.15 mm	
Interval	10-55-10 Hz 1 minute	

   2 hours in each direction X/Y/Z (6 hours as total)
   EIAJ ED-2531
*6 Acceleration: 490m/s^2
   Pulse width : 11 ms
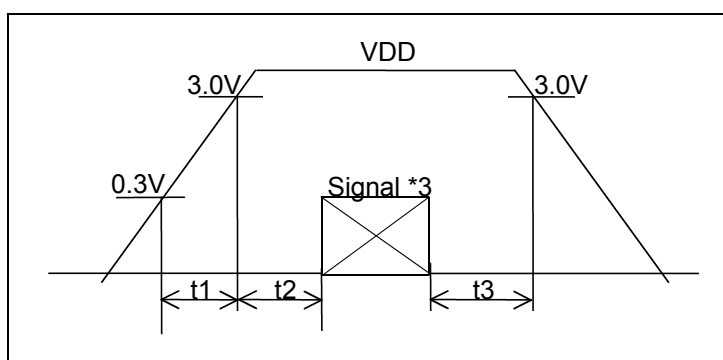   3 times in each direction : ±X/±Y/±Z.
   EIAJ ED-2531

5．Electrical Characteristics

5-1. LCD

Temp. = -10〜70℃

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Power input voltage ＊1	VDD=3.3V	VDD	3.0	3.3	3.6	V
Current consumption ＊2		IDD	―	230	300	mA
Permissive input ripple voltage(VDD=3.3V)		VRP	―	―	100	mVp-p
Input signal voltage (Low) ＊3		VIL	0	―	0.3VDD	V
Input signal voltage (High) ＊3		VIH	0.7VDD	―	+5.5	V

＊1 VDD-turn-on conditions
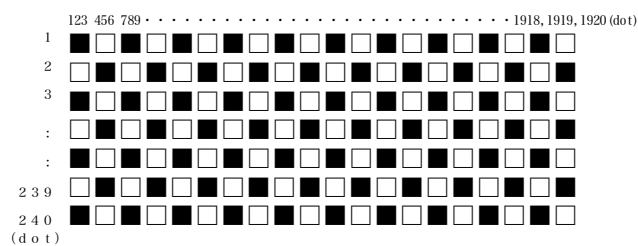

$0 < t\,1 \leqq 2\,0\,\text{ms}$

$0 < t\,2 \leqq 5\,0\,\text{ms}$

$0 < t\,3 \leqq 1\,\text{s}$

＊2 Power consumption
Black & White pattern :

VDD = 3.3V, V/Q=H

＊3 Input signals : CK, R0〜R5, G0〜G5, B0〜B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

6．Optical Characteristics

Measuring Spot ϕ 6.0mm, Temp.=25℃

ITEM		SYMBOL	CONDITION		MIN	TYP	MAX	UNIT
Response time	Rise	τ r	$\theta = \phi = 0°$		－	10	－	ms
	Down	τ d	$\theta = \phi = 0°$		－	15	－	ms
Viewing angle range		θ	CR≧5	Upper	－	85	－	deg.
				Lower	－	50	－	
		ϕ		Left	－	75	－	deg.
				Right	－	75	－	
Contrast ratio		CR	$\theta = \phi = 0°$		300	500	－	－
Brightness		L	IL = 5.0mArms.		260	350	－	cd/m²
Chromaticity coordinates	Red	x	$\theta = \phi = 0°$		0.56	0.61	0.66	－
		y			0.29	0.34	0.39	
	Green	x	$\theta = \phi = 0°$		0.26	0.31	0.36	
		y			0.49	0.54	0.59	
	Blue	x	$\theta = \phi = 0°$		0.10	0.15	0.20	
		y			0.07	0.12	0.17	
	White	x	$\theta = \phi = 0°$		0.25	0.30	0.35	
		y			0.27	0.32	0.37	

6-1. Definition of Contrast (Transmissive Mode)

$$CR \ (Contrast) \ = \ \frac{Brightness \ at \ all \ pixels \ "White"}{Brightness \ at \ all \ pixels "Black"}$$

6-2. Definition of viewing angle

$\phi = 0°$

+Y

$+\theta$ $-\theta$

-X   +X

-Y

(θ direction)

$\theta = 0°$

+Y

$-\phi$ $+\phi$

-X   +X

-Y

(ϕ direction)
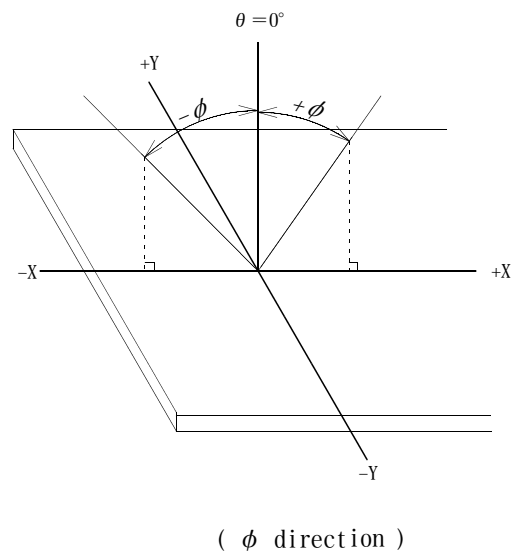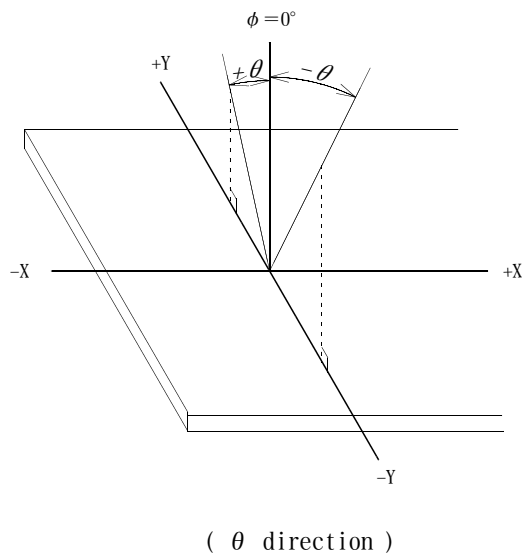
6-3. Measuring points

160×3   320×3   480×3   (dot)

60   1   4

3

120

2   5

180

(dot)

1) Rating is defined as the average brightness inside the viewing area.
2) 30 minutes after CFL is turned on. (Ambient Temp.=25℃)
3) The inverter should meet the rating of the CFL.
   Sine, symmetric waveform without spike in positive and negative.

7. Interface signals

7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	I/O	Note
1	GND	GND	−	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	Vsync	Vertical synchronous signal (negative)	I	
5	GND	GND	−	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	−	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	−	
20	B0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	B3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	−	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	−	
29	VDD	3.3V power supply	−	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal L : Normal , H : Up / Down reverse mode	I	*2
32	V/Q	H : Normal (VGA)	I	
33	GND	GND	−	

LCD side connector　　　　　: 08-6210-033-340-800+ (ELCO)
Recommended matching FFC　: FFC or FPC (P=0.5mm)

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2.
Don't keep ENAB "High" during operation.

*2

R/L = L
U/D = L

R/L = H
U/D = L

R/L = L
U/D = H

R/L = H
U/D = H
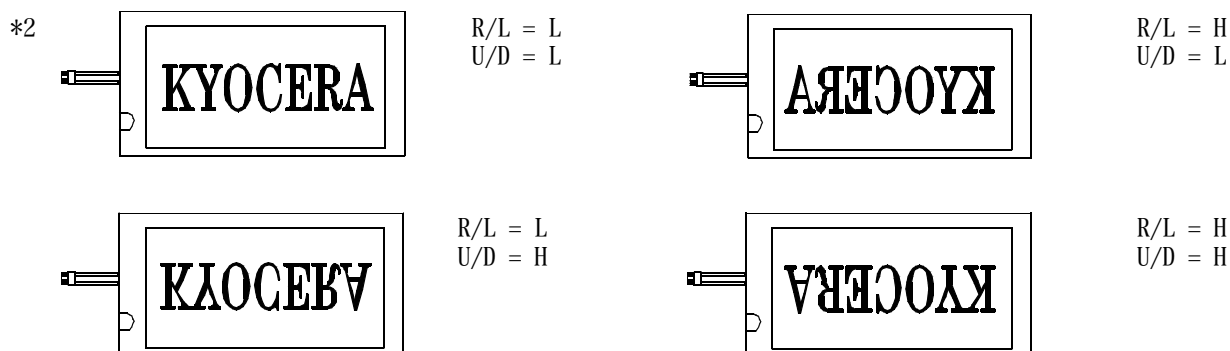
7-2. C F L

PIN NO.	SYMBOL	DESCRIPTION
1	Hot	Inverter output high voltage side
2	NC	—
3	Cold	Inverter output low voltage side

LCD side connector   : BHR-03VS-1   (JST)
Recommended matching connector : SM02-(8.0)B-BHS-1   (JST)
                                       : SM02-(8.0)B-BHS-1-TB(LF)(SN) (JST)・・・(RoHS)

＊ Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low
   voltage side. It may result in damage or electric chock.

8. Timing Characteristics of input signals

8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Clock	Frequency	1/Tc	—	25.18	28.33	MHz	V/Q=H
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	—	—	ns	
	Hold time	Tdh	10	—	—	ns	
Horizontal sync. signal	Cycle	TH	30.0	31.8	—	μs	V/Q=H
			770	800	900	clock	
	Pulse width	THp	2	96	200	clock	
Vertical sync. signal	Cycle	TV	515	525	560	line	V/Q=H
	Pulse width	TVp	2	—	34	line	
Horizontal display period		THd		640		clock	
Hsync.-Clock phase difference		THc	10	—	Tc-10	ns	
Hsync.-Vsync. phase difference		TVh	0	—	TH-THp	ns	
Vertical sync.signal start position		TVs		34		line	V/Q=H
Vertical display period		TVd		240		line	

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position
The horizontal display position is determined by ENAB signal.

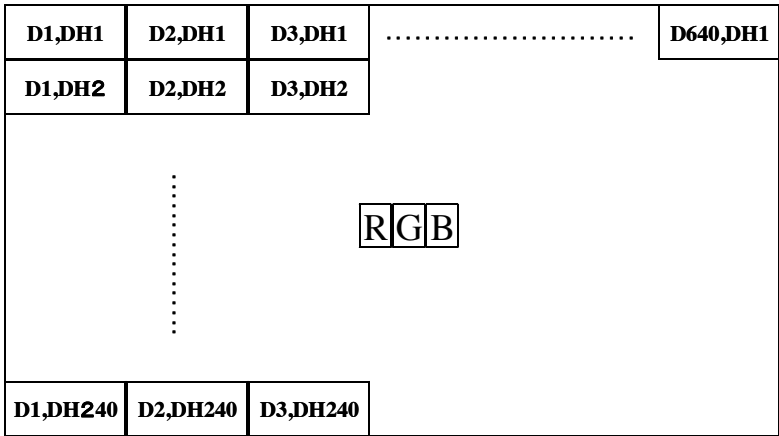| ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Enable signal	Set up time	Tes	5	—	Tc-10	ns	
	Pulse width	Tep	2	320	TH-10	clock	
Hsync.-Enable signal phase difference		The	44	—	TH-664	clock	V/Q=H

* When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.
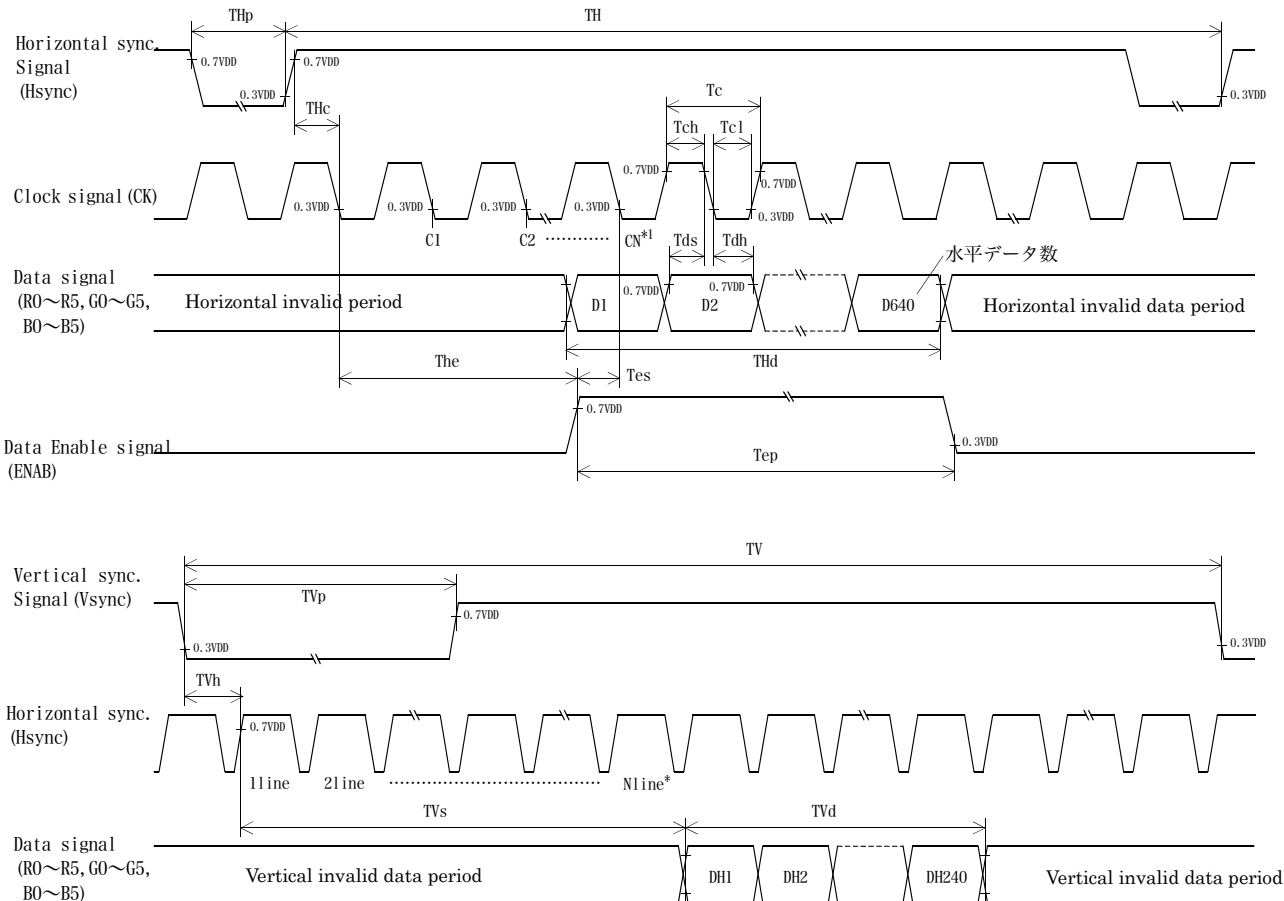
8-3. Vertical display position
The vertical display position (TVs) is fixed at 34th line (V/Q=H).
Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1	....	D640,DH1
D1,DH2	D2,DH2	D3,DH2		
			R G B	
D1,DH240	D2,DH240	D3,DH240		

8-5. Input Timing Characteristics

Horizontal sync. Signal (Hsync) — THp — TH — 0.7VDD — 0.3VDD — THc — 0.3VDD

Clock signal (CK) — 0.3VDD — Tc — Tch — Tcl — 0.7VDD — 0.7VDD — 0.3VDD — C1 — C2 .... — CN^{*1} — Tds — Tdh

Data signal (R0～R5, G0～G5, B0～B5) — Horizontal invalid period — D1 — 0.7VDD — D2 — 0.7VDD — 水平データ数 — D640 — Horizontal invalid data period — The — Tes — THd

Data Enable signal (ENAB) — 0.7VDD — Tep — 0.3VDD

Vertical sync. Signal (Vsync) — TV — TVp — 0.7VDD — 0.3VDD — 0.3VDD — TVh

Horizontal sync. (Hsync) — 0.7VDD — 1line — 2line .... — $Nline^{*}$ — TVs — TVd

Data signal (R0～R5, G0～G5, B0～B5) — Vertical invalid data period — DH1 — DH2 — DH240 — Vertical invalid data period

*1 When ENAB is fixed "Low" the display starts from the data of C104(Clock)

*2 The vertical display position(TVs) is fixed at 34th line.

9．Backlight Characteristics

ITEM		SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage		VS	—	—	1,077 Vrms.	−10 ℃
	*1		—	—	645 Vrms.	25 ℃
Discharging tube current *2		IL	3.0 mArms.	—	6.0 mArms.	—
Discharging tube voltage		VL	—	395 Vrms.	—	IL=5.0mArms
Operating life *3		T	36,000 h	54,000 h	—	IL=5.0mArms
Operating frequency *4		F	40 kHz	—	100 kHz	—

*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin,
     because VS may increase due to the leak current which may be caused by wiring of CFL cables.
     (Reference value : 1,400 Vrms Min.)

*2 We recommend that you should set the discharging tube current at lower than typical value
     so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.

*3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the
     initial value. Illuminance of light will drastically decrease when LCD is operated at lower
     temperature for long hours.

*4 The driving frequency of the CFL may interfere with the horizontal synchronous signal ,leaving
     interference stripes on the display. So please evaluate LCD panels beforehand.
     To avoid interference stripes, we recommend to separate as far as possible the CFL frequency
     from the horizontal synchronous signal and its high harmonic frequency.

*   There may be cases where interface noise on LCD PCB, generated by high-voltage products such as
     inverters, may leave stripes on the display. Please be careful when designing a mold to take
     into consideration that the inverter shall be located as far as possible from PCB. Shield
     protection may be effective.

１０．Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

```
ＴＣＧ０６２ＨＶ１ＡＥ－Ｇ００ －□ □－□ □ － □ ＭＡＤＥ ＩＮ □□□□□
                                ↓ ↓   ↓  ↓     ↓                      ↓
                                ①  ②  ③      ④                      ⑤
```

①YEAR
②MONTH
③DATE
④Version Number
⑤Country of origin(Japan or China)

YEAR	2006	2007	2008	2009	2010	2011
CODE	6	7	8	9	0	1

MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

１１．Warranty

11-1. Incoming inspection

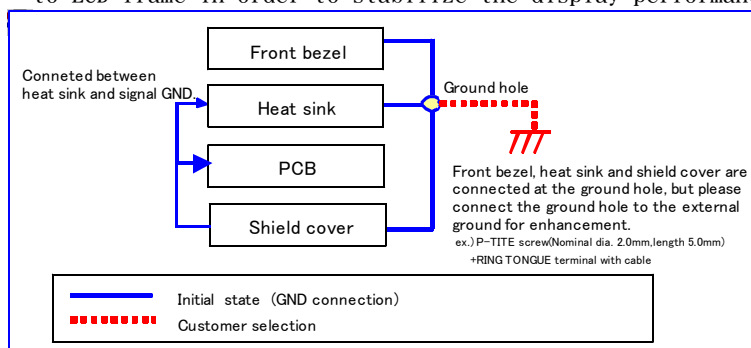Please inspect the LCD within one month after your receipt.

11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

12. Precautions for use

12-1. Installation of the LCD
1. LCD hole(left side) are not connected with GND, but the LCD is structured to have GND connection available to protect against noise. We recommend to connect customer's frame GND to LCD frame in order to stabilize the display performance.

```
                          ┌─────────────────┐
Conneted between          │  Front bezel    │
heat sink and signal GND  └─────────────────┘      Ground hole
                          ┌─────────────────┐
                          │   Heat sink     │──────○········
                          └─────────────────┘       ⏚
                          ┌─────────────────┐
                          │      PCB        │      Front bezel, heat sink and shield cover are
                          └─────────────────┘      connected at the ground hole, but please
                          ┌─────────────────┐      connect the ground hole to the external
                          │  Shield cover   │      ground for enhancement.
                          └─────────────────┘      ex.) P-TITE screw(Nominal dia. 2.0mm,length 5.0mm)
                                                   +RING TONGUE terminal with cable

   ───────────    Initial state (GND connection)
   ▪▪▪▪▪▪▪▪       Customer selection
```

2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
3. The LCD shall be installed so that there is no pressure on the LSI chips.
4. The LCD shall be installed flat, without twisting or bending.
5. The display window size should be the same as the effective viewing area.
6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
7. Please refer to the following our recommendable value of Clamp-down torque when installing.
   Clamp-down torque:0.265±0.025N.m
   Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .
   Recommendable screw P-TITE screw two types nominal dia.3.0mm installing boss hole depth 4.2+/-0.5mm
   Please be careful not to use high torque which may damage LCD module in installation.
8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
9. Do not pull the CFL lead wires and do not bend the root of the wires.
   Housing should be designed to protect CFL lead wires from external stress.
10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.
    Hence, when the LCD is supposed to be used in a special environment, evaluate
    the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity
1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation
1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.
   It may also change the characteristics of the liquid crystal.
   This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage
1. The LCD shall be stored within normal temperature and humidity.
   Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
2. Always store the LCD so that it is free from external pressure onto it.

12-5. Handling
  1. <u>DO NOT</u> store in a high humidity environment for extended periods.
     Image degradation, bubbles, and/or peeling off of polarizer may result.
  2. The front polarizer is easily scratched or damaged.
     Prevent touching it with any hard material, and from being pushed or rubbed.
  3. The LCD screen may be cleaned with a soft cloth or cotton pad.
     Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
  4. Water may cause damage or discoloration of the polarizer.
     Clean any condensation or moisture from any source immediately.
  5. Always keep the LCD free from condensation during testing.
     Condensation may permanently spot or stain the polarizers.
  6. Do not disassemble LCD module because it will result in damage.
  7. Please do not use solid-base image pattern for long hours because a temporary afterimage may
     appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must
     be used.
  8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid
     go into your eyes and mouth. In the case the fluid touches your body, rinse it off
     right away with water and soap.

１３．Reliability Data / Environmental Test

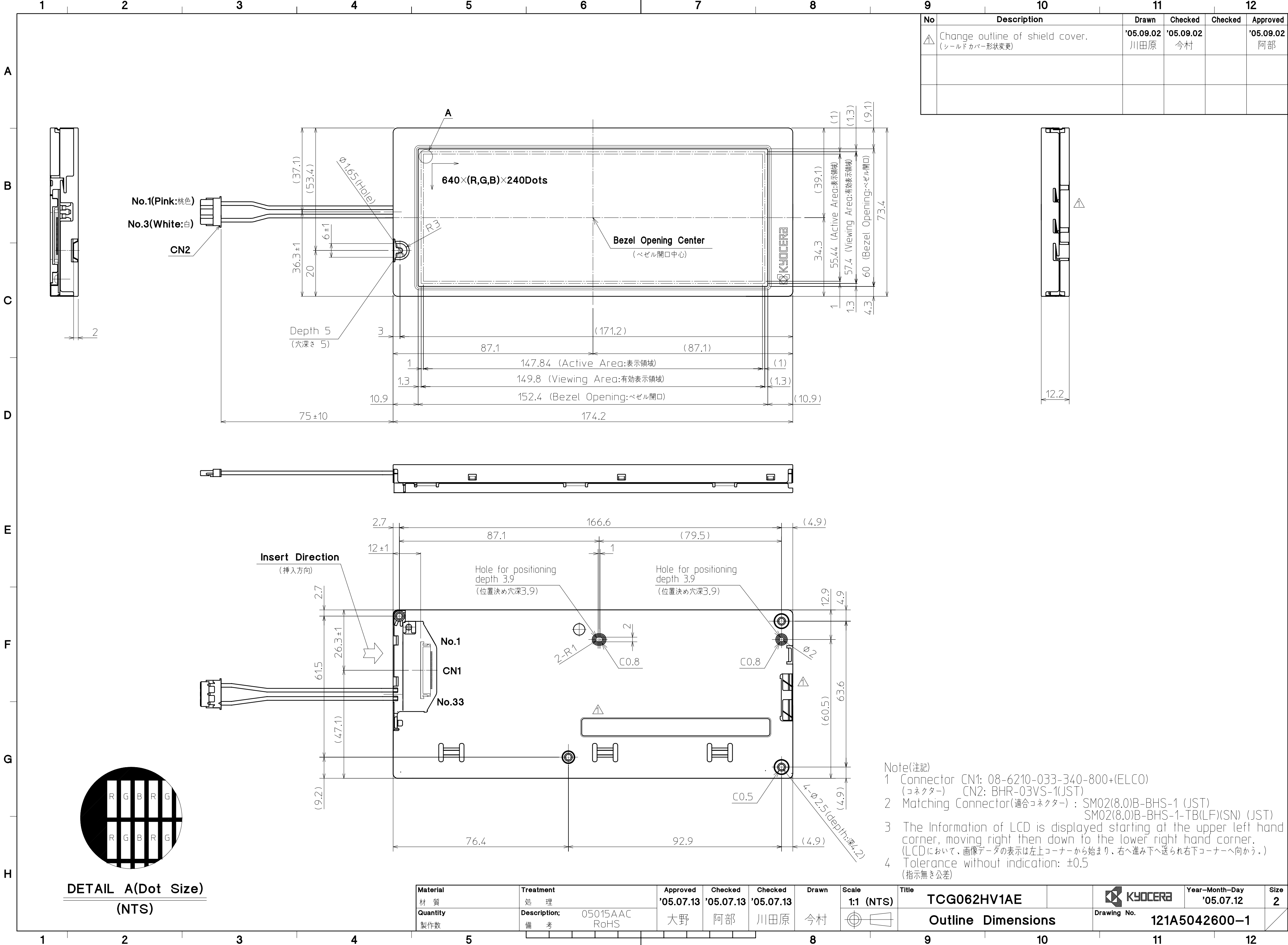| TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	–30℃	240 h	Low Temp. Bubble: None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90 %RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	–30℃ 0.5 h R.T. 0.5 h 80℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	70℃	500 h	Display Quality : No defect Current Consumption : No defect

  ＊ Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

  ＊ The LCD is tested in circumstances in which there is no condensation.

  ＊ The tested LCD is inspected after 24 hours of storage at room temperature and room humidity
     after each test is finished.

  ＊ The reliability test is not an out-going inspection.

  ＊ The results of the reliability test are for your reference purpose only.
     The reliability test is conducted only to examine the LCD's capability.

DETAIL A(Dot Size)
(NTS)

A

640×(R,G,B)×240Dots

Bezel Opening Center
(ベゼル開口中心)

No.1(Pink:桃色)
No.3(White:白)
CN2

φ1.65(Hole)
(穴)

Depth 5
(穴深さ5)

R3

(37.1)
(53.4)
36.3±1
20
6±1

3
(171.2)
87.1   (87.1)
1   147.84 (Active Area:表示領域)   (1)
1.3   149.8 (Viewing Area:有効表示領域)   (1.3)
10.9   152.4 (Bezel Opening:ベゼル開口)   (10.9)
75±10
174.2

(1)
(1.3)
(9.1)
(39.1)
55.44 (Active Area:表示領域)
57.4 (Viewing Area:有効表示領域)
60 (Bezel Opening:ベゼル開口)
73.4
34.3
1
1.3
4.3

2
12.2

Insert Direction
(挿入方向)

2.7
12±1
87.1
166.6
(79.5)
(4.9)
1

Hole for positioning
depth 3.9
(位置決め穴深3.9)

Hole for positioning
depth 3.9
(位置決め穴深3.9)

2.7
26.3±1
61.5
(47.1)
(9.2)

No.1
CN1
No.33

2-R1
C0.8
2
C0.8
φ2

12.9
4.9
63.6
(60.5)

76.4   92.9   (4.9)
C0.5
4-φ2.5(depth:深4.2)

R G B R G
R G B R G

Note(注記)
1 Connector CN1: 08-6210-033-340-800+(ELCO)
   (コネクター)   CN2: BHR-03VS-1(JST)
2 Matching Connector(適合コネクター) : SM02(8.0)B-BHS-1 (JST)
                                      SM02(8.0)B-BHS-1-TB(LF)(SN) (JST)
3 The Information of LCD is displayed starting at the upper left hand
   corner, moving right then down to the lower right hand corner.
   (LCDにおいて、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)
4 Tolerance without indication: ±0.5
   (指示無き公差)

SPEC. NO.	TQ3C-8EAF0-E2DDM04-00
DATE	January 24, 2006

FOR : _____

KYOCERA INSPECTION STANDARD
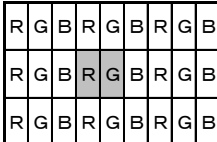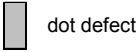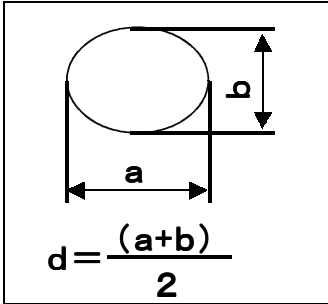
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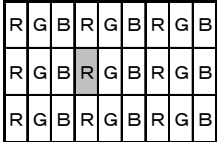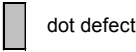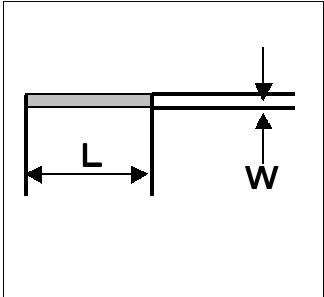
KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 24, 2006	H. Nishino	S. Oshita	M. Fujitani	Y. Matsumoto	S. Higashi

Revision Record

Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

1) Note

	Note		
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. Inspection Conditions 　Luminance　　　　　　 : 500 Lux minimum 　Inspection distance　 : 300 mm (from the sample) 　Temperature　　　　　 : 25 ± 5 ℃ 　Direction　　　　　　 : directly above		
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter.
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.
	Definition of size	Definition of circle size $d = \dfrac{(a+b)}{2}$	Definition of linear size

2) Standard

Classification		Inspection item		Judgement standard		
defect (in LCD glass)	Dot defect	Bright dot defect		Acceptable number　　：4 bright dots defects Bright dot spacing　：5 mm or more		
		Black dot defect		Acceptable number　　：5 black dots defects Black dot spacing　　：5 mm or more		
		2 dots join	Bright dot defect	Acceptable number　　：2		
			Black dot defect	Acceptable number　　：3		
		3 or more dots join		Acceptable number　　：0		
		Total dot defects		Acceptable number　　：5 Max		
	Others	White dot, Dark dot (Circle)		Size(mm)		Acceptable Number
				d＜0.2		(neglected)
				0.2＜d≦0.4		5
				0.4＜d≦0.5		3
				0.5＜d		0
External inspection (Defect on Polarizer or between Polarizer and LCD glass)		Polarizer(Scratches)		Width(mm)	Length(mm)	Acceptable Number
				W≦0.1	–	(neglected)
				0.1＜W≦0.3	L≦5.0	(neglected)
					5.0＜L	0
				0.3＜W	–	0
		Polarizer Touch panel (Bubble, Dent)		Size(mm)		Acceptable Number
				d＜0.2		(neglected)
				0.2＜d≦0.3		5
				0.3＜d≦0.5		3
				0.5＜d		0
		Foreign Particle(Circular shape)		Size(mm)		Acceptable Number
				d＜0.2		(neglected)
				0.2＜d≦0.4		5
				0.4＜d≦0.5		3
				0.5＜d		0
		Foreign Particle (Linear shape), Scratches		Width(mm)	Length(mm)	Acceptable Number
				W≦0.03	–	(neglected)
				0.03＜W≦0.1	L≦2.0	(neglected)
					2.0＜L≦4.0	3
					4.0＜L	0
				0.1＜W	–	(According to Circular shape)