SPEC.NO.	TQ3C-8EAF0-E1DKB15-00
DATE	September 7, 2007

S P E C

FOR : _____

TYPE : THG062HVLAM-G00

C O N T E N T S

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KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 7, 2007	T. Onodera	Y. Yamazaki	Y. Matsumoto	J. Sakaguchi	

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.

2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

1. Application

This data sheet defines the specification for a $640 \times (R.G.B) \times 240$ dot, amorphous silicon TFT transflective color dot matrix type Liquid Crystal Display with LED backlight. 『RoHS Compliant』
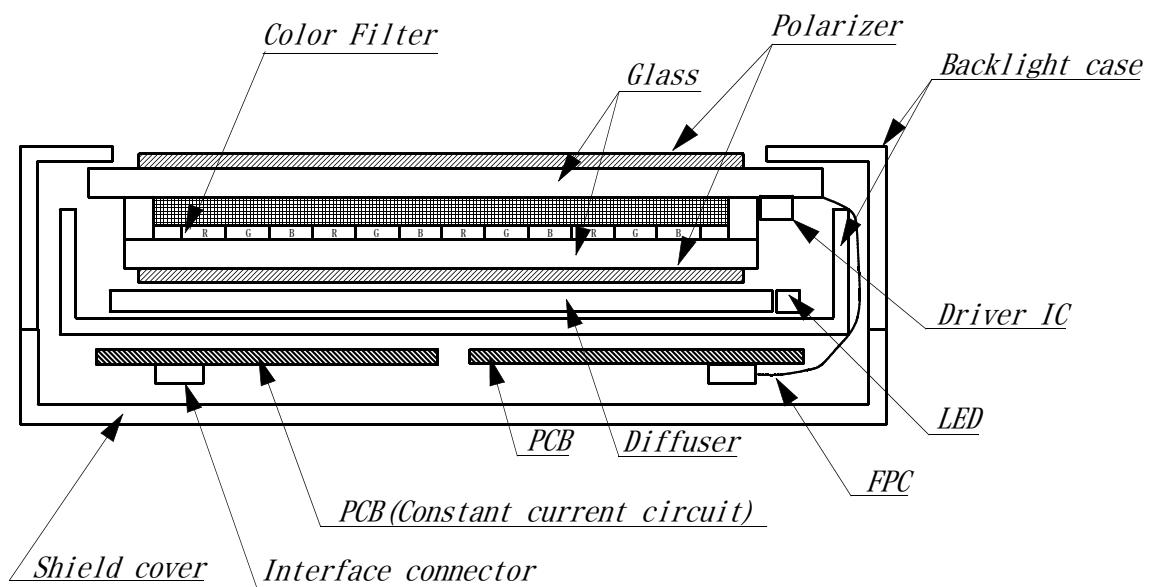
2. Construction and Outline

$640 \times (R.G.B) \times 240$ dots, COG type LCD with LED backlight.

Backlight system   : Side-edge type (LED).

Polarizer   : Glare treatment.

Additional circuits : Timing controller, Power supply (3.3V input)
                      Constant current circuit for LED backlight.

This drawing is showing conception only.

3．Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	174.2 (W) × 73.4 (H) × 12.2 (D)	mm
Effective viewing area	149.8 (W) × 57.4 (H)	mm
Dot number	640×(R.G.B) (W) × 240 (H)	Dots
Dot pitch	0.077 (W) × 0.231 (H)	mm
Display mode ＊1	Normally white	－
Mass	(TBD)	g

＊1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

4．Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage ＊1	Vin	−0.3	6.0	V
LED forward current	IF	－	(17)	mA
Supply voltage for backlight	VinB	0	(6.0)	V
Backlight ON-OFF	BLEN	0	VinB	V
Brightness adjust voltage	VBRT	0	VinB	V

＊1 Input signals ： CK, R0～R5, G0～G5, B0～B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Operating temperature　*1	Top	(-20)	(70)	℃
Storage temperature　*2	Tsto	(-30)	(80)	℃
Operating humidity　*3	Hop	10	*4	%RH
Storage humidity　*3	Hsto	10	*4	%RH
Vibration	―	*5	*5	―
Shock	―	*6	*6	―

*1　Operating temperature means a temperature which operation shall be guaranteed.
　　Since display performance is evaluated at 25℃, another temperature range should be
　　confirmed.

*2　Temp. = (-30) ℃ < 48 h , Temp = (80) ℃ < 168 h
　　Store LCD panel at normal temperature/humidity.
　　Keep it free from vibration and shock.
　　LCD panel that is kept at low or high temperature for a long time can be defective due to
　　the other conditions, even if the temperature satisfies standard.
　　(Please refers to 12. Precautions for use as detail).

*3　Non-condensation.

*4　Temp. ≦ 40℃, 85%RH Max.
　　Temp. > 40℃, Absolute Humidity shall be less than 85% RH at 40℃.

*5

Frequency	10～55 Hz	Converted to acceleration value : (0.3～9 m/s^2)
Vibration width	0.15 mm	
Interval	10-55-10 Hz　1 minute	

　　2 hours in each direction　X/Y/Z (6 hours as total)
　　EIAJ ED-2531

*6　Acceleration:　490m/s^2
　　Pulse width :　11 ms
　　3 times in each direction :　±X/±Y/±Z.
　　EIAJ ED-2531

5．Electrical Characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Power input voltage *1	VDD=3.3V Temp.=25℃	VDD	(3.0)	(3.3)	(3.6)	V
Current consumption *2		IDD	—	(230)	(300)	mA
Permissive input ripple voltage(VDD=3.3V)		VRP	—	—	100	mVp-p
Input signal voltage (Low) *3		VIL	0	—	0.3VDD	V
Input signal voltage (High) *3		VIH	0.7VDD	—	5.5	V

*1 VDD-turn-on conditions
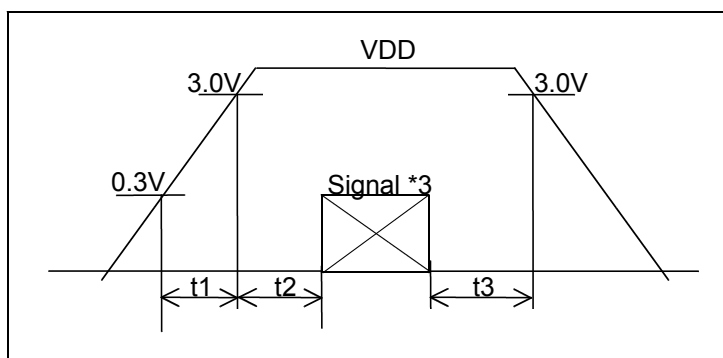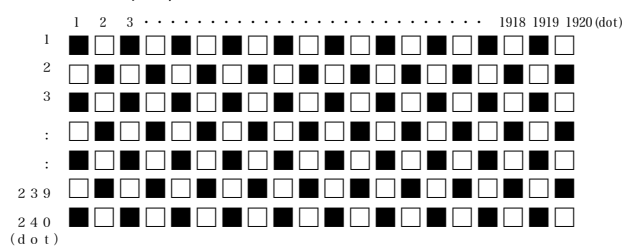

$0 < t1 \leqq 20\,ms$

$0 < t2 \leqq 50\,ms$

$0 < t3 \leqq 1\,s$

*2 Power consumption
     pattern :

VDD = 3.3V, V/Q=H

*3 Input signals :   CK, R0～R5, G0～G5, B0～B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

6. Optical Characteristics

6-1. Reflective mode

Measuring points = ϕ6.0mm , Temp. = 25℃

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Response time	Rise	τ r	$\theta=\phi=0°$	―	(10)	―	ms
	Down	τ d	$\theta=\phi=0°$	―	(10)	―	ms
Contrast ratio		CR	$\theta=\phi=0°$	(20)	(35)	―	―
Refrectance		ρ	$\theta=\phi=0°$	(15)	(25)	―	%

6-2. Transmissive mode

Measuring points = ϕ6.0mm , Temp. = 25℃

ITEM		SYMBOL	CONDITION		MIN	TYP	MAX	UNIT
Response time	Rise	τ r	$\theta=\phi=0°$		―	(10)	―	ms
	Down	τ d	$\theta=\phi=0°$		―	(10)	―	ms
Viewing angle range		θ	CR$\geqq$5	Upper	―	(40)	―	deg.
				Lower	―	(40)	―	
		ϕ		Left	―	(60)	―	deg.
				Right	―	(70)	―	
Contrast ratio		CR	$\theta=\phi=0°$		(25)	(40)	―	―
Brightness		L	IF=15mA/1LED line		(40)	(60)	―	cd/m²
Chromaticity coordinates	Red	x	$\theta=\phi=0°$		(TBD)	(TBD)	(TBD)	―
		y			(TBD)	(TBD)	(TBD)	
	Green	x	$\theta=\phi=0°$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	Blue	x	$\theta=\phi=0°$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	White	x	$\theta=\phi=0°$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	

6-3. Definition of Reflectance

$$\rho \text{ (Reflectance)} = \frac{\text{Measured Reflection Brightness}}{\text{Reflection Brightness against Standard White Board}} \times 100 \quad (\%)$$
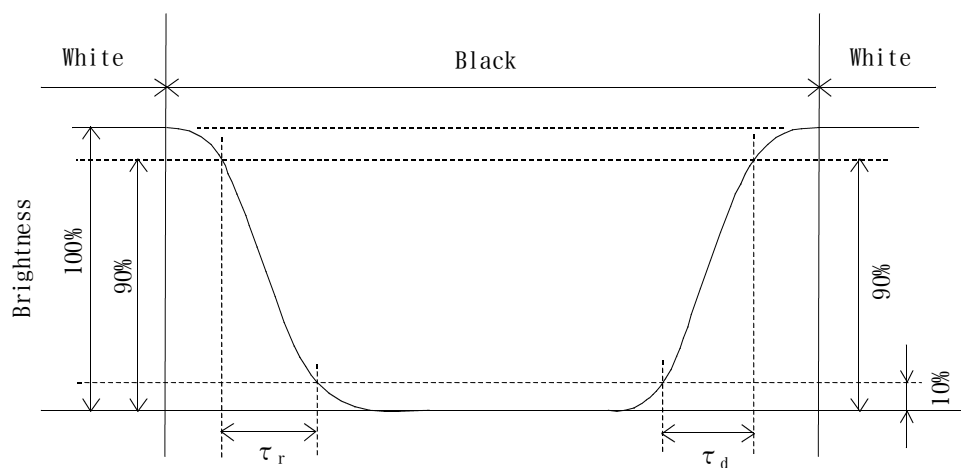
6-4. Definition of Contrast (Reflective Mode)

$$CR \text{ (Contrast)} = \frac{\text{Reflectance at all pixels "White"}}{\text{Reflectance at all pixels "Black"}}$$
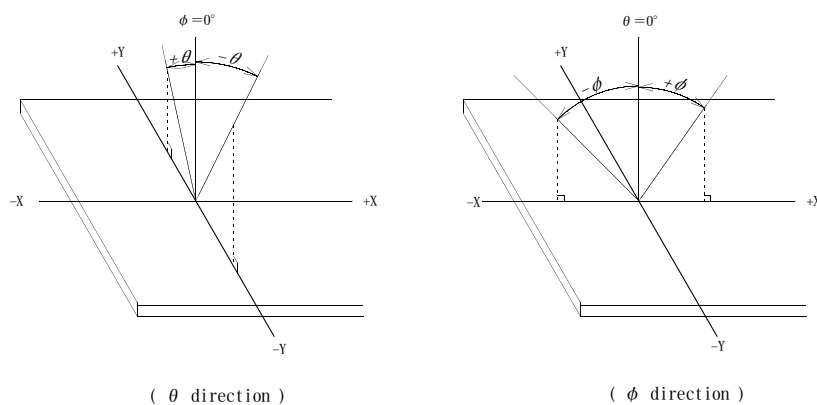
6-5. Definition of Contrast (Transmissive Mode)

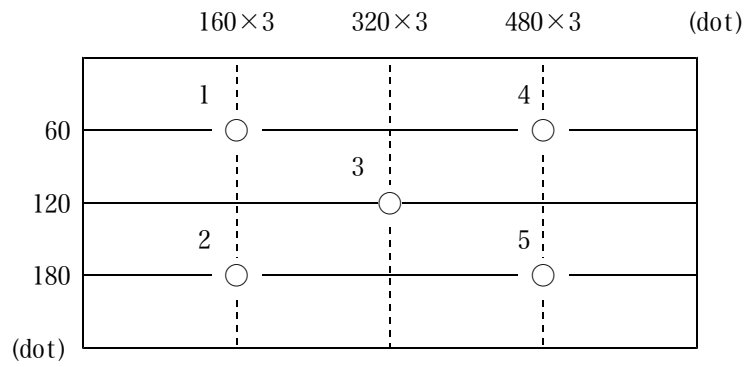$$CR \text{ (Contrast)} = \frac{\text{Brightness at all pixels "White"}}{\text{Brightness at all pixels "Black"}}$$

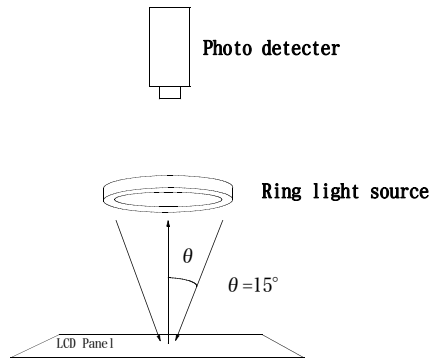## 6-6. Definition of response time

6-7. Definition of viewing angle

(θ direction) (ϕ direction)

6-8. Measuring points

1) Rating is defined as the average brightness inside the viewing area.
2) 30 minutes after LED is turned on. (Ambient Temp.=25℃)
3) Backlight : IF=15mA / 1 LED line

6-9. Measurement method of reflectance

7. Interface signals

7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	I/O	Note
1	GND	GND	—	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	Vsync	Vertical synchronous signal (negative)	I	
5	GND	GND	—	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	—	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	—	
20	B0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	B3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	—	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	—	
29	VDD	3.3V power supply	—	
30	R/L	Horizontal display mode select signal H : Normal , L : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal L : Normal , H : Up / Down reverse mode	I	*2
32	V/Q	H : Normal	I	
33	GND	GND	—	

    LCD side connector   : 08-6210-033-340-800+ (ELCO)
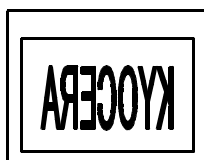   Recommended matching FFC or FPC : P = 0.5mm

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
   In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2.
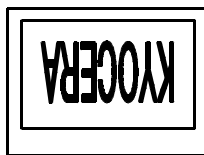   Don't keep ENAB "High" during operation.

*2

R/L = H
U/D = L

R/L = L
U/D = L

R/L = H
U/D = H

R/L = L
U/D = H

7-2. LED

PIN No.	SYMBOL	DESCRIPTION
1	VinB	
2	VinB	
3	VinB	
4	VinB	Supply voltage
5	VinB	
6	VinB	
7	VinB	
8	VinB	
9	BLEN	Backlight ON-OFF (H : ON , L : OFF)
10	VBRT	Brightness adjust voltage
11	GND	
12	GND	
13	GND	
14	GND	
15	GND	GND
16	GND	
17	GND	
18	GND	

LED Backlight side connector　　：　08-6212-018-340-800+　(ELCO)
Recommended matching FFC or FPC　：　pitch=0.5mm

8．Timing Characteristics of input signals

8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Clock	Frequency	1/Tc	—	25.18	28.33	MHz	V/Q=H
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	—	—	ns	
	Hold time	Tdh	10	—	—	ns	
Horizontal sync. signal	Cycle	TH	30.0	31.8	—	μs	V/Q=H
			770	800	900	clock	
	Pulse width	THp	2	96	200	clock	
Vertical sync. signal	Cycle	TV	515	525	560	line	V/Q=H
	Pulse width	TVp	2	—	34	line	
Horizontal display period		THd		640		clock	
Hsync.-Clock phase difference		THc	10	—	Tc-10	ns	
Hsync.-Vsync. phase difference		TVh	0	—	TH-THp	ns	
Vertical sync.signal start position		TVs		34		line	V/Q=H
Vertical display period		TVd		240		line	

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position
     The horizontal display position is determined by ENAB signal.

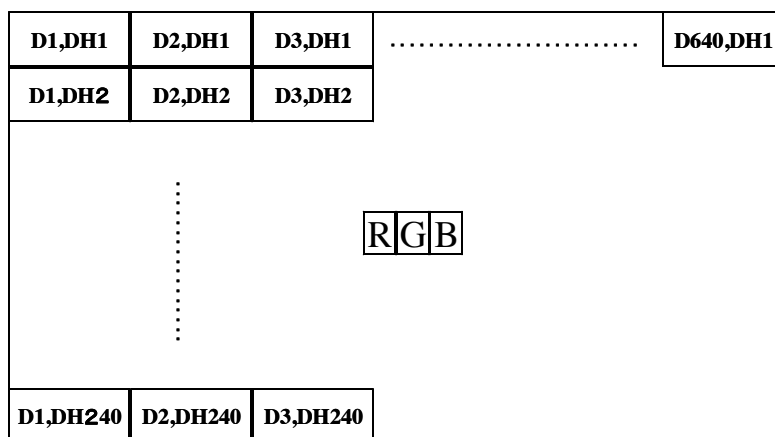| ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Enable signal	Set up time	Tes	5	—	Tc-10	ns	
	Pulse width	Tep	2	640	TH-10	clock	
Hsync.-Enable signal phase difference		The	44	—	TH-664	clock	V/Q=H

 *When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.
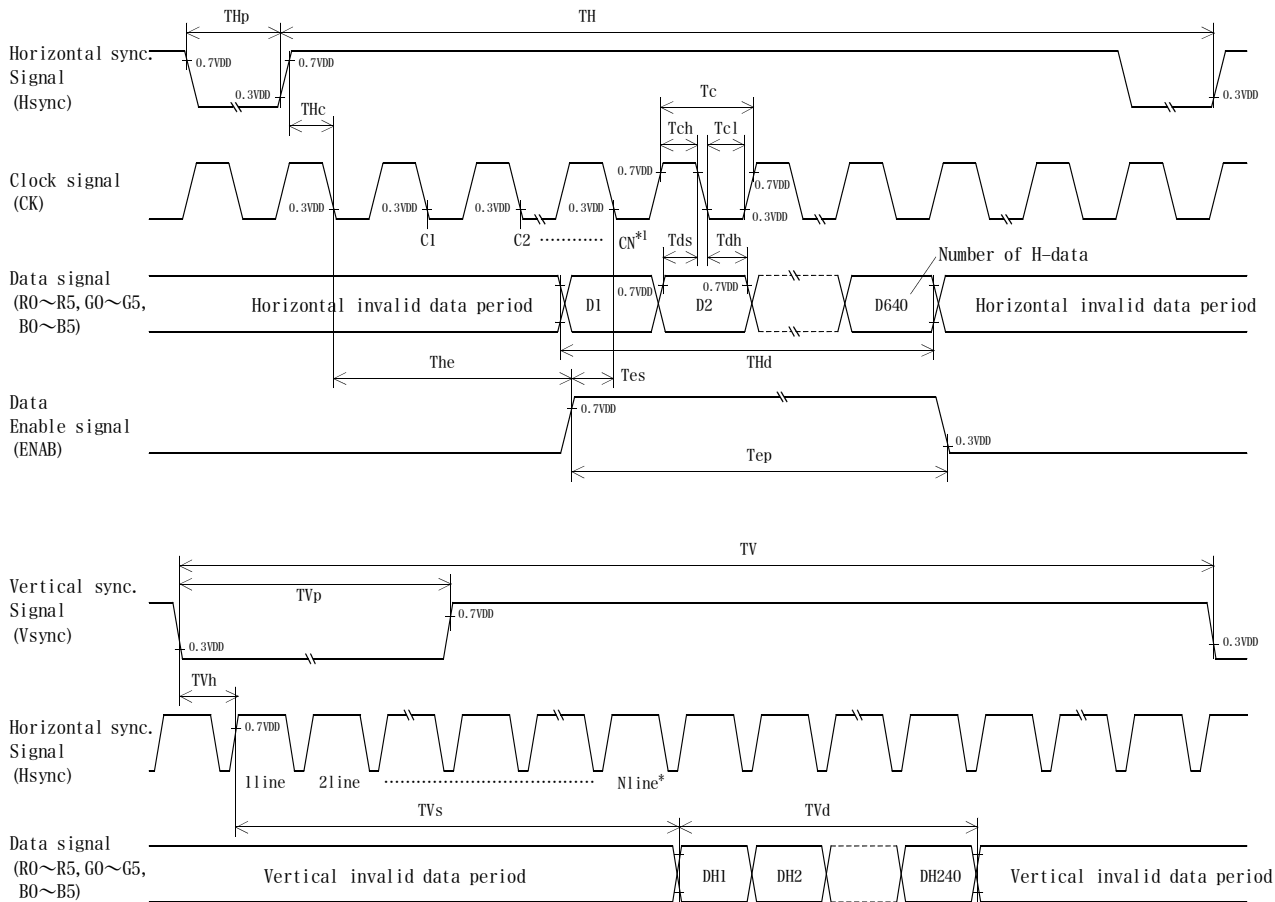

8-3. Vertical display position
     The vertical display position (TVs) is fixed at 34th line (V/Q=H).
     Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1	....	D640,DH1
D1,DH2	D2,DH2	D3,DH2		

R G B

D1,DH240	D2,DH240	D3,DH240

8-5. Input Timing Characteristics

Horizontal sync. Signal (Hsync)

THp
TH
0.7VDD
0.7VDD
0.3VDD
0.3VDD

Clock signal (CK)

THc
Tc
Tch Tcl
0.7VDD
0.3VDD   0.3VDD   0.3VDD   0.3VDD
0.7VDD
0.3VDD

Data signal (R0〜R5, G0〜G5, B0〜B5)

C1   C2 ············ CN*1   Tds Tdh   Number of H-data
Horizontal invalid data period   D1   0.7VDD   D2   0.7VDD   D640   Horizontal invalid data period
The   Tes   THd

Data Enable signal (ENAB)

0.7VDD
Tep
0.3VDD

Vertical sync. Signal (Vsync)

TV
TVp
0.7VDD
0.3VDD
0.3VDD

Horizontal sync. Signal (Hsync)

TVh
0.7VDD
1line   2line ··························· Nline*
TVs   TVd

Data signal (R0〜R5, G0〜G5, B0〜B5)

Vertical invalid data period   DH1   DH2   DH240   Vertical invalid data period

*1 When ENAB is fixed at V/Q="H", the display starts from the data of C104(Clock).

*2 The vertical display position(TVs) is fixed at 34th line(V/Q=H).

9. Backlight Characteristics
9-1. LED Backlight Characteristics

Temp. = 25℃

ITEM	Symbol	MIN	TYP.	MAX	UNIT	NOTE
Supply voltage	VinB	3.0	—	5.5	V	−20〜70℃
ON-OFF (H)	BLEN	0.8VinB	—	VinB	V	−20〜70℃
ON-OFF (L)		0.0	—	0.2VinB		−20〜70℃
LED forward current	IF	(14.0)	(15.0)	(16.0)	mA	VBRT=0〜0.8V
		—	(2.5)	—		VBRT=2.8V
Supply current	IinB	—	T.B.D.	T.B.D.	mA	VinB=3.3V, IF=15mA
		—	T.B.D.	T.B.D.		VinB=5.0V, IF=15mA
Operating Life *1,*2	T	—	T.B.D.	—	hour	IF=15mA

*1 When surface brightness decreases 50% of initial brightness .

*2 Life time is estimated data.

* A forward current below 8.0mA may reduce the brightness uniformity of the LED backlight.
  This is because the amount of light from each LED chip is different. Therefore, please
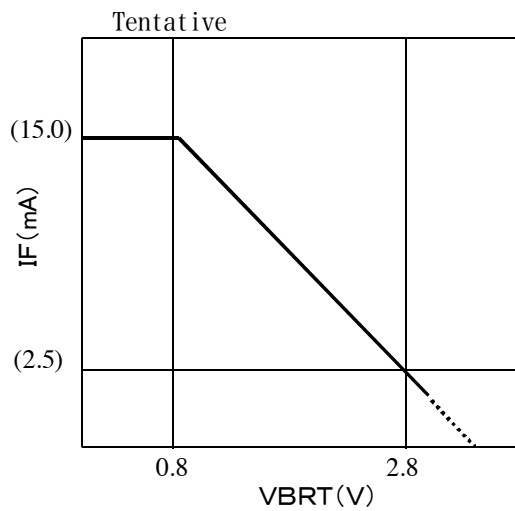 evaluate carefully before finalizing the input current.

* When you start-up, please charge in sequence of VinB->BLEN, or VBRT.
  When you shut-down, please stop in sequence of BLEN and/or VBRT->VinB.

* Please do not connect the other than our backlight to this output connector on the PCB.

* In case VDD and VinB are supplied by a single power source, VDD & VinB, and VSS & GND are
  connected directly and separately from the output on the power source. If the common wire are
  used for VDD & VinB, and for VSS & GND, and are split near the PCB, and connect to each LCD
  driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple
  between the both circuit.

Right

Power supply (Customer side) — Output terminal connected to THG062 PCB:
- Output terminal → VinB
- Output terminal → GND
- Power supply → VDD
- Power supply → VSS

Wrong

Power supply (Customer side) — Output terminal connected to THG062 PCB:
- Output terminal → VinB
- GND / VDD / VSS connections
- Power supply → VSS
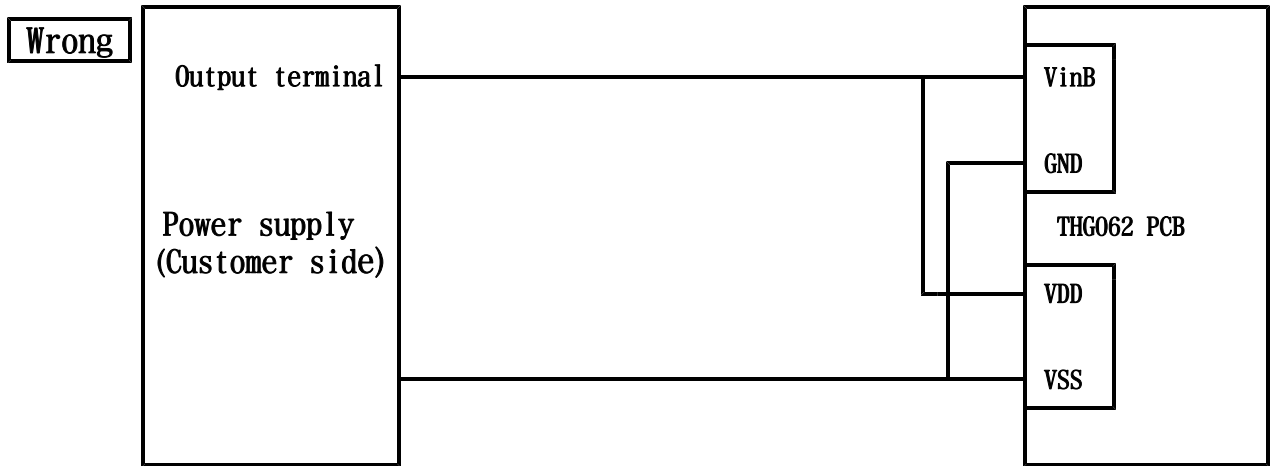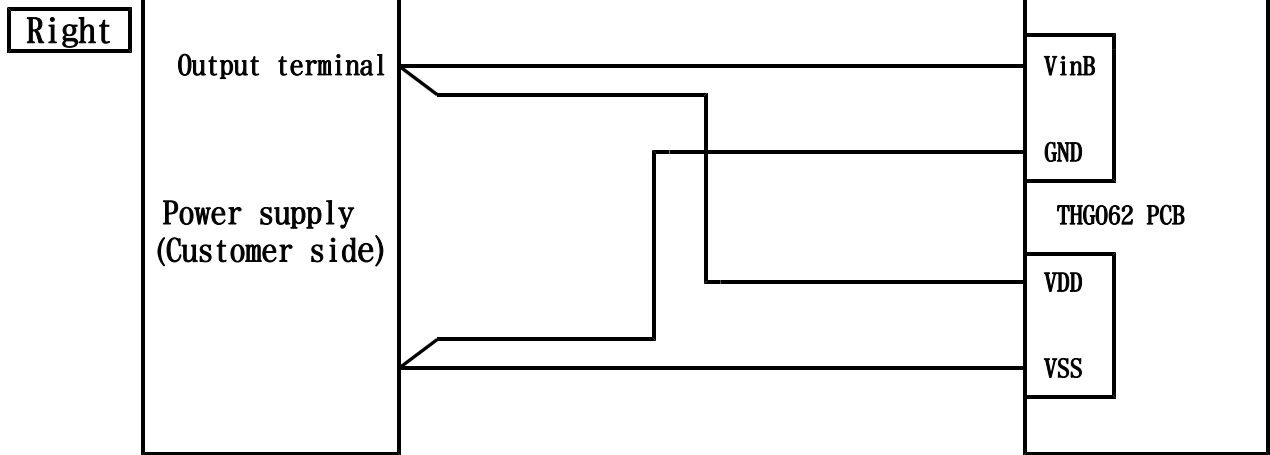
9-2. VBRT-IF characteristics

Tentative

IF(mA) vs VBRT(V)

(15.0)

(2.5)

0.8   2.8

１０．Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

ＴＨＧ０６２ＨＶＬＡＭ－Ｇ００ － □ □－□ □ － □ ＭＡＤＥ ＩＮ □□□□□
         ↓ ↓ ↓ ↓ ↓
         ① ② ③ ④ ⑤

① YEAR
② MONTH
③ DATE
④ Version Number
⑤ Country of origin (JAPAN or CHINA)

YEAR	2007	2008	2009	2010	2011	2012
CODE	7	8	9	0	1	2

MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

１１．Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

12. Precautions for use

12-1. Installation of the LCD
1. The LCD's bezel must be grounded. The heat sink and shield cover are connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.
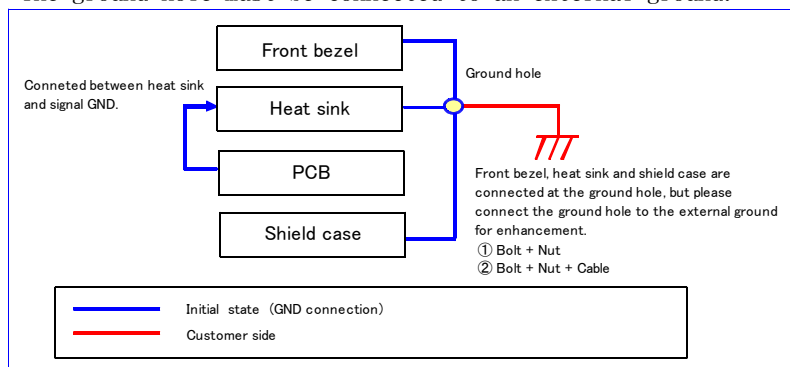
2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
3. The LCD shall be installed so that there is no pressure on the LSI chips.
4. The LCD shall be installed flat, without twisting or bending.
5. Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
   Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.
6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
7. Please refer to the following our recommendable value of Clamp-down torque when installing.
   Clamp-down torque:(0.26±0.02)N·m Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .
   Recommendable screw JIS tapping screw two types nominal dia.3.0mm
   installing boss hole depth 4.2±0.5mm
   Please be careful not to use high torque which may damage LCD module in installation.
8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
9. Do not pull the LED lead wires and do not bend the root of the wires.
   Housing should be designed to protect LED lead wires from external stress.
10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.
    Hence, when the LCD is supposed to be used in a special environment, evaluate
    the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity
1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation
1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.
   It may also change the characteristics of the liquid crystal.
   This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage
1. The LCD shall be stored within normal temperature and humidity.
   Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
2. Always store the LCD so that it is free from external pressure onto it.

12-5. Handling
1. <u>DO not</u> store in a high humidity environment for extended periods.
   Image degradation, bubbles, and/or peeling off of polarizer may result.
2. The front polarizer is easily scratched or damaged.
   Prevent touching it with any hard material, and from being pushed or rubbed.
3. The LCD screen may be cleaned with a soft cloth or cotton pad.
   Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
4. Water may cause damage or discoloration of the polarizer.
   Clean any condensation or moisture from any source immediately.
5. Always keep the LCD free from condensation during testing.
   Condensation may permanently spot or stain the polarizers.
6. Do not disassemble LCD module because it will result in damage.
7. Please do not use solid-base image pattern for long hours because a temporary afterimage may
   appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must
   be used.
8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid
   go into your eyes and mouth. In the case the fluid touches your body, rinse it off
   right away with water and soap.

1 3. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−30℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90 %RH	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Temp. Cycle	−30℃ 0.5 h R.T. 0.5 h 80℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Operation	70℃	500 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect

＊ Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

＊ The LCD is tested in circumstances in which there is no condensation.

＊ The reliability test is not an out-going inspection.

＊ The results of the reliability test are for your reference purpose only.
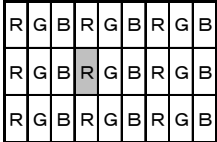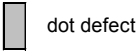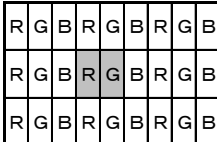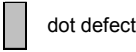   The reliability test is conducted only to examine the LCD's capability.

171
(3.2)

φ3±0.2
(Hole)
(1)
(1.3)
(9.1)

A

73.4
(39.1)

640×(R,G,B)×240Dots

R2.5

34.3

Bezel Opening Center
(ベゼル開口中心)

(36.7)

36.7

55.44 (Active Area:表示領域)
57.4 (Viewing Area:有効表示領域)
60 (Bezel Opening:ベゼル開口)

1
1.3
4.3

87.1
(87.1)

1
147.84 (Active Area:表示領域)
(1)
1.3
149.8 (Viewing Area:有効表示領域)
(1.3)
10.9
152.4 (Bezel Opening:ベゼル開口)
(10.9)
174.2

5

1.8
6.1
(6.1)
12.2

2.7
166.6
(4.9)
149.7 ±1
(24.5)
87.1
(79.5)

Hole for positioning
depth 3.9
(位置決め穴深3.9)

12 ±1

Hole for positioning
depth 3.9
(位置決め穴深3.9)

1

Insert Direction
(FPC or FFC)
(挿入方向)

CN2

No.18 No.1

3.5 ±1
12.9
4.9

2.7
26.3 ±1

Insert Direction
(FPC or FFC)
(挿入方向)

No.1

No.33

2-R1

2

ø2

61.5

CN1

C0.8

C0.8

6.4

(47.1)

9.8
(60.5)
63.6

(9.2)

76.4
92.9

C0.5
4-ø2.5 (depth:深4.2)
(4.9)
(4.9)

Note. (注記)
1 Connector CN1:08-6210-033-340-800+ (ELCO)
   (コネクター) CN2:08-6212-018-340-800+ (ELCO)
2 The Information of LCD is displayed starting at the upper left hand
   corner, moving right then down to the lower right hand corner.
   (LCDにおいて、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)
3 Tolerance without indication: ±0.5
   (指示無き公差)

DETAIL A (Dot Size)
(NTS)

R G B R G
R G B R G

Material 材 質	Treatment 処 理		Approved '07.09.20	Checked	Checked '07.09.20	Drawn	Scale 1:1 (NTS)	Title	THG062HVLAM	KYOCERA	Year-Month-Day '07.09.20	Size 2
Quantity 製作数	Description; 備 考	RoHS	松元		今村	野崎			Outline Dimensions	Drawing No.	121A5063500	

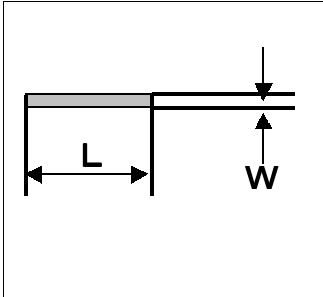No | Description | Drawn | Checked | Checked | Approved

SPEC.NO.	TQ3C-8EAF0-E2DKB15-00
DATE	September 7, 2007

FOR:

KYOCERA INSPECTION STANDARD

TYPE : THG062HVLAM-G00

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 7, 2007	T. Onodera	Y. Yamazaki	Y. Matsumoto	J. Sakaguchi	

Revision Record

Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

Designed by: Engineering Dept.   Confirmed by: QA Dept.

1) Note

	Note
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. Inspection Conditions 　Luminance　　　　　　 : 500 Lux minimum 　Inspection distance　 : 300 mm (from the sample) 　Temperature　　　　　 : 25 ± 5 ℃ 　Direction　　　　　　 : directly above

Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter.
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.
	Definition of size	Definition of circle size	Definition of linear size
		$$d=\frac{(a+b)}{2}$$	

2) Standard

Classification		Inspection item		Judgement standard
defect (in LCD glass)	Dot defect	Bright dot defect		Acceptable number　　 : 4 bright dots defects Bright dot spacing　 : 5 mm or more
		Black dot defect		Acceptable number　　 : 5 black dots defects Black dot spacing　 : 5 mm or more
		2 dots join	Bright dot defect	Acceptable number　　 : 2
			Black dot defect	Acceptable number　　 : 3
		3 or more dots join		Acceptable number　　 : 0
		Total dot defects		Acceptable number　　 : 5 Max
	Others	White dot, Dark dot (Circle)		<table><tr><td>Size(mm)</td><td>Acceptable Number</td></tr><tr><td>d＜0.2</td><td>(neglected)</td></tr><tr><td>0.2＜d≦0.4</td><td>5</td></tr><tr><td>0.4＜d≦0.5</td><td>3</td></tr><tr><td>0.5＜d</td><td>0</td></tr></table>
External inspection (Defect on Polarizer or between Polarizer and LCD glass)		Polarizer(Scratches)		<table><tr><td>Width(mm)</td><td>Length(mm)</td><td>Acceptable Number</td></tr><tr><td>W≦0.1</td><td>–</td><td>(neglected)</td></tr><tr><td rowspan=2>0.1＜W≦0.3</td><td>L≦5.0</td><td>(neglected)</td></tr><tr><td>5.0＜L</td><td>0</td></tr><tr><td>0.3＜W</td><td>–</td><td>0</td></tr></table>
		Polarizer Touch panel (Bubble, Dent)		<table><tr><td>Size(mm)</td><td>Acceptable Number</td></tr><tr><td>d＜0.2</td><td>(neglected)</td></tr><tr><td>0.2＜d≦0.3</td><td>5</td></tr><tr><td>0.3＜d≦0.5</td><td>3</td></tr><tr><td>0.5＜d</td><td>0</td></tr></table>
		Foreign Particle(Circular shape)		<table><tr><td>Size(mm)</td><td>Acceptable Number</td></tr><tr><td>d＜0.2</td><td>(neglected)</td></tr><tr><td>0.2＜d≦0.4</td><td>5</td></tr><tr><td>0.4＜d≦0.5</td><td>3</td></tr><tr><td>0.5＜d</td><td>0</td></tr></table>
		Foreign Particle (Linear shape), Scratches		<table><tr><td>Width(mm)</td><td>Length(mm)</td><td>Acceptable Number</td></tr><tr><td>W≦0.03</td><td>–</td><td>(neglected)</td></tr><tr><td rowspan=3>0.03＜W≦0.1</td><td>L≦2.0</td><td>(neglected)</td></tr><tr><td>2.0＜L≦4.0</td><td>3</td></tr><tr><td>4.0＜L</td><td>0</td></tr><tr><td>0.1＜W</td><td>–</td><td>(According to Circular shape)</td></tr></table>