Product Information

Customer :SAMT

SAMSUNG TFT-LCD

MODEL : **LTA520HB09**

NOTE :

Customer's *Approval*	
SIGNATURE	DATE

Kyunghwan Ko	DATE 30.Oct.2008
Jinsu Jung	DATE 30.Oct.2008

LCD Business

Samsung Electronics Co . , LTD.

정진수05156387응용기술2파트(HD)129252203 20080702125551

017831214970900

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* Revision History

Date	Rev. No	Page	Summary
July 2, 2008	000	all	First issued

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General Description

Description

LTA520HB09 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 52.0“ is 1920 x 1080 and this model can display up to (8bit+FRC) 1.06Giga colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast & aperture ratio
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Direct Type 24 CCFLs (Cold Cathode Fluorescent Lamp)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	1226.0(H_{TYP}) x 719.2(V_{TYP})	mm	±1.0mm
	60.0(D_{MAX})		
Weight	19,000(Max.)	g	
Pixel Pitch	0.6(H) x 0.6 (V)	mm	
Active Display Area	1152.0(H) x 648.0(V)	mm	
Surface Treatment	Haze 14% , Hard Coating(3H)	-	3G N-TAC
Display Colors	(8 bit+ FRC) – 1.06G	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	500 (Typ.)	cd/m²	

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	V_{DD} -1.2	*13.2*	V	(1)
Storage temperature		T_{STG}	-20	60	℃	(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	50	℃	(2),(5)
	T. Uniformity	△T	-	10	℃	
Shock (non - operating)		S_{nop}	-	30	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.
- a. 90 % RH Max. (Ta ≤ 39 °C)
- b. Relative Humidity is 90% or less. (Ta > 39 °C)
- c. No condensation

(3) 11ms, sine wave, one time for ±X, ±Y, ±Z axis

(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

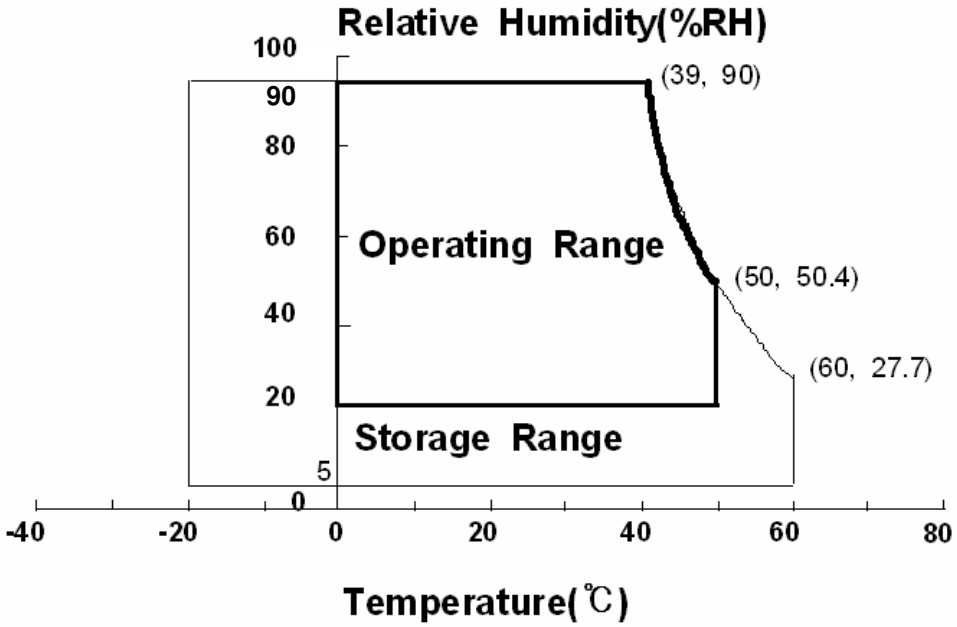

Fig. Temperature and Relative humidity range

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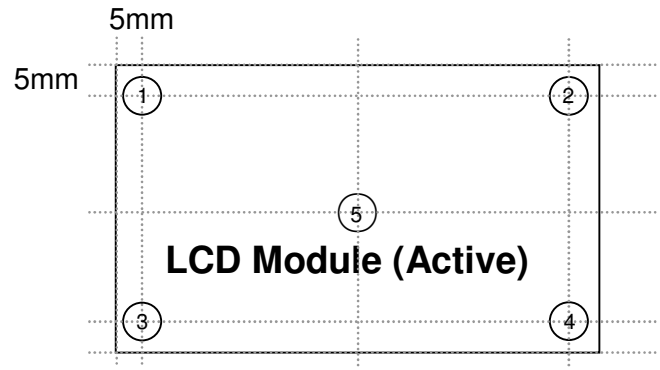
(5) Definition of test point



△T should be less than *10 ℃* (△T = | T_{OPR} – T_{MAX}|)

T_{OPR} : Temperature of the center of the glass surface (Test point 5)
T1~ T4 : Temperature of each edge of the glass surface
T_{MAX} : The highest temperature of the glass surface

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2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 ± 2°C, VDD=12.0V, fv= 60Hz, f_{DCLK}=148.5 MHz, IL = 6.5 mArms (Hot))

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal θL,R=0 θU,D=0 Viewing Angle	2300	3000	-		(1) SR-3
Response Time	Rising	Tr		-	14	24	msec	(3) RD-80S
	Falling	Tf		-	6	8		
	G to G	Tg		-	8	10		
Luminance of White (Center of screen)		Y_L		400	500	-	cd/m²	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	0.638	TYP. +0.03		(5),(6) SR-3
		Ry			0.336			
	Green	Gx			0.284			
		Gy			0.607			
	Blue	Bx			0.145			
		By			0.060			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	72	-	%	(5) SR-3
Color Temperature		-		-	10000	-	K	
Viewing Angle	Hor.	θ_L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		θ_R		75	89	-		
	Ver.	θ_U		75	89	-		
		θ_D		75	89	-		
Brightness Uniformity (9 Points)		B_{uni}		-	-	25	%	(2) SR-3

- Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Lamp current : 6.5mA(HOT)
Environment condition : Ta = 25 ± 2 °C

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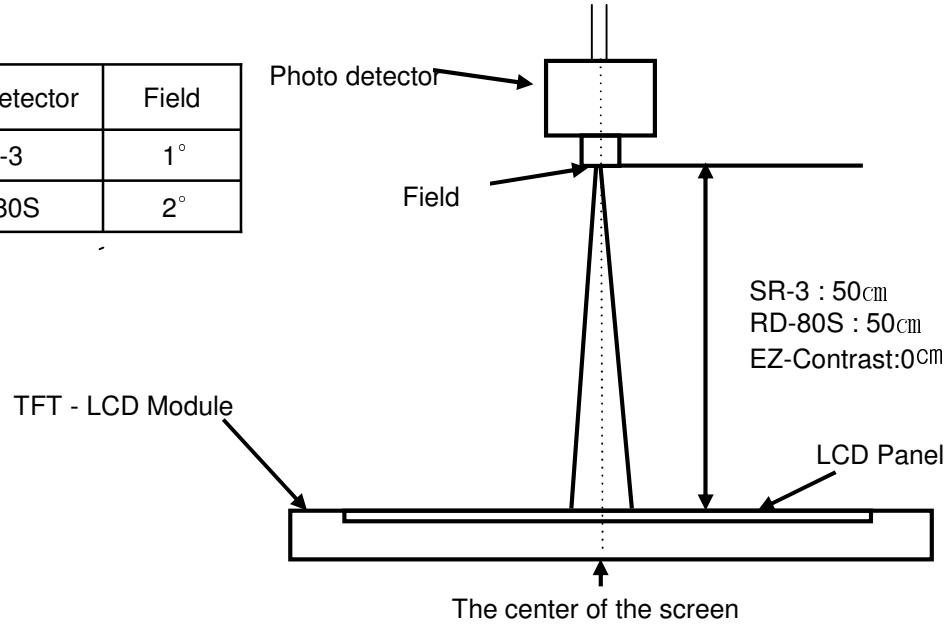
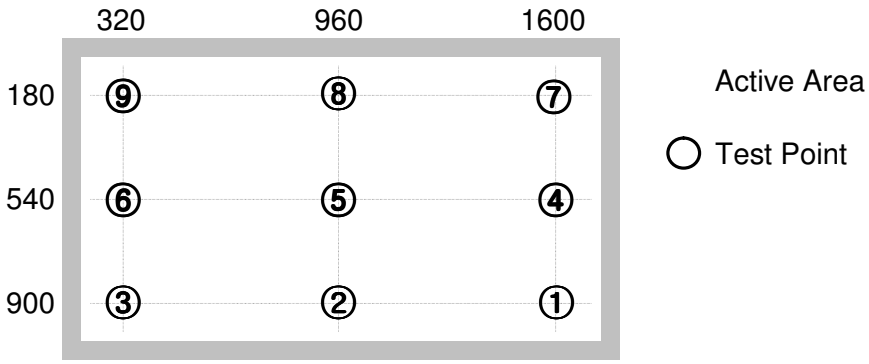
Photo detector	Field
SR-3	1°
RD-80S	2°



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G\max}{G\min}$$

Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

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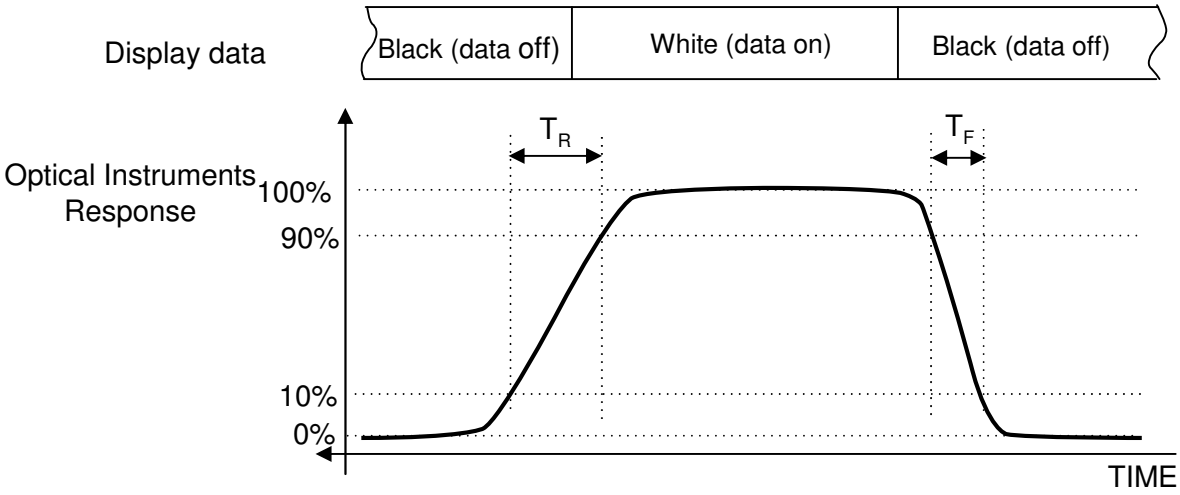
Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

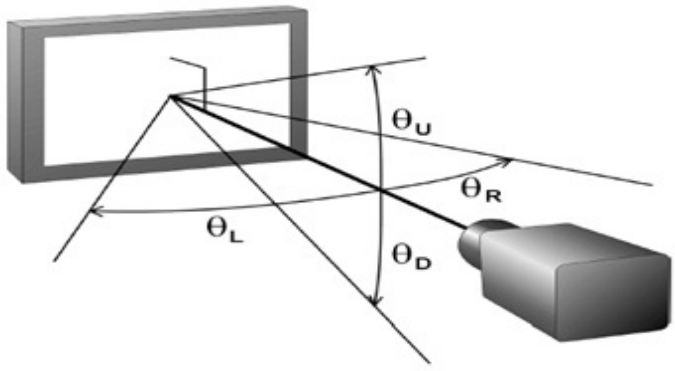


※ G-to-G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ⩾10)



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3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25°C ± 2 °C

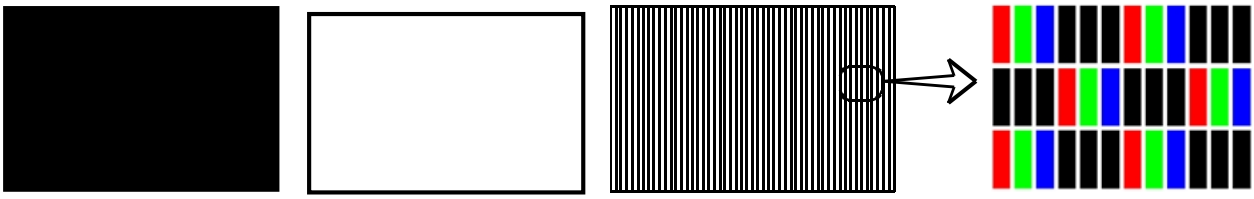
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	730	800	mA	(2),(3)
	(b) White		-	1200	1430	mA	
	(c) Checker		-	1380	1520	mA	
Vsync Frequency		f_V	48	60	62	Hz	
Hsync Frequency		f_H	50	67.5	75	kHz	
Main Frequency		Fdclk	130	148.5	155	MHz	
Rush Current		I_{RUSH}	-	-	5	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.

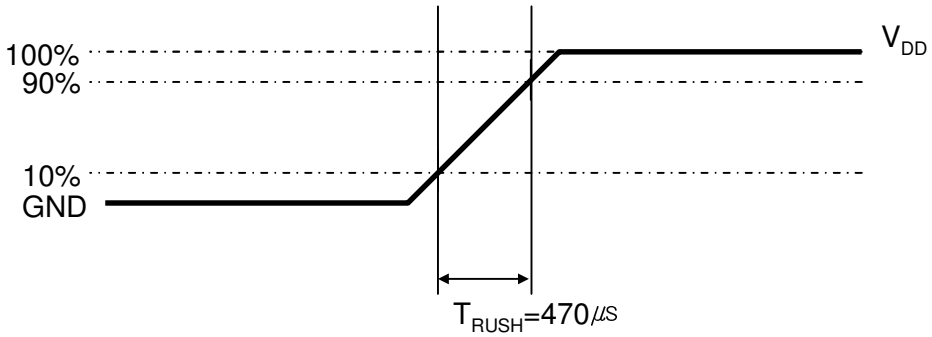
(2) fv=60Hz, f_{DCLK} = 148.5MHz, V_{DD} = 12.0V, DC Current.

(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern b) White c) Checker



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH}. is 470㎲.

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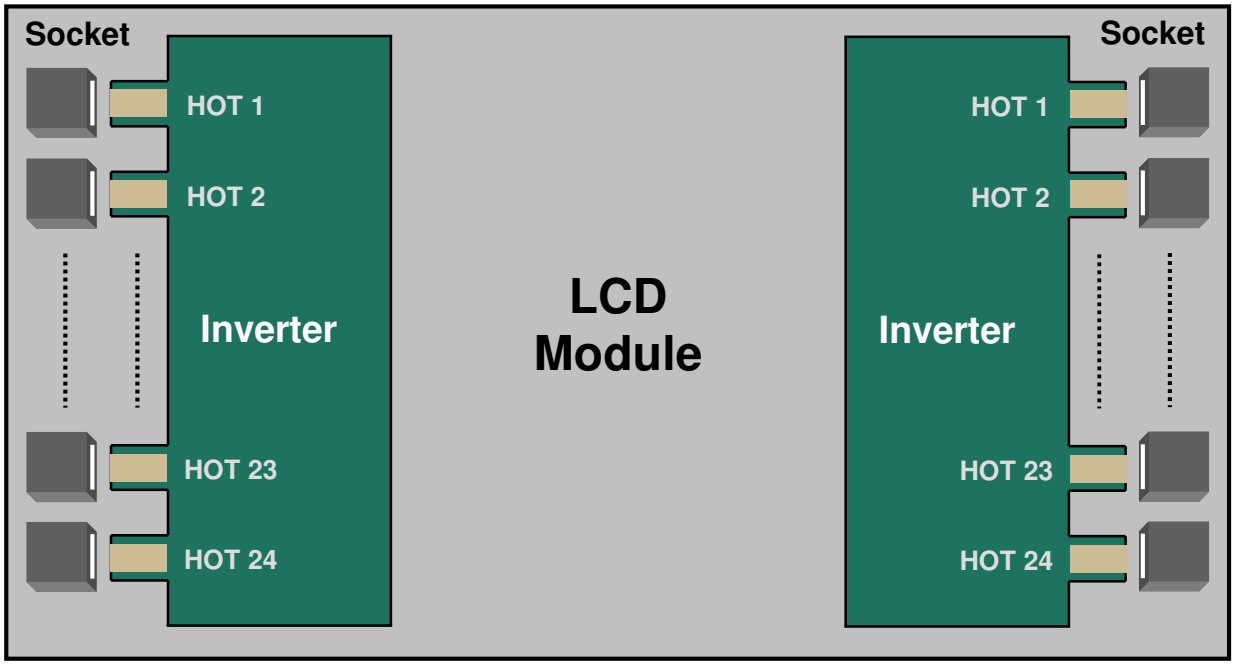
3.2 Back Light Unit

The back light unit contains 24 direct-lighting type CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of lamps are shown in the following tables.

Ta=25 ± 2°C



Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.
[Operating condition : Ta = 25±2℃, I_L = 6.5 mArms, For single lamp only.]

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3.3 Inverter Input Condition & Specification

Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	Vin	-	22	24	26	V	Ta=25±2 °C
Input Current	I_{RUSH}	Vin=24.0V Vdim=3.3V	-	-	13.4	A	Initial Turn-on
Lamp Current	I_O	Vin = 24V Vdim = 3.3V	5.8	6.5	7.2	mArms	After 1 hour Warm-up (1)
Frequency	F_{LAMP}	Vin = 24V	40	42	44	kHz	-
Backlight On/Off	ON	Vin = 24V	2.4	-	5.5	V	-
	OFF	Vin = 24V	0	-	0.8		
Dimming Control	V_{DIM}	Max Lum	3.3	-	-	V	-
		Min. Lum	-	-	0		

Note (1) Power Consumption is measured at 500[cd/m2] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.

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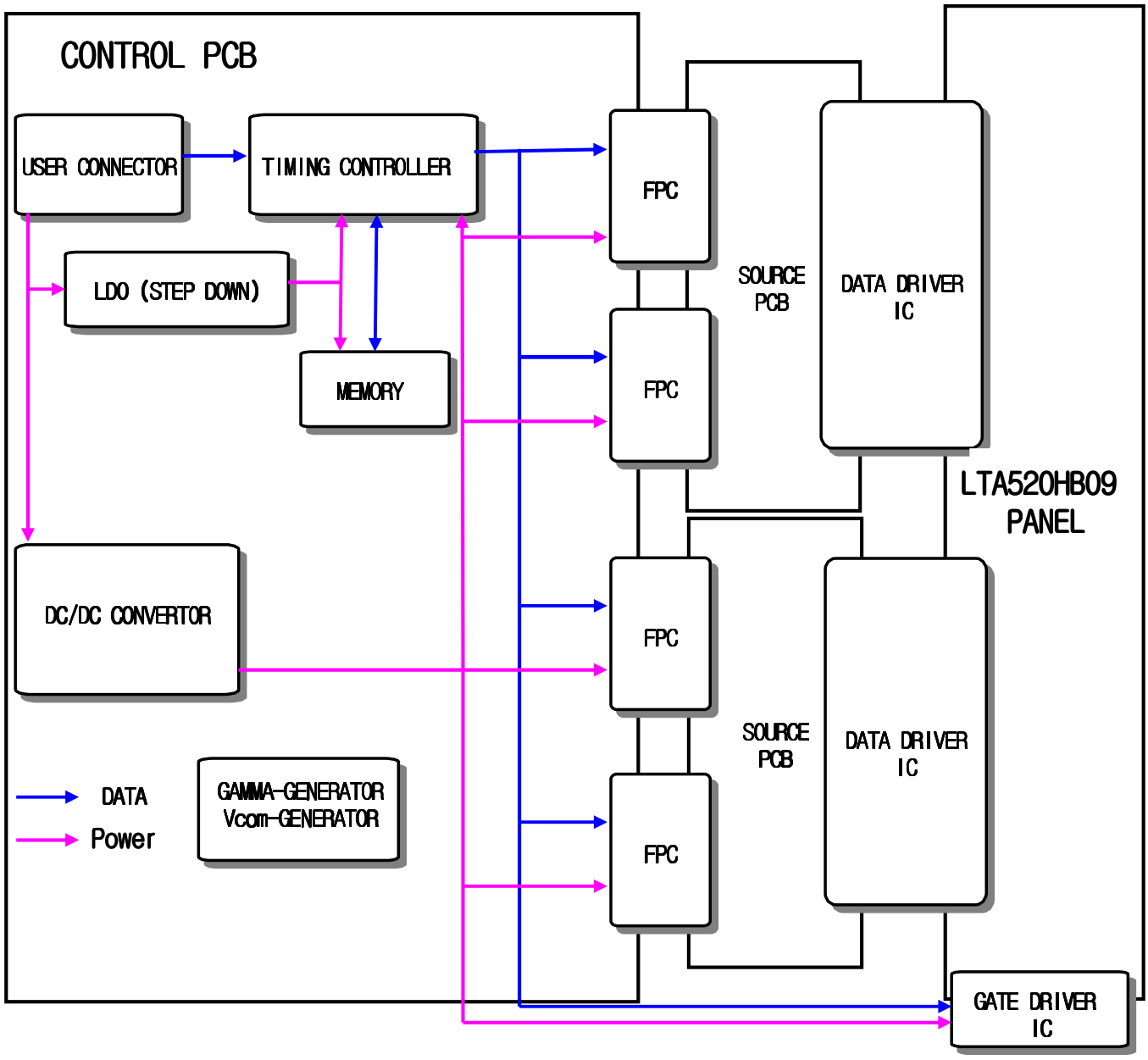
4. Block Diagram



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5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector : FI-RE51S-HF (JAE)

Pin	Symbol	Description		Pin	Symbol	Description	
1	Power	DC 12V		26	RE[0]P	Even LVDS Signal +	
2	Power	DC 12V		27	RE[1]N	Even LVDS Signal –	
3	Power	DC 12V		28	RE[1]P	Even LVDS Signal +	
4	Power	DC 12V		29	RE[2]N	Even LVDS Signal –	
5	Power	DC 12V		30	RE[2]P	Even LVDS Signal +	
6	NC			31	GND	GND	
7	GND	GND		32	ROCLK–	Even LVDS Signal –	
8	GND	GND		33	ROCLK+	Even LVDS Signal +	
9	GND	GND		34	GND	GND	
10	RO[0]N	Odd LVDS Signal –		35	RE[3]N	Even LVDS Signal –	
11	RO[0]P	Odd LVDS Signal +		36	RE[3]P	Even LVDS Signal +	
12	RO[1]N	Odd LVDS Signal –		37	RE[4]N	Even LVDS Signal –	NOTE 2
					Pull-UP or Pull Down		
13	RO[1]P	Odd LVDS Signal +		38	RE[4]P	Even LVDS Signal +	
					Pull-UP or Pull Down		
14	RO[2]N	Odd LVDS Signal –		39	GND		
15	RO[2]P	Odd LVDS Signal +		40	NC	NOTE 1	
16	GND	GND		41	NC		
17	ROCLK–	Odd LVDS Signal –		42	NC		
18	ROCLK+	Odd LVDS Signal +		43	NC		
19	GND	GND		44	NC		
20	RO[3]N	Odd LVDS Signal –		45	NC		
21	RO[3]P	Odd LVDS Signal +		46	NC		
22	RO[4]N	Odd LVDS Signal –	NOTE 2	47	NC		
	Pull-UP or Pull Down						
23	RO[4]P	Odd LVDS Signal +		48	NC		
	Pull-UP or Pull Down						
24	GND	GND		49	NC		
25	RE[0]N	Even LVDS Signal –		50	NC		
				51	NC	NOTE 1	

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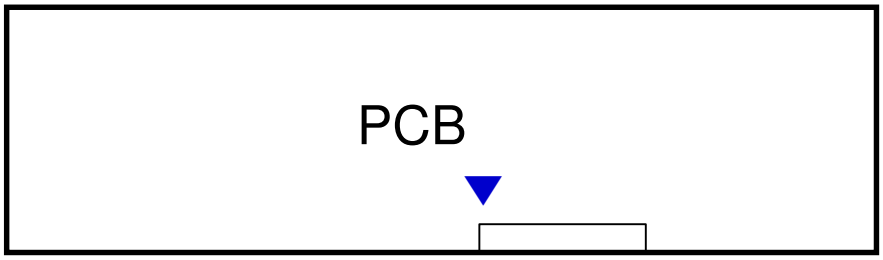
Note1) No Connection: This PINS are only used for SAMSUNG internal using.
Note2) For 8bit+FRC : Input 10bit LVDS data to Panel.
For 8bit : Pull down or Pull up should be applied on Pin22,23,37,38.
- Pin22&Pin37 Pull down/Pull up selection should be same.
- Pin23&Pin38 Pull down/Pull up selection should be same

SEQUENCE : On = V_{DD}(T1) ≥ LVDS Option ≥ Interface Signal(T2)
OFF = Interface Signal(T3) ≥ LVDS Option ≥ V_{DD}

Note(1) Pin number starts from Right side



Pin No. 1 Pin No. 51

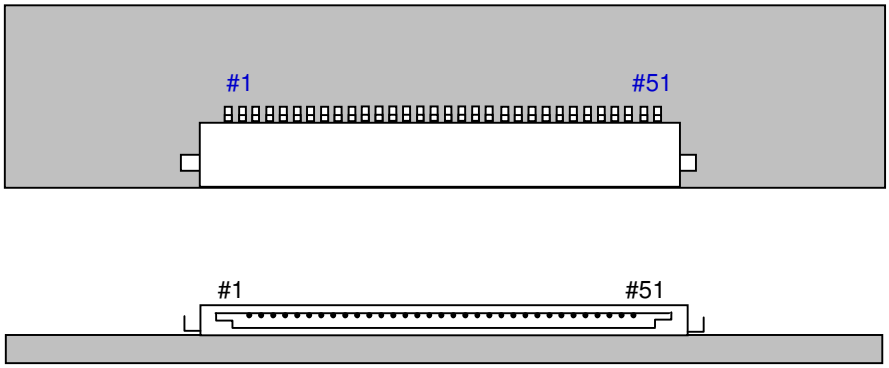


Fig. Connector diagram

a. Power GND pins should be connected to the LCD's metal chassis.
b. All power input pins should be connected together.
c. All NC pin should be separated from other signal or power.

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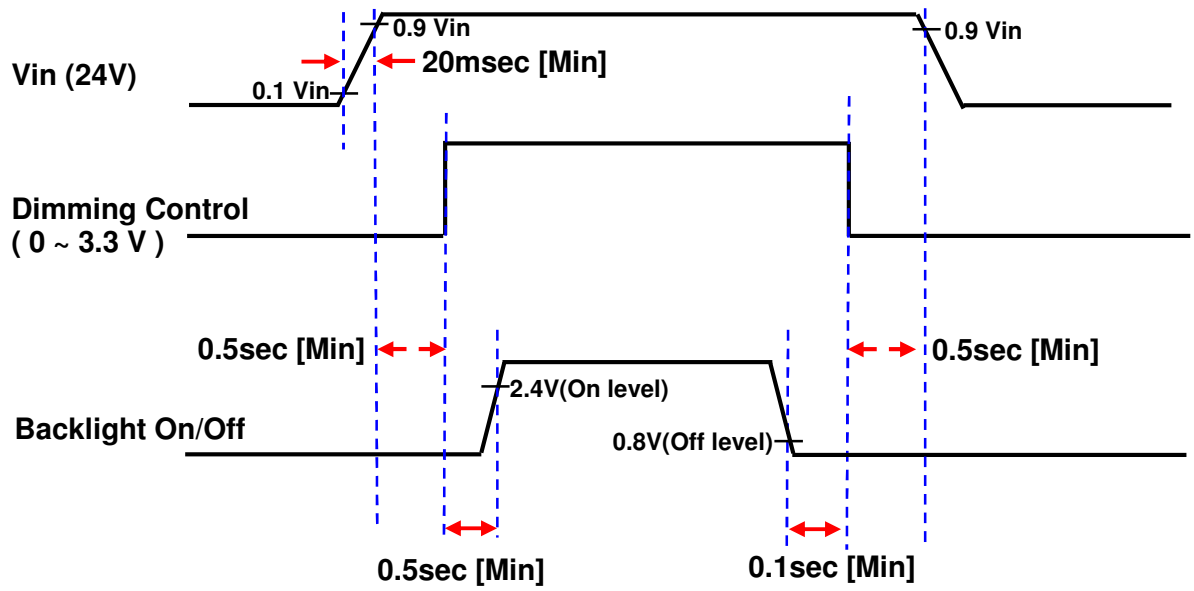
5.2. Inverter Input Pin Configuration

(1)Connector (Master& Slave): S14B-PHA-SM-TB(LF) (JST)

Pin No.	Pin Configuration (FUNCTION)	
	Master	Slave
1	Vin (24 V)	Vin (24 V)
2	Vin (24 V)	Vin (24 V)
3	Vin (24 V)	Vin (24 V)
4	Vin (24 V)	Vin (24 V)
5	Vin (24 V)	Vin (24 V)
6	GND	GND
7	GND	GND
8	GND	GND
9	GND	GND
10	GND	GND
11	No Connection	No Connection
12	Backlight On /Off [ON:2.4 - 5.25 V, OFF: 0 - 0.8 V]	No Connection
13	Dimming Control [0V: Min, 3.3V: Max]	No Connection
14	No Connection	No Connection

5.3. Inverter Input Power Sequence



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5.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA only)

	LVDS pin	JEIDA -DATA	
		8bit+ FRC	8bit
TxOUT/RxIN0	TxIN/RxOUT0	R4	R2
	TxIN/RxOUT1	R5	R3
	TxIN/RxOUT2	R6	R4
	TxIN/RxOUT3	R7	R5
	TxIN/RxOUT4	R8	R6
	TxIN/RxOUT6	R9	R7
	TxIN/RxOUT7	G4	G2
TxOUT/RxIN1	TxIN/RxOUT8	G5	G3
	TxIN/RxOUT9	G6	G4
	TxIN/RxOUT12	G7	G5
	TxIN/RxOUT13	G8	G6
	TxIN/RxOUT14	G9	G7
	TxIN/RxOUT15	B4	B2
	TxIN/RxOUT18	B5	B3
TxOUT/RxIN2	TxIN/RxOUT19	B6	B4
	TxIN/RxOUT20	B7	B5
	TxIN/RxOUT21	B8	B6
	TxIN/RxOUT22	B9	B7
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R0
	TxIN/RxOUT5	R3	R1
	TxIN/RxOUT10	G2	G0
	TxIN/RxOUT11	G3	G1
	TxIN/RxOUT16	B2	B0
	TxIN/RxOUT17	B3	B1
	TxIN/RxOUT23	RESERVED	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0	Pull-UP or Pull Down
	TxIN/RxOUT29	R1	
	TxIN/RxOUT30	G0	
	TxIN/RxOUT31	G1	
	TxIN/RxOUT32	B0	
	TxIN/RxOUT33	B1	
	TxIN/RxOUT34	RESERVED	

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5.5-1 Input Signals, Basic Display Colors and Gray Scale of Each Color (For 8bit+FRC)

COLOR	DISPLAY (8bit)	DATA SIGNAL																														GRAY SCALE LEVEL
		RED										GREEN										BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

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5.5-2 Input Signals, Basic Display Colors and Gray Scale of Each Color (For 8bit)

COLOR	DISPLAY (8bit)	DATA SIGNAL																								GRAY SCALE LEVEL
		RED								GREEN								BLUE								
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			R3~ R252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~ G252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~ B252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

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6. Interface Timing

6.1 Timing Parameters (DE only mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	130	148.5	155	MHz	-
Hsync		F_H	50	67.5	75	KHz	-
Vsync		F_V	48	60	62	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	Lines	-
	Vertical Total	T_V	1100	1125	1480	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	Clocks	-
	Horizontal Total	T_H	2050	2200	2350	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal V_{DD} = 3.3V

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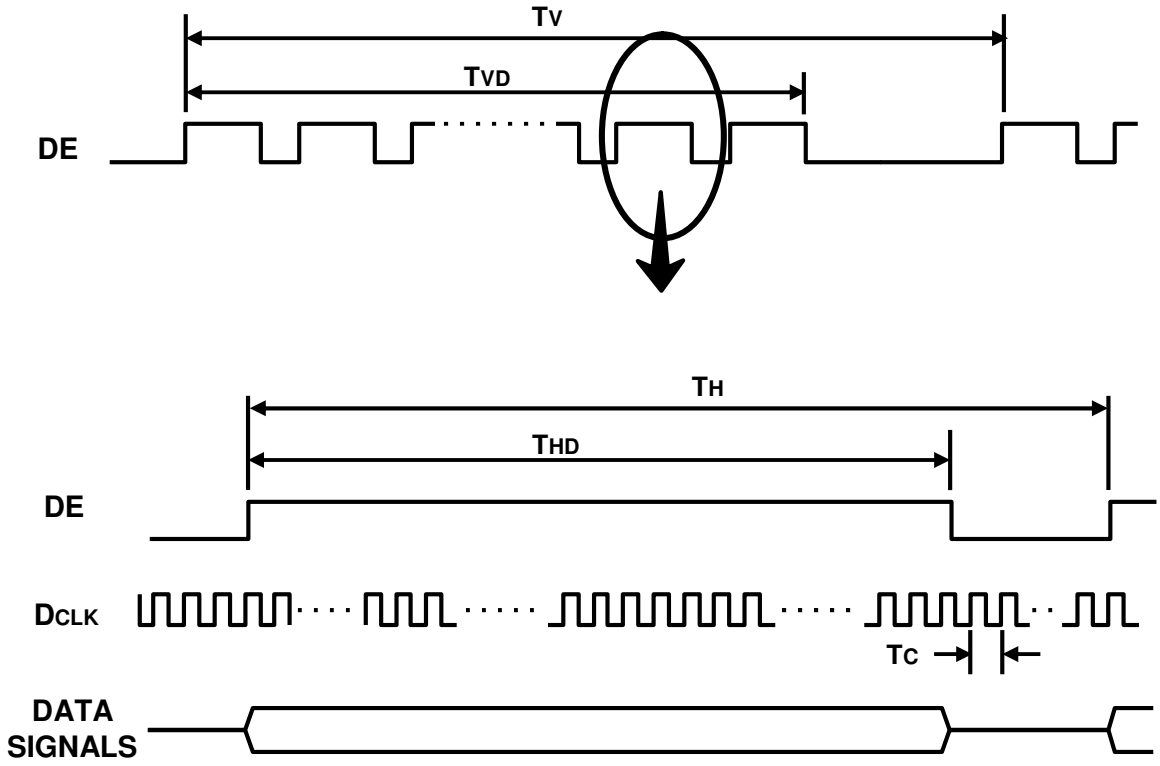
6.2 Timing diagrams of interface signal (DE only mode)



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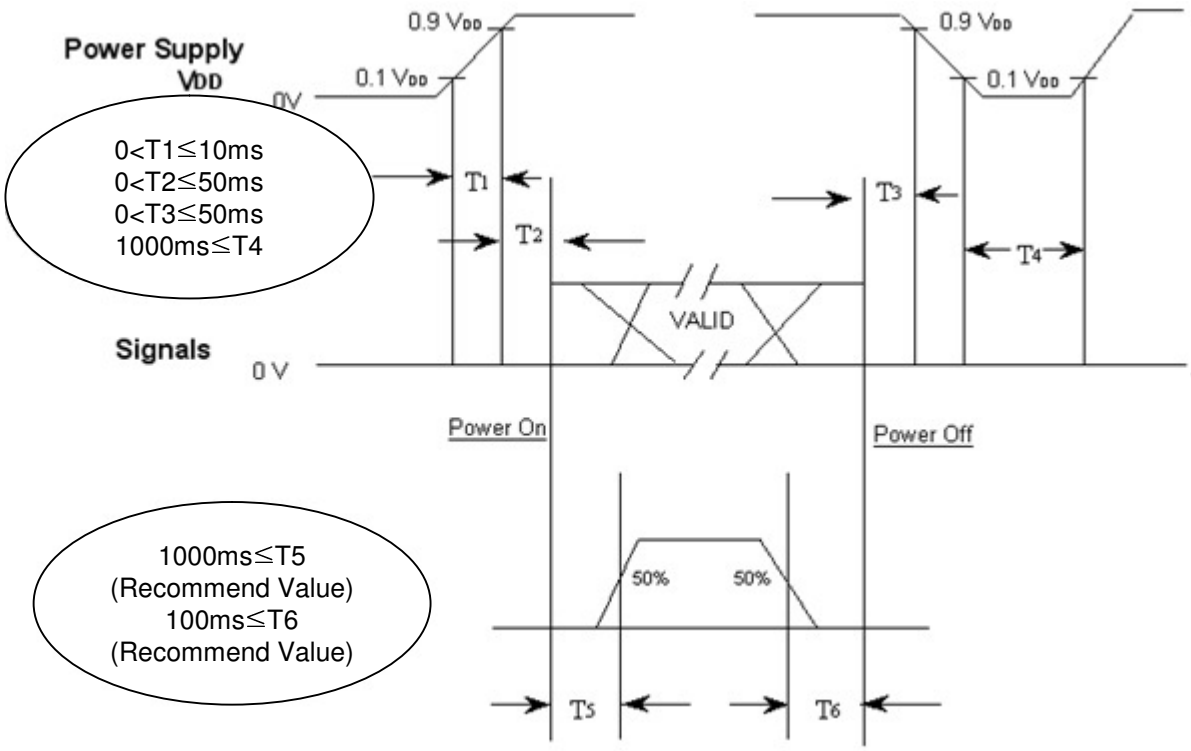
6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%
T2 : The time from V_{DD} to valid data at power ON.
T3 : The time from valid data off to V_{DD} off at power Off.
T4 : V_{DD} off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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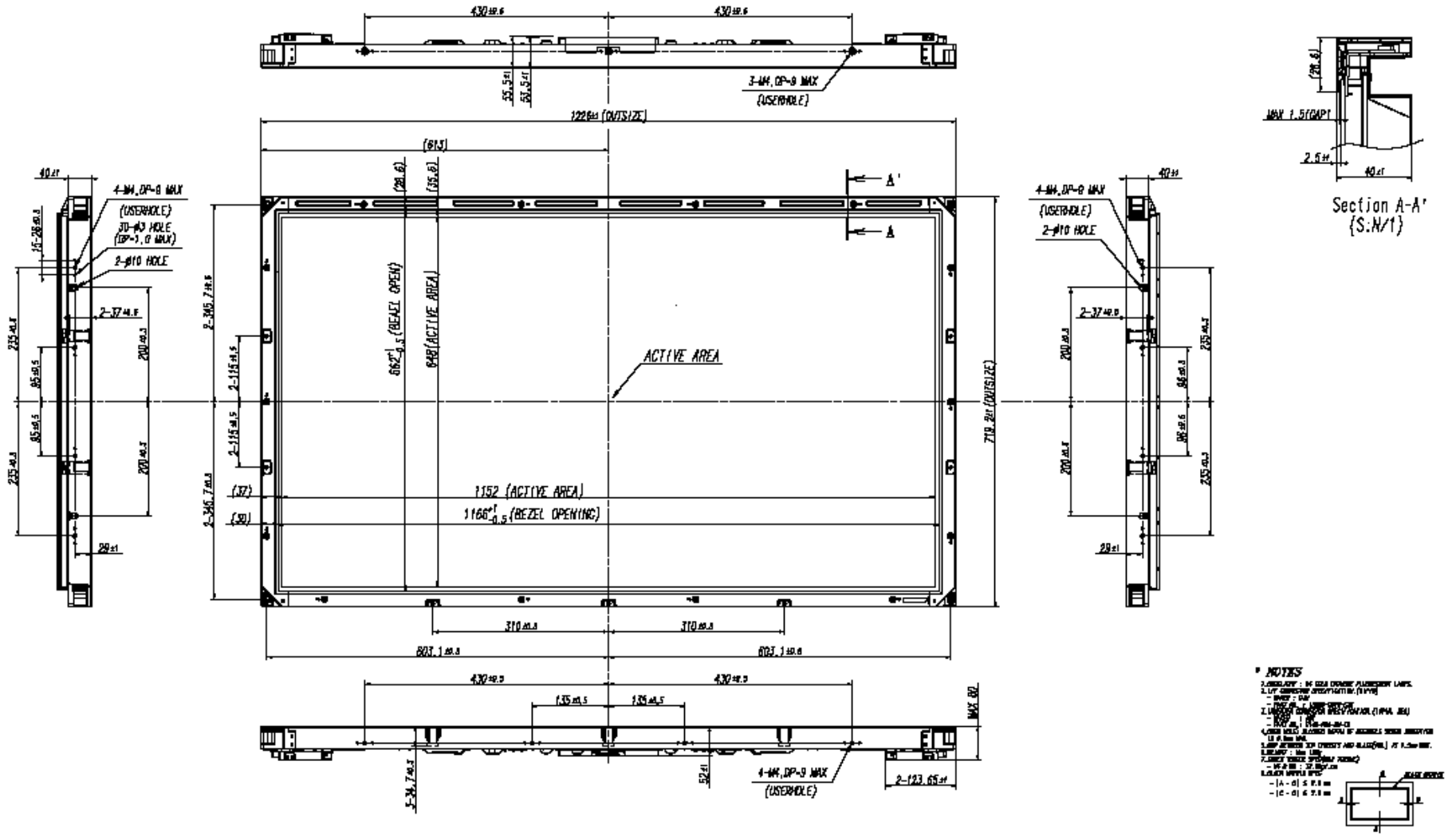
7. Outline Dimension (Front View)



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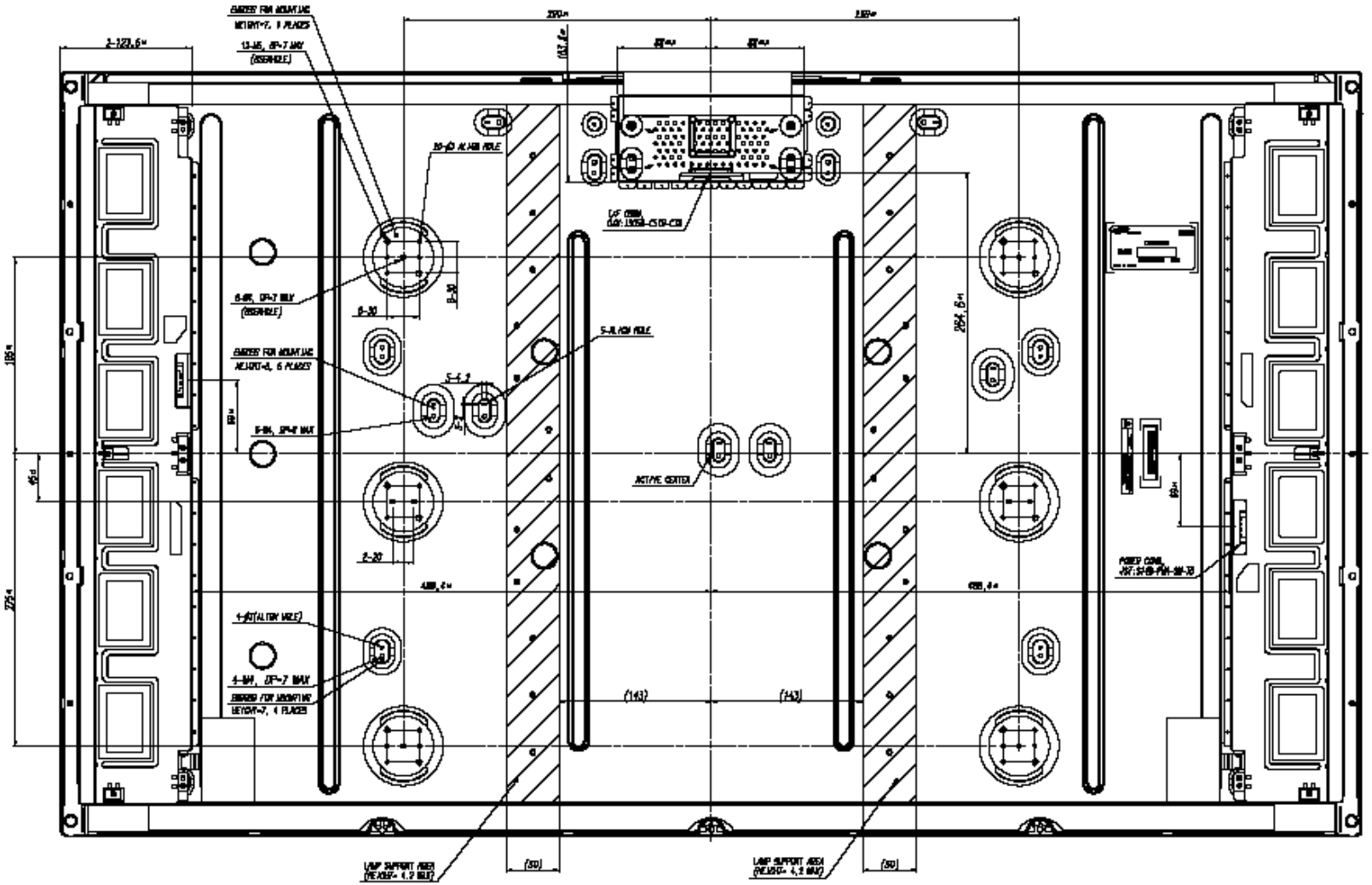
7. Outline Dimension (Rear View)



Model	LTA520HB09	Doc. No	06-000-G-080702	Page	24 / 29

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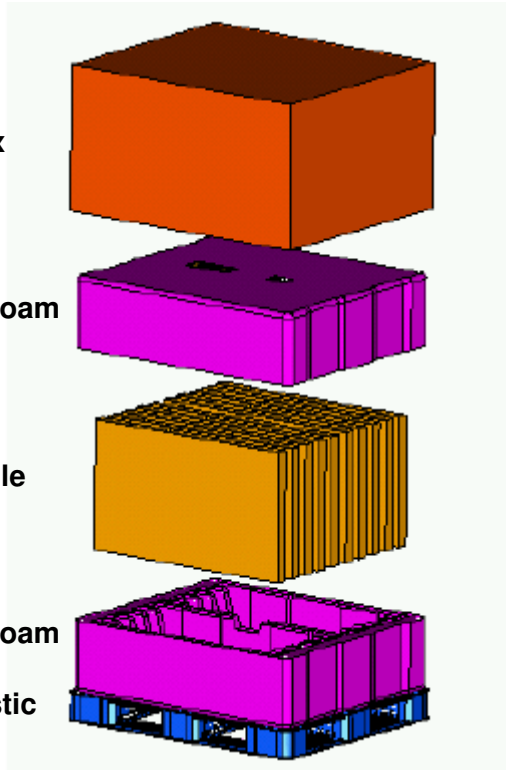
8. PACKING

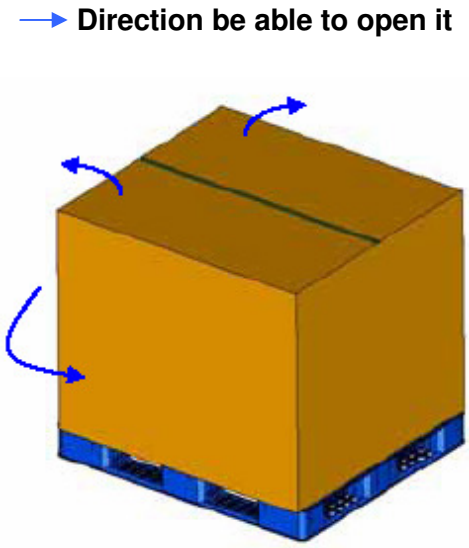
8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method





8.2 Packing Specification

Item	Specification	Remark
LCD Packing	9ea / (Packing-Pallet Box)	1. 171 Kg / LCD (9ea) 2. 15.6 Kg / Cushion-pallet (2ea) 3. 10.5 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 10kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1475mm(H) x 1150mm(V) x995mm(height)
Total Pallet Weight	207.1kg	Pallet(10kg) + Module(21*9=171kg) + Cushion(up+botton=15.6kg) + Pallet-BOX(10.5kg)

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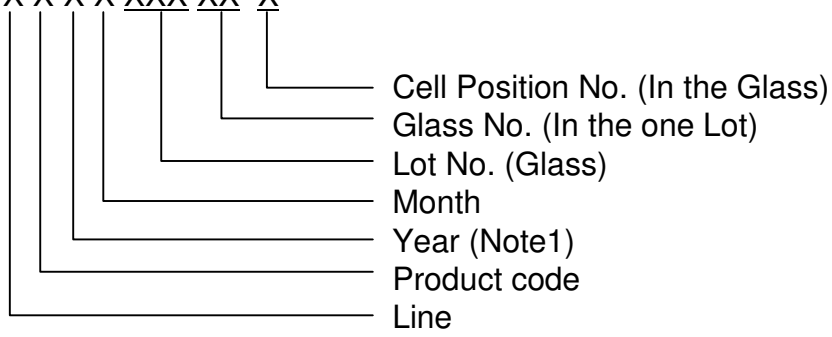
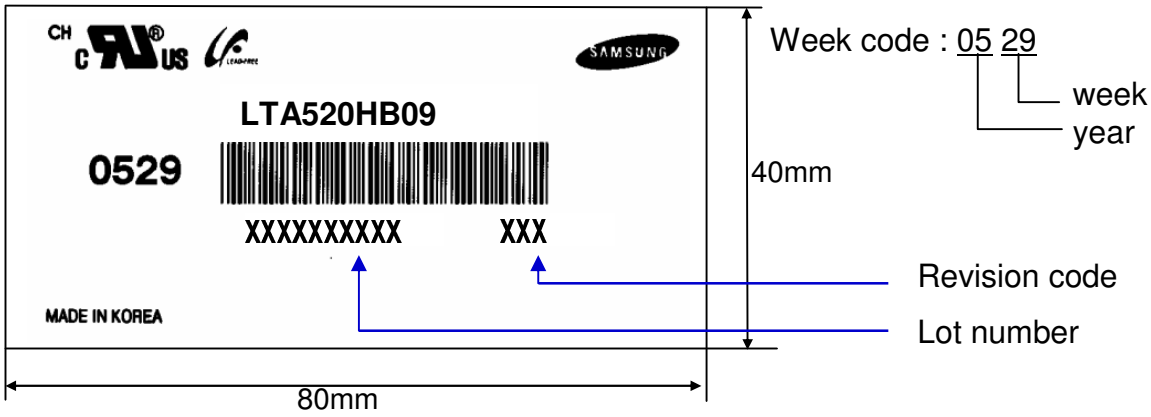
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

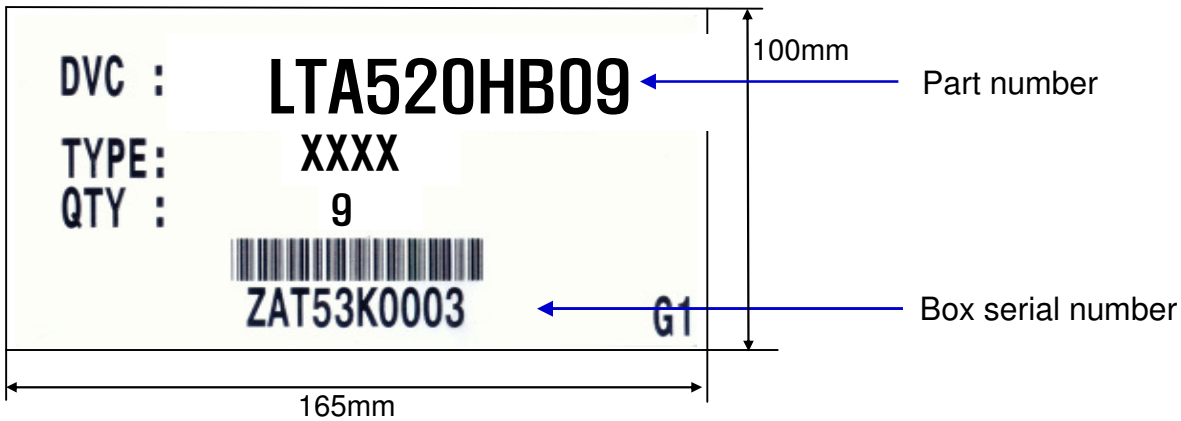
(1) Part number : LTA520HB09
(2) Revision: Three letters
(3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part
   Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

10.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFL back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not disassemble shield case of inverter & LVDS board.

(m) Do not connect N.C pins. (Samsung internal use only)

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module

(o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

(a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35℃ and relative humidity of less than 70%.

(b) Do not store the TFT-LCD Module in direct sunlight.

(c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

(a) Do not connect or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.

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