



Product Specification

AU Optronics Corporation

(V) Preliminary Specifications

() Final Specifications

Module	15.4" WXGA+ Color TFT-LCD
Model Name	B154PW04 V4

Customer

Date

Approved by

Date

Kendra Wang

2007/11/12

Checked &
Approved by

Prepared by

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2007/11/12

Note: This Specification is subject to change without notice.

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Record of Revision

Version and Date	Page	Old description	New Description	Remark
0.1 2007/11/12	All	First Edition for Customer		

1. Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the CCFL Reflector edge. Instead, press at the far ends of the CFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source(, IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CCFL in it is supplied by Limited Current Circuit(IEC60950 or UL1950). Do not connect the CCFL in Hazardous Voltage Circuit.



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2. General Description

B154PW04 V4 is a Color Active Matrix Liquid Crystal Display composed of a TFT LCD panel, a driver circuit, and backlight system. The screen format is intended to support the WXGA+ (1440(H) x 900(V)) screen and 262k colors (RGB 6-bits data driver). All input signals are LVDS interface compatible. Inverter of backlight is not included.

B154PW04 V4 is designed for a display unit of notebook style personal computer and industrial machine.

2.1 General Specification

The following items are characteristics summary on the table at 25 °C condition:

Items	Unit	Specifications
Screen Diagonal	[mm]	391 (15.4W")
Active Area	[mm]	331.560 (H) x 207.225 (V)
Pixels H x V		1440x3(RGB) x 900
Pixel Pitch	[mm]	0.23025X0.23025
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Normally White
White Luminance ($I_{CCFL}=19mA$) Note: I_{LED} is LED current	[cd/m ²]	330 typ. (5 points average) 270 min. (5 points average) (Note1)
Luminance Uniformity		1.25 max. (5 points)
Contrast Ratio		400 typ
Optical Rise Time/Fall Time	[msec]	4/12 typ.
Nominal Input Voltage VDD	[Volt]	+3.3 typ.
Power Consumption	[Watt]	4.8 max
Weight	[Grams]	430 typ. 450 max.
Physical Size	[mm]	344.0 typ. x 222.0 typ. x 6.1 max.
Electrical Interface		2 channel LVDS
Surface Treatment		Anti-Glare, Hardness 2H
Support Color		262K colors (RGB 6-bit)
Temperature Range Operating Storage (Non-Operating)	[°C] [°C]	0 to +50 -20 to +60
RoHS Compliance		RoHS Compliance



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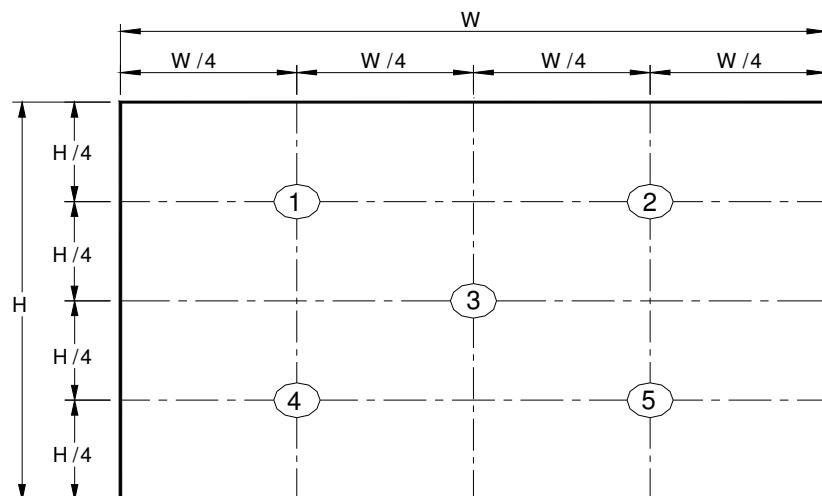
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2.2 Optical Characteristics

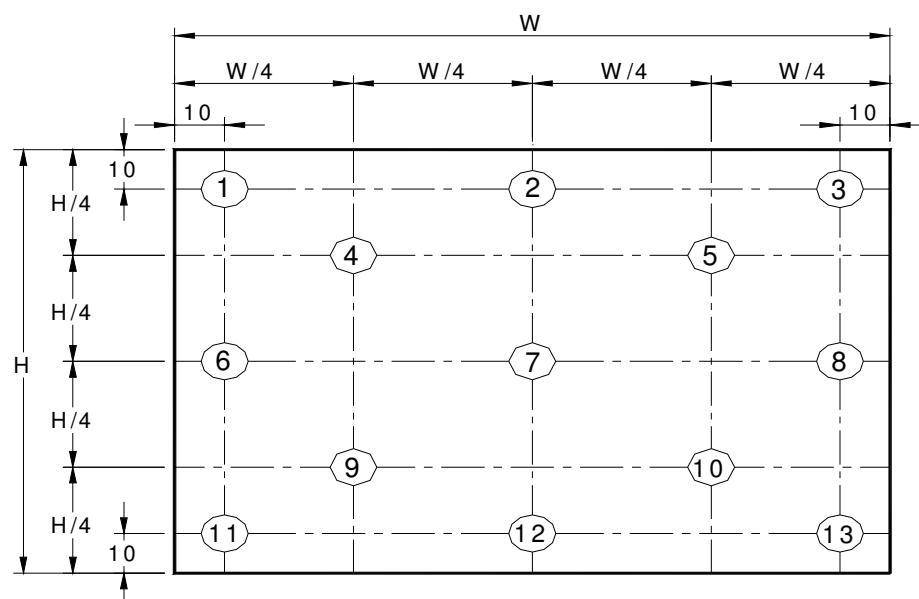
The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
White Luminance I _{LED} =19mA	[cd/m ²]	160 points average	300	330	-	1, 3, 4.
Viewing Angle	[degree] [degree]	Horizontal (Right) CR = 10 (Left)	65 65	70 70	-	8
	[degree] [degree]	Vertical (Upper) CR = 10 (Lower)	65 60	60 60	-	
Luminance Uniformity		5 Points			1.25	1
Luminance Uniformity		13 Points			1.50	2
Response Time	[msec]	Rising	-	4	8	7
	[msec]	Falling	-	12	17	
	[msec]	Rising + Falling		16	25	
Color / Chromaticity Coordinates (CIE 1931)		Red x Red y Green x Green y Blue x Blue y White x White y	0.570 0.315 0.290 0.525 0.120 0.090 0.283 0.299	0.600 0.345 0.320 0.555 0.150 0.120 0.313 0.329	0.630 0.375 0.350 0.585 0.180 0.150 0.343 0.359	2,7

Note 1: 5 points position (Display area : 331.2mm x 207.0mm)



Note 2: 13 points position



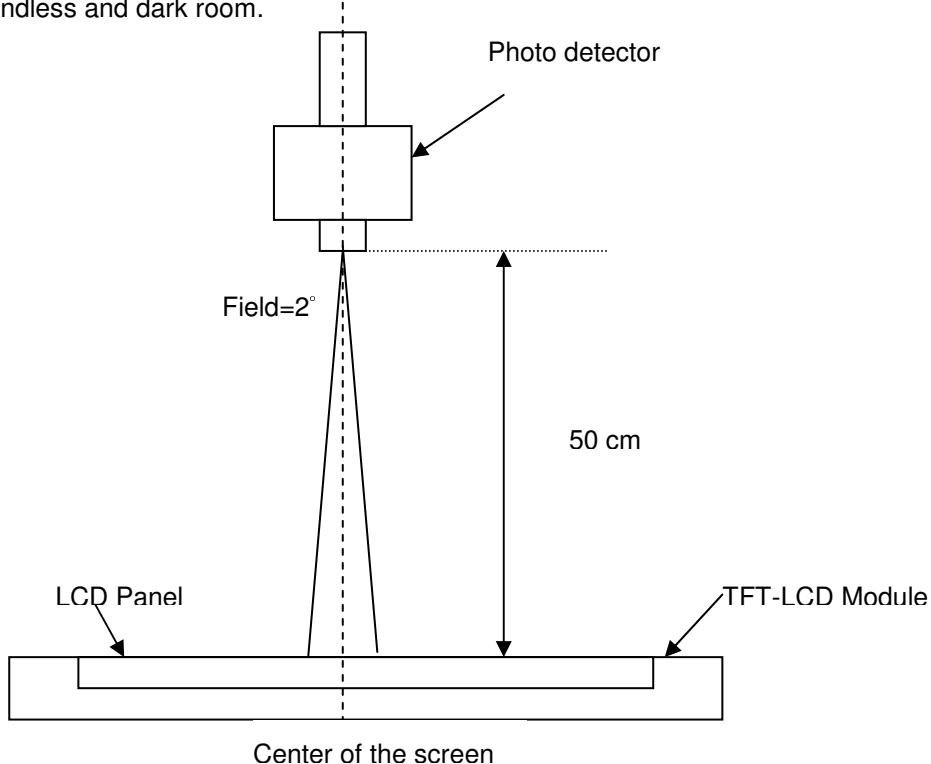
Note 3: The luminance uniformity of 5 and 13 points is defined by dividing the maximum luminance values by the minimum test point luminance

$$\delta_{W5} = \frac{\text{Maximum Brightness of five points}}{\text{Minimum Brightness of five points}}$$

$$\delta_{W13} = \frac{\text{Maximum Brightness of thirteen points}}{\text{Minimum Brightness of thirteen points}}$$

Note 4: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.

**Note 5 : Definition of Average Luminance of White**

$$\text{Average Luminance} : L_{\text{Ave}} = \text{SUM}(L_1:L_{160}) / 160$$

where L1 to L160 are the luminance values measured at point #1 to #160.

Measure the luminance of gray level 63 at 5 points.

Note 6 : Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

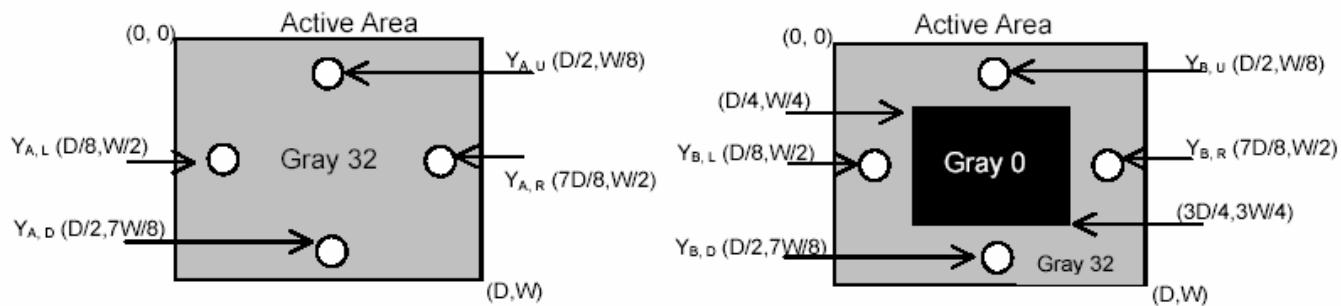
Note 7 : Definition of Cross Talk (CT)

$$CT = | Y_B - Y_A | / Y_A \times 100 (\%)$$

Where

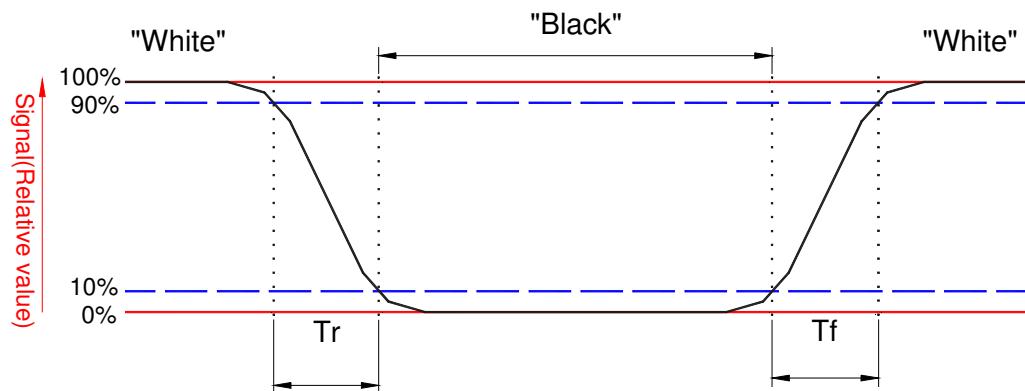
Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)



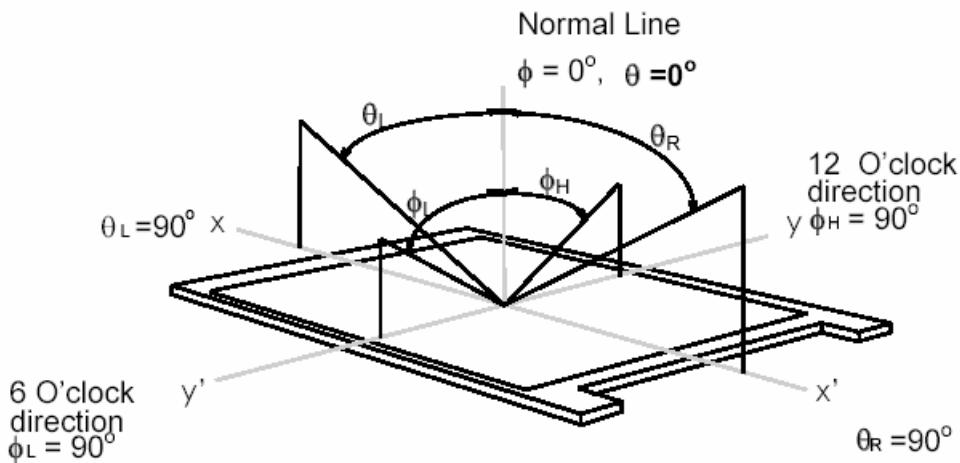
Note 8: Definition of response time:

The output signals of BM-7 or equivalent are measured when the input signals are changed from "Black" to "White" (falling time) and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.



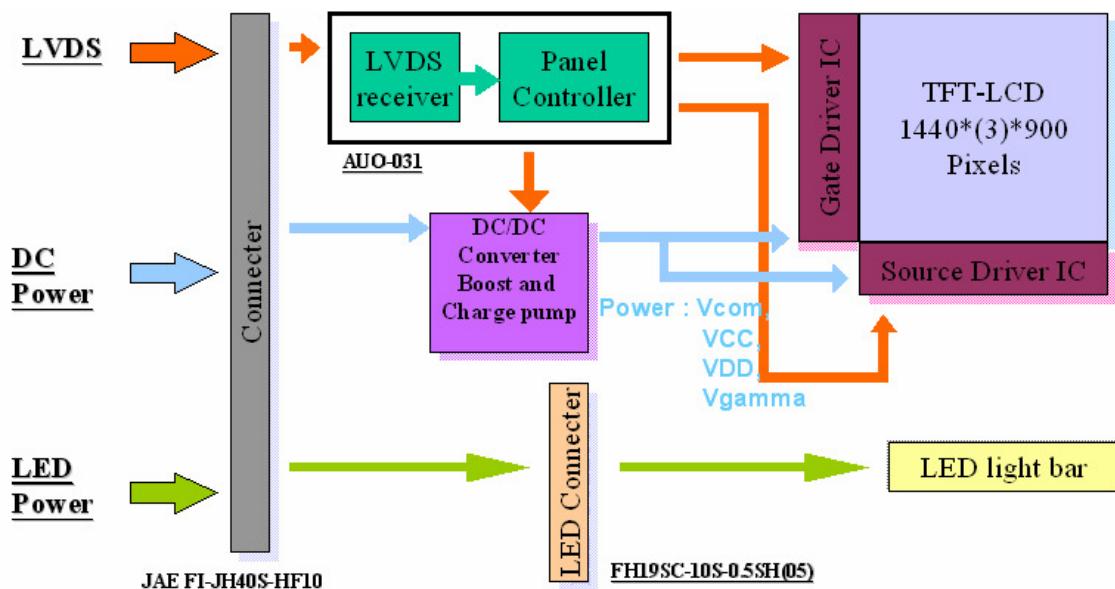
Note 9. Definition of viewing angle

Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; $90^\circ (\theta)$ horizontal left and right and $90^\circ (\Phi)$ vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



3. Functional Block Diagram

The following diagram shows the functional block of the 15.4WXGA+ TFT/LCD Module:



4. Absolute Maximum Ratings

Absolute maximum ratings of the module is as following:

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	V _{in}	-0.3	+4.0	[Volt]	Note 1,2

4.2 Absolute Ratings of Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
LED Current	I _{LED}	-	20	[mA] rms	Note 1,2

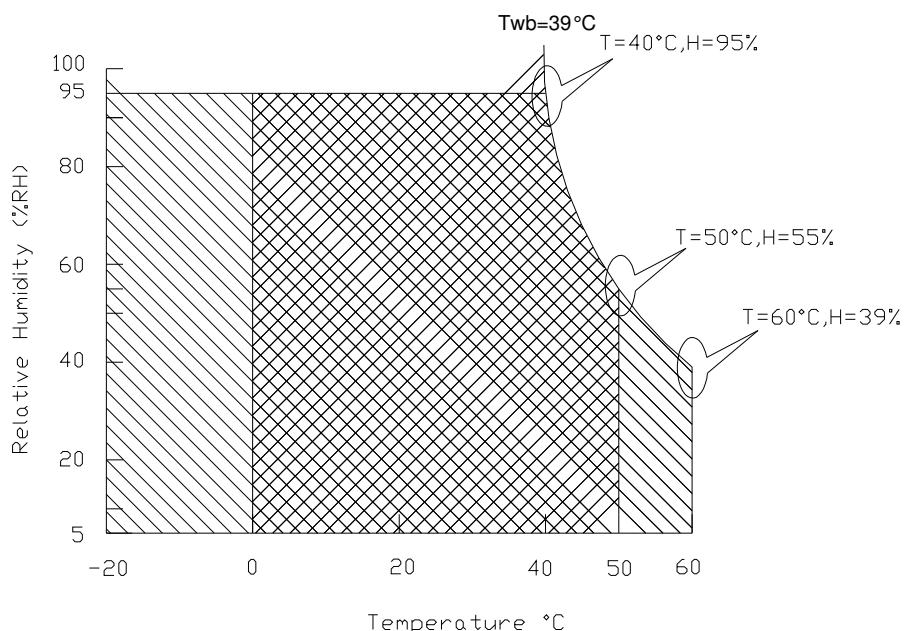
4.3 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit	Conditions
Operating Temperature	T _{OP}	0	+50	[°C]	Note 3
Operation Humidity	H _{OP}	--	95	[%RH]	Note 3
Storage Temperature	T _{ST}	-20	+60	[°C]	Note 3
Storage Humidity	H _{ST}	5	95	[%RH]	Note 3

Note 1: At Ta (25°C)

Note 2: Permanent damage to the device may occur if exceed maximum values

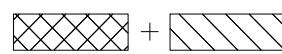
Note 3: For quality performance, please refer to AUO IIS(Incoming Inspection Standard).



Operating Range



Storage Range



5. Electrical characteristics

5.1 TFT LCD Module

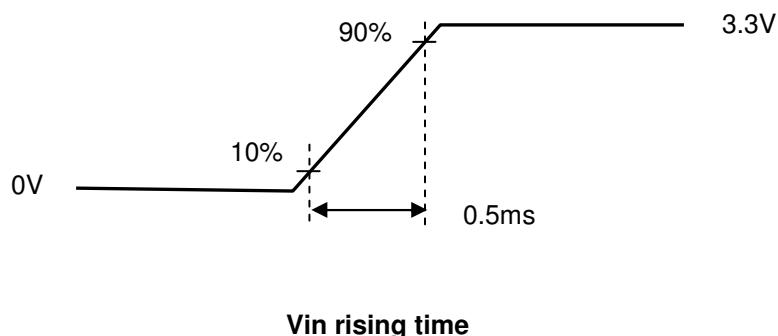
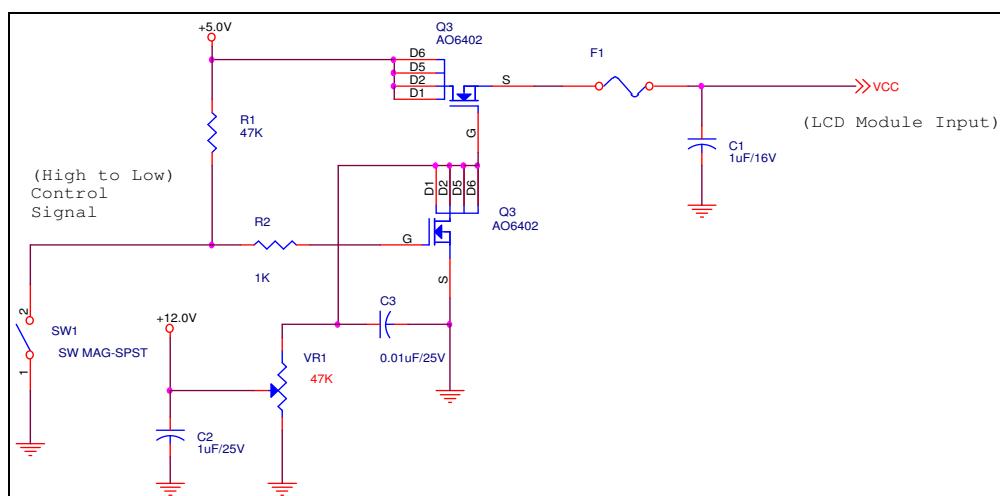
5.1.1 Power Specification

Input power specifications are as follows;

Symbol	Parameter	Min	Typ	Max	Units	Note
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	
PDD	VDD Power			1.3	[Watt]	Note 1
IDD	IDD Current		280	394	[mA]	Note 1
IRush	Inrush Current			2000	[mA]	Note 2
VDDRp	Allowable Logic/LCD Drive Ripple Voltage			100	[mV] p-p	

Note 1 : Maximum Measurement Condition : Black Pattern

Note 2 : Measure Condition



Vin rising time

5.1.2 Signal Electrical Characteristics

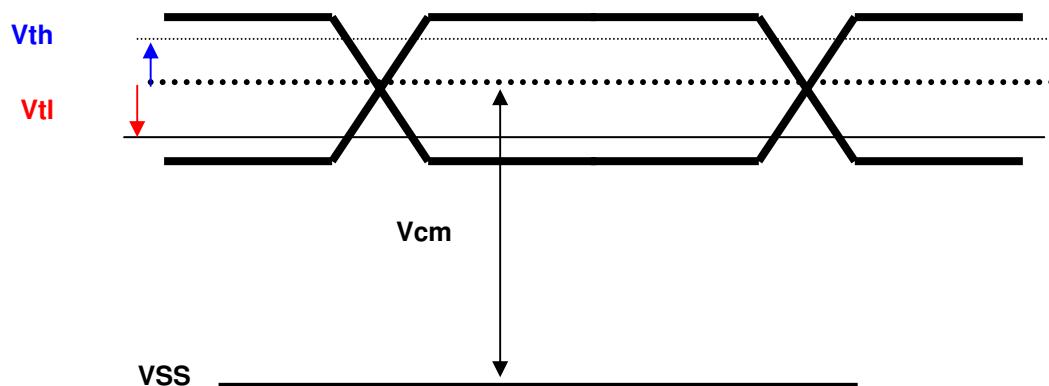
Input signals shall be low or High-impedance state when VDD is off.

It is recommended to refer the specifications of THC63LVDF84A(Thine Electronics Inc.) in detail.

Signal electrical characteristics are as follows:

Parameter	Condition	Min	Max	Unit
Vth	Differential Input High Threshold ($V_{cm}=+1.2V$)		100	[mV]
Vtl	Differential Input Low Threshold ($V_{cm}=+1.2V$)	-100		[mV]
Vcm	Differential Input Common Mode Voltage	1.1	1.45	[V]

Note: LVDS Signal Waveform

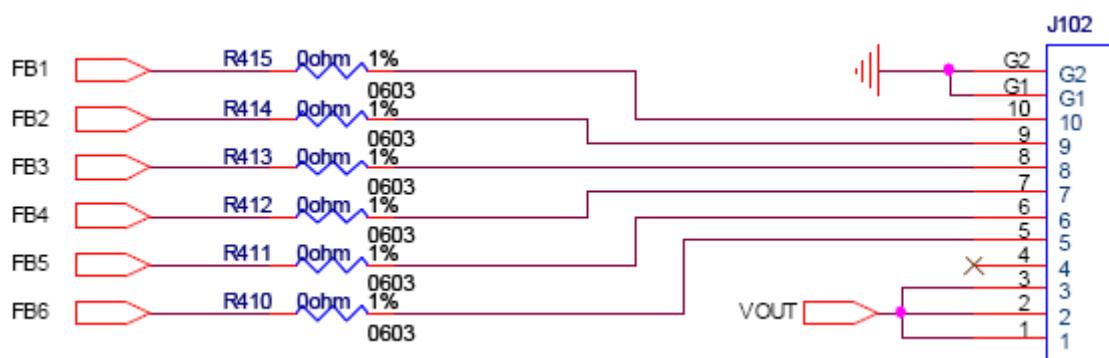


5.2 Backlight Unit

The BLU system is an edge type light source with LED (Light Emitting Diode) light bar

Item	Symbol	Min	Typ	Max	Units	Condition
Fixed input current	IL_t			20	[mA] rms	
Light bar Voltage	VL		32	34	[Volt] rms	
Light bar Power	PL		3.7	3.88	Wt	

Light bar PIN assignment:

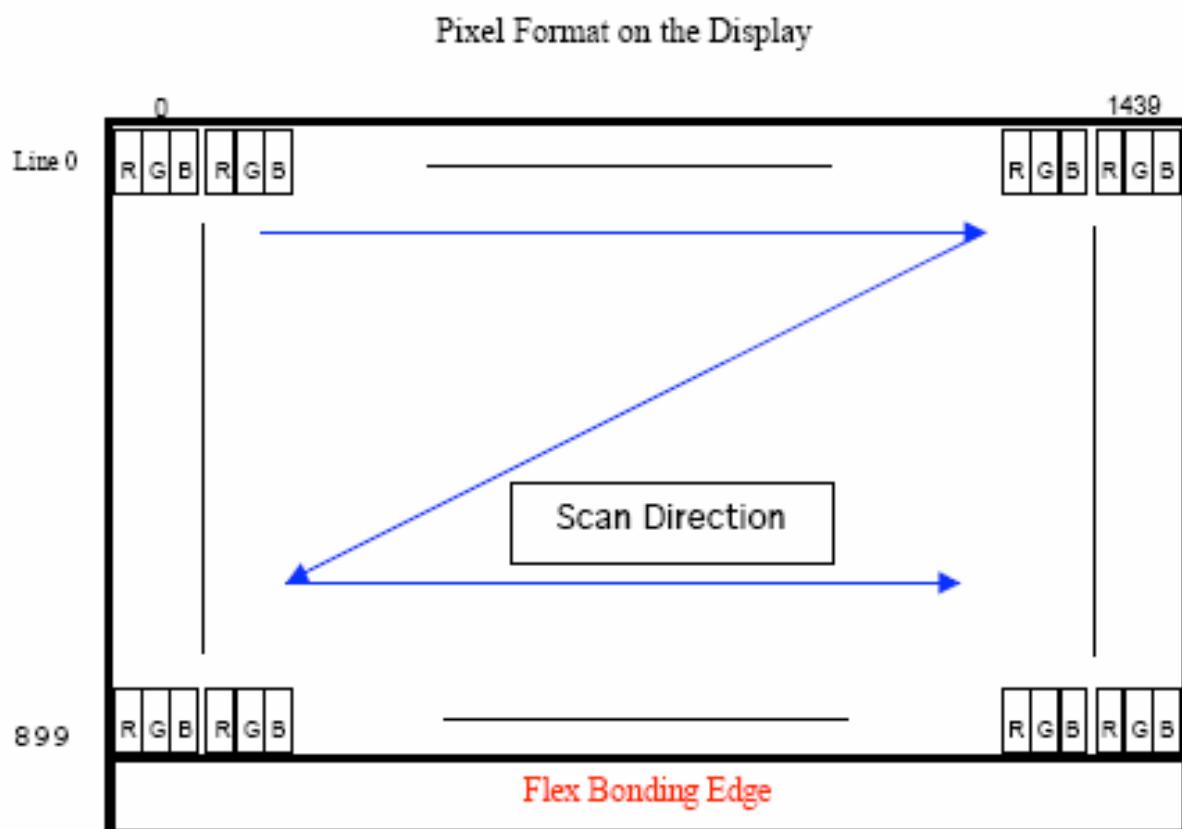


PIN NO.	Pin assignment	Function
1	VOUT	LED Anode (Positive)
2	VOUT	LED Anode (Positive)
3	VOUT	LED Anode (Positive)
4	NC	NC
5	FB6	LED Cathode (Negative)
6	FB5	LED Cathode (Negative)
7	FB4	LED Cathode (Negative)
8	FB3	LED Cathode (Negative)
9	FB2	LED Cathode (Negative)
10	FB1	LED Cathode (Negative)

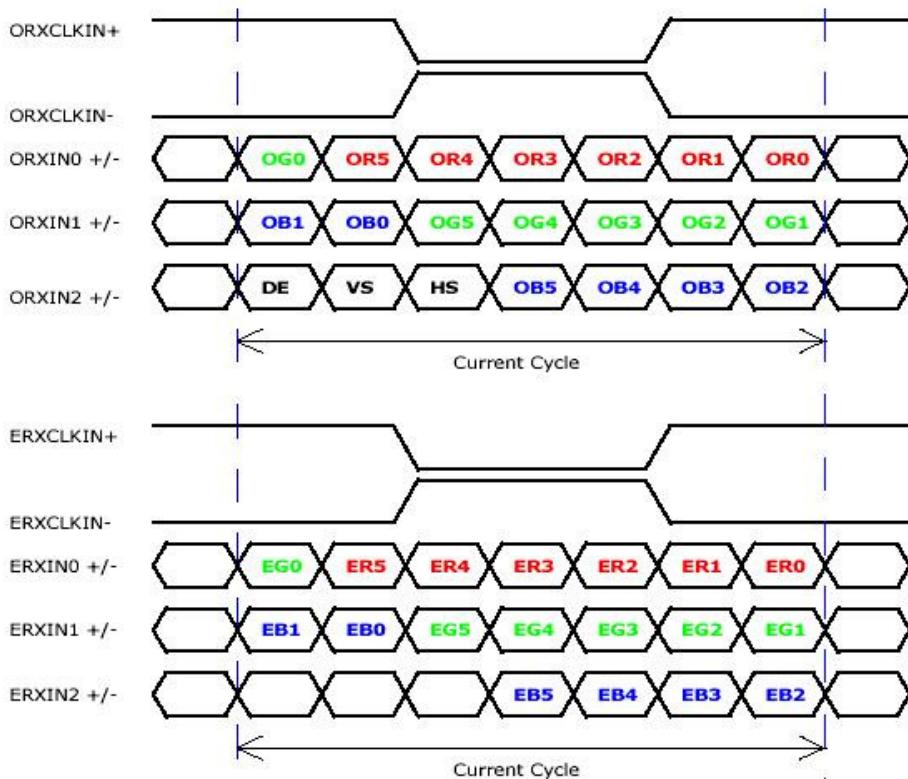
6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.



6.2 The input data format



Signal Name	Description
V_{EDID}	+3.3V EDID Power
CLK_{EDID}	EDID Clock Input
$DATA_{EDID}$	EDID Data Input
ORXIN0-, ORXIN0+	Odd LVDS differential data input(ORed0-ORed5, OGreen0)
ORXIN1-, ORXIN1+	Odd LVDS differential data input(OGreen1-OGreen5, OBlue0-OBlue1)
ORXIN2-, ORXIN2+	Odd LVDS differential data input(OBlue2-OBlue5, Hsync, Vsync, DE)
ORXCLKIN-, ORXCLKIN+	Odd LVDS differential clock input
ERXIN0-, ERXIN0+	Even LVDS differential data input(ERed0-ERed5, EGreen0)
ERXIN1-, ERXIN1+	Even LVDS differential data input(EGreen1-EGreen5, EBlue0-EBlue1)
ERXIN2-, ERXIN2+	Even LVDS differential data input(EBlue2-EBlue5)
ERXCLKIN-, ERXCLKIN+	Even LVDS differential clock input
VDD	+3.3V Power Supply
GND	Ground

Note: Output signals from any system shall be low or High-impedance state when VDD is off.



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6.3 Signal Description/Pin Assignment

LVDS is a differential signal technology for LCD interface and high speed data transfer device.

Pin	Symbol	Description	Micro-coax cable gauge (AWG)
1	GND	Ground	40
2	Vcc	Power Supply (+3.3V)	36
3	Vcc	Power Supply (+3.3V)	36
4	VEDID	DDC 3.3V Power	40
5	Vcc	Power Supply (+3.3V)	36
6	ClkEDID	DDC Clock	40
7	DATAEDID	DDC Data	40
8	Odd_Rin0-	Odd Channel Differential Data Input	40
9	Odd_Rin0+	Odd Channel Differential Data Input	40
10	GND	Ground	40
11	Odd_Rin1-	Odd Channel Differential Data Input	40
12	Odd_Rin1+	Odd Channel Differential Data Input	40
13	GND	Ground	40
14	Odd_Rin2-	Odd Channel Differential Data Input	40
15	Odd_Rin2+	Odd Channel Differential Data Input	40
16	GND	Ground	40
17	Odd_Clkin-	Odd Channel Differential Clock Input	40
18	Odd_Clkin+	Odd Channel Differential Clock Input	40
19	GND	Ground	40
20	Even_Rin0-	Even Channel Differential Data Input	40
21	Even_Rin0+	Even Channel Differential Data Input	40
22	GND	Ground	40
23	Even_Rin1-	Even Channel Differential Data Input	40
24	Even_Rin1+	Even Channel Differential Data Input	40
25	GND	Ground	40
26	Even_Rin2-	Even Channel Differential Data Input	40
27	Even_Rin2+	Even Channel Differential Data Input	40
28	GND	Ground	40
29	Even_Clkin-	Even Channel Differential Clock Input	40
30	Even_Clkin+	Even Channel Differential Clock Input	40
31	Vdc1	LED Cathode (Negative)	40
32	Vdc2	LED Cathode (Negative)	40
33	Vdc3	LED Cathode (Negative)	40
34	Vdc4	LED Cathode (Negative)	40
35	Vdc5	LED Cathode (Negative)	40

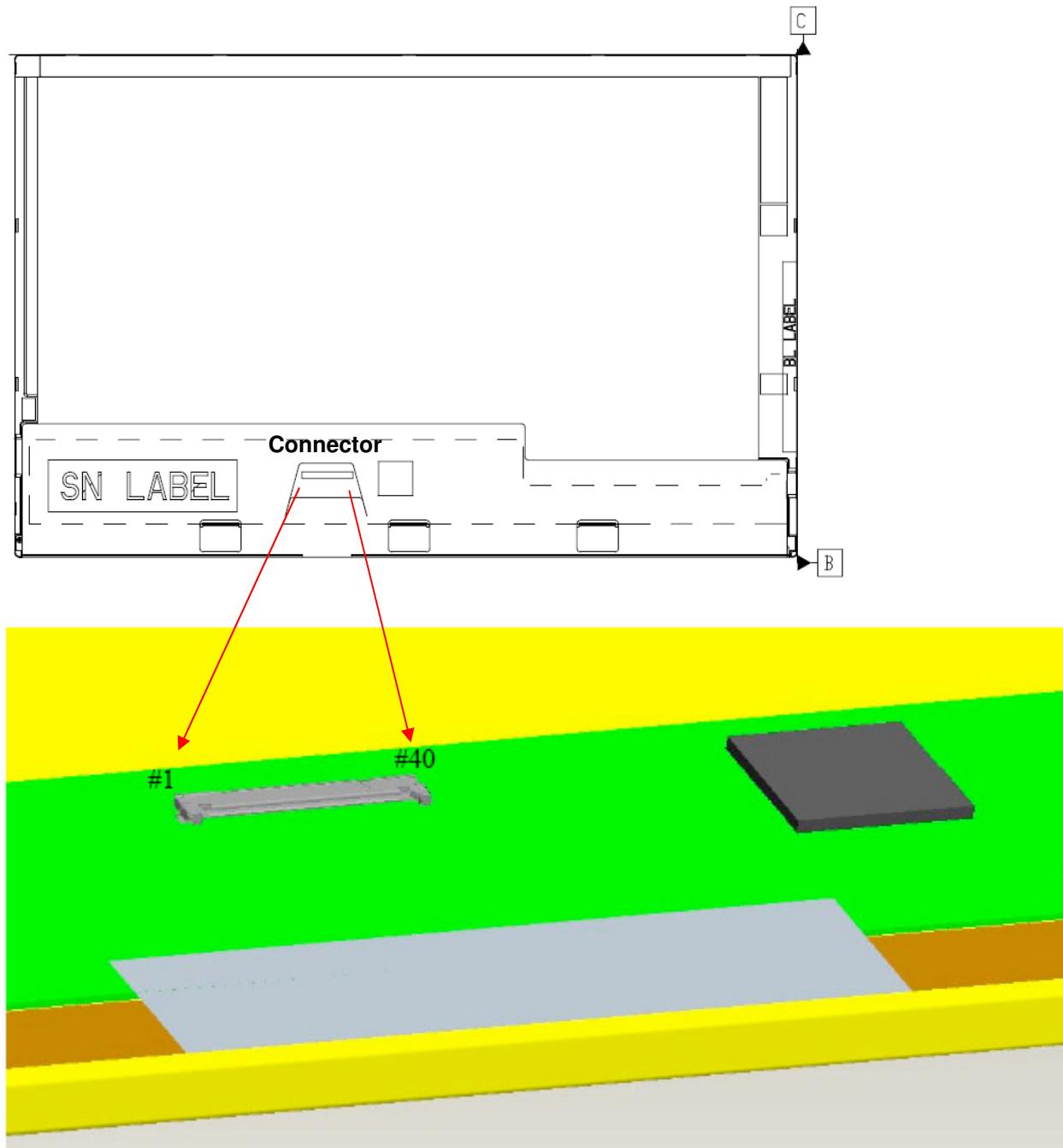


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36	Vdc6	LED Cathode (Negative)	40
37	AGINE	AGINE PIN	40
38	Vdc(1,2)	LED Arnold (Positive)	40
39	Vdc(3,4,)	LED Arnold (Positive)	40
40	Vdc(5,6)	LED Arnold (Positive)	40

Note1: Start from right side



6.4 Interface Timing

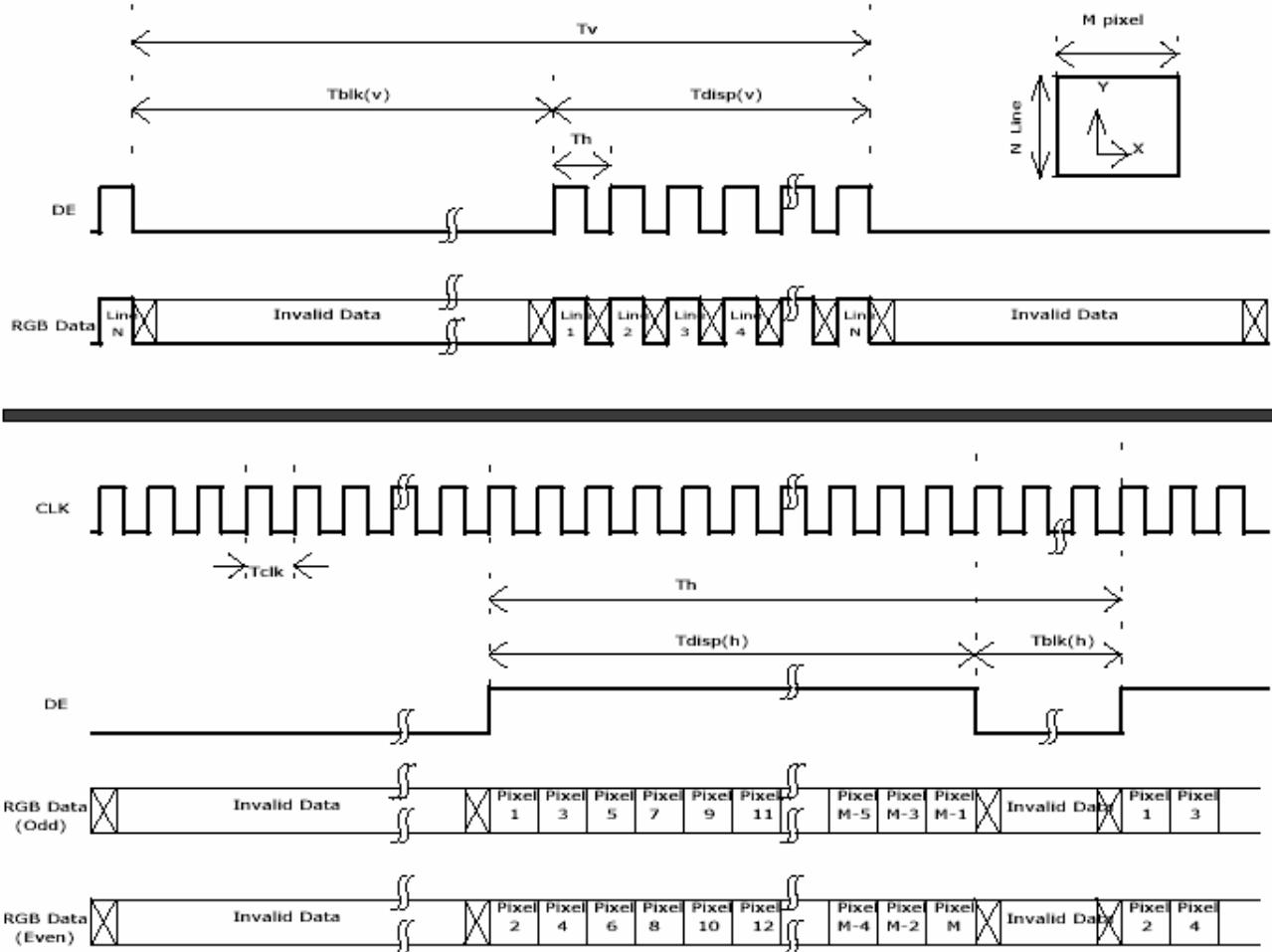
6.4.1 Timing Characteristics

Basically, interface timings should match the 1440X900 /60Hz manufacturing guide line timing.

Parameter	Symbol	Min.	Typ.	Max.	Unit
Frame Rate	-	50	60	-	Hz
Clock frequency	$1/T_{Clock}$	-	48.2	60.2	MHz
Vertical Section	Period	T_V	904	912	2048
	Active	T_{VD}	900	900	900
	Blanking	T_{VB}	4	12	-
Horizontal Section	Period	T_H	760	880	1024
	Active	T_{HD}	720	720	720
	Blanking	T_{HB}	40	160	-

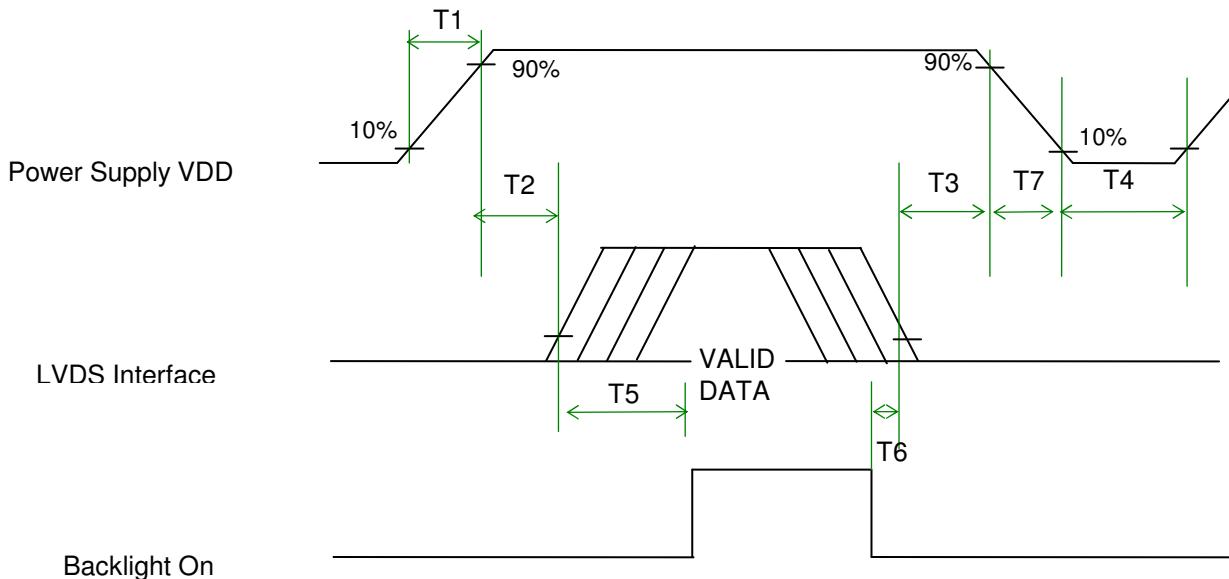
Note : DE mode only

6.4.2 Timing diagram



6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power Sequence Timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	(ms)
T2	5	-	50	(ms)
T3	0.5	-	50	(ms)
T4	400	-	-	(ms)
T5	200	-	-	(ms)
T6	200	-	-	(ms)
T7	0	-	10	(ms)



7. Connector Description

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	For Signal Connector
Manufacturer	JAE or equivalent
Type / Part Number	JAE FI-JH-40S-HF10 or equivalent
Mating Housing/Part Number	JAE FI-JH-40C series or equivalent (micro-coax type)

8. Vibration and Shock Test (Stand alone)

8.1 Vibration Test

Test Spec:

- Test method: Non-Operation
- Acceleration: 1.5G
- Frequency: 26 - 500Hz Random
- Sweep: 30 Minutes each Axis (X, Y, Z)

8.2 Shock Test Spec:

Test Spec:

- Test method: Non-Operation
- Acceleration: 260 G , Half sine wave
- Active time: 2 ms
- Pulse: X,Y,Z .one time for each side

9. Reliability

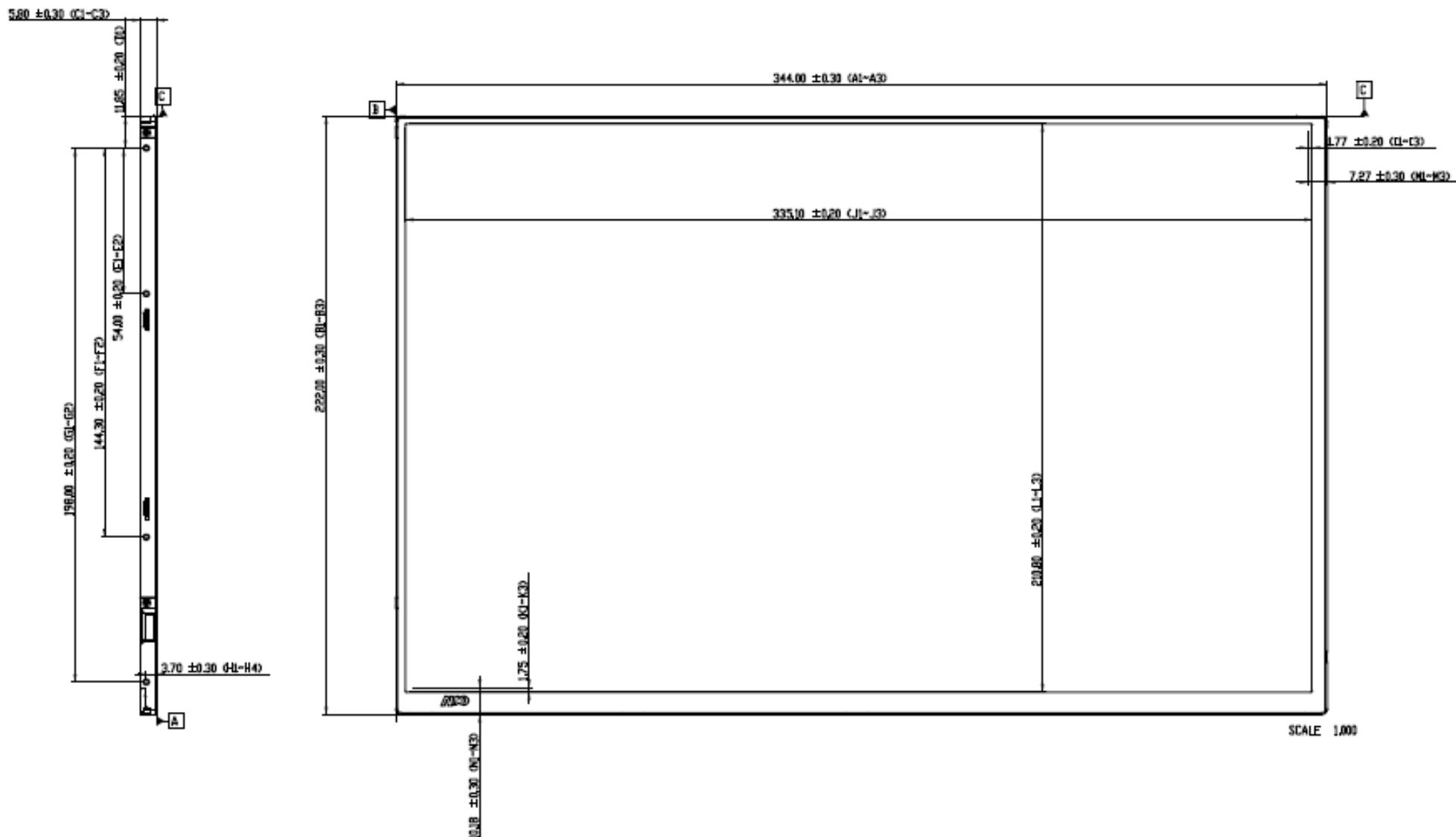
Items	Required Condition	Note
Temperature Humidity Bias	40°C/95%,300Hrs	
High Temperature Operation	50°C/Dry,300Hrs	
Low Temperature Operation	0°C,300Hrs	
On/Off Test	25°C,150hrs(ON/30 sec. OFF/30sec., 10,000 cycles)	
Hot Storage	65°C/20% RH ,300 hours	
Cold Storage	-25°C/50% RH ,300 hours	
Thermal Shock Test	-25°C/30 min ,65°C/30 min 100cycles non-OP	
Shock Test (Non-Operating)	260G, 2ms, Half-sine wave, +/- X, Y,Z direction,1 cycle	
Vibration Test (Non-Operating)	Sinusoidal vibration, 3.0 G zero-to-peak, 10 to 150 Hz, 30 mins in each of three mutually perpendicular axes.	
ESD	Contact : ±8KV/ operation Air : ±15KV / operation	Note 1
Image sticking	10X10 checker pattern, 10 hrs, 25°C. The persisting pattern should be disappeared in 5 minutes	

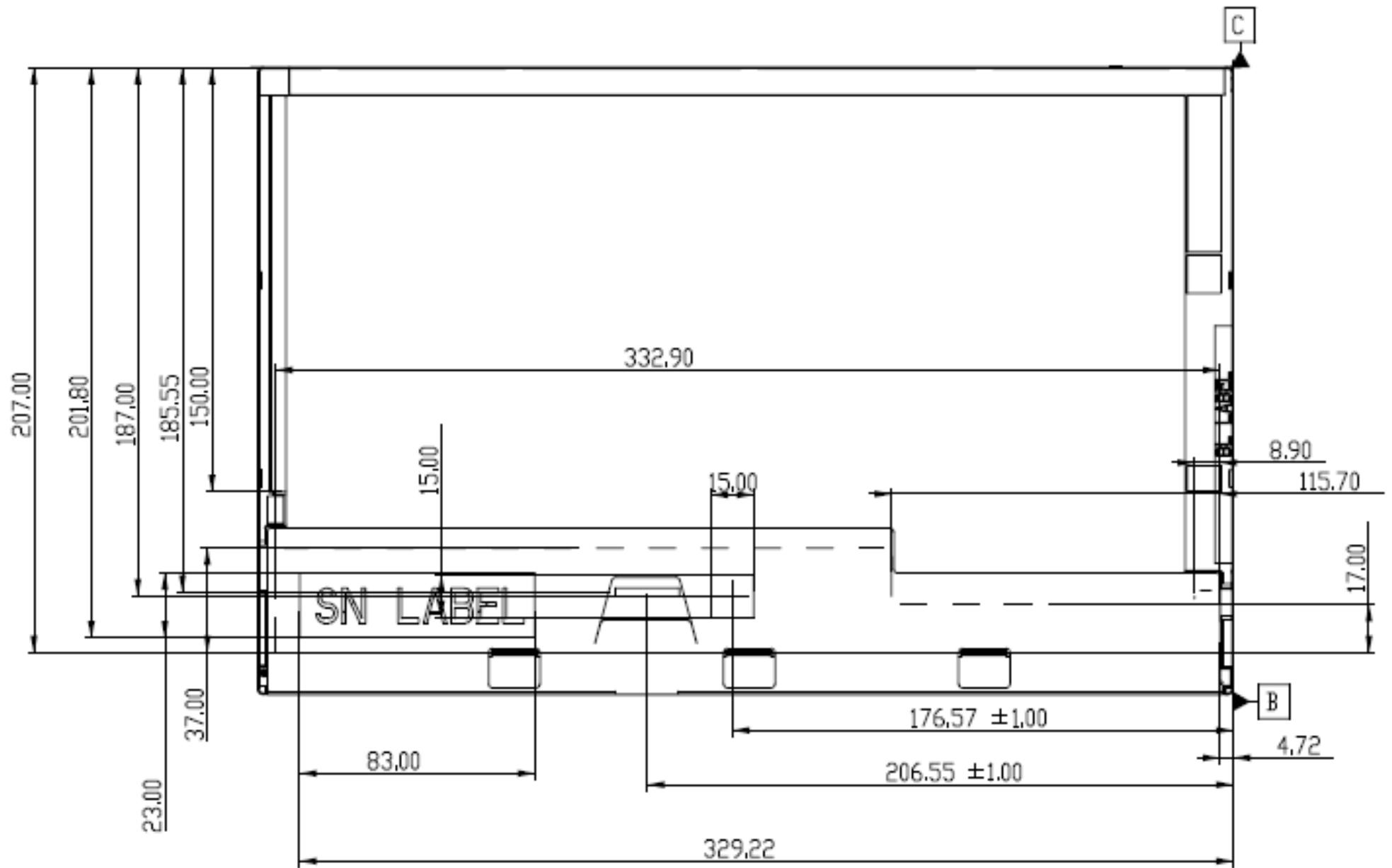
Note1: According to EN61000-4-2 , ESD class B: Some performance degradation allowed. No data lost

- . Self-recoverable. No hardware failures.

10. Mechanical Characteristics

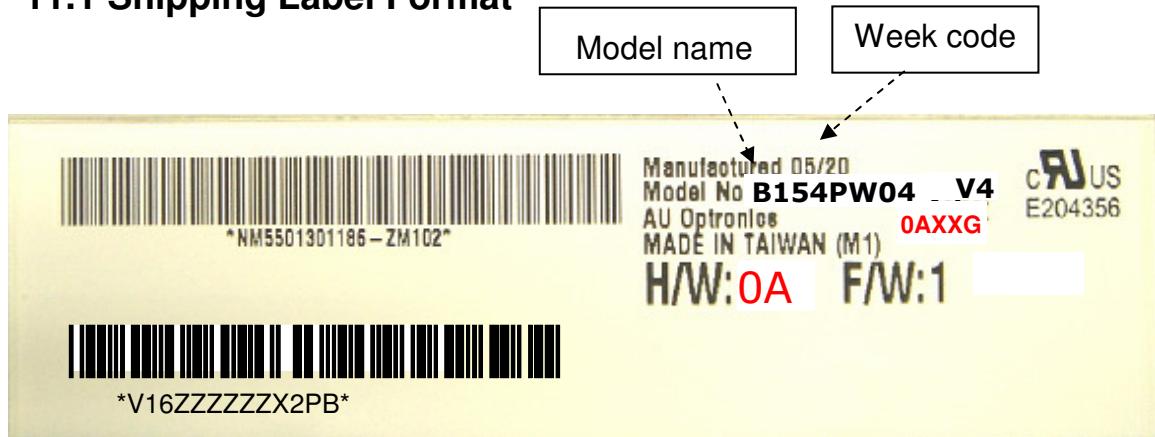
10.1 LCM Outline Dimension





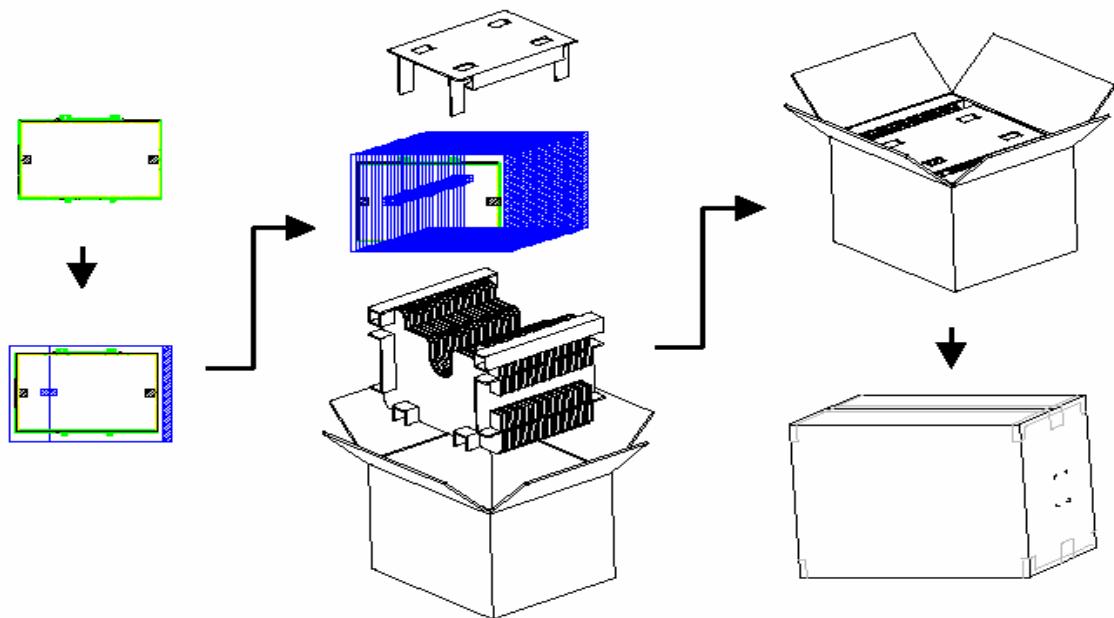
11. Shipping and Package

11.1 Shipping Label Format

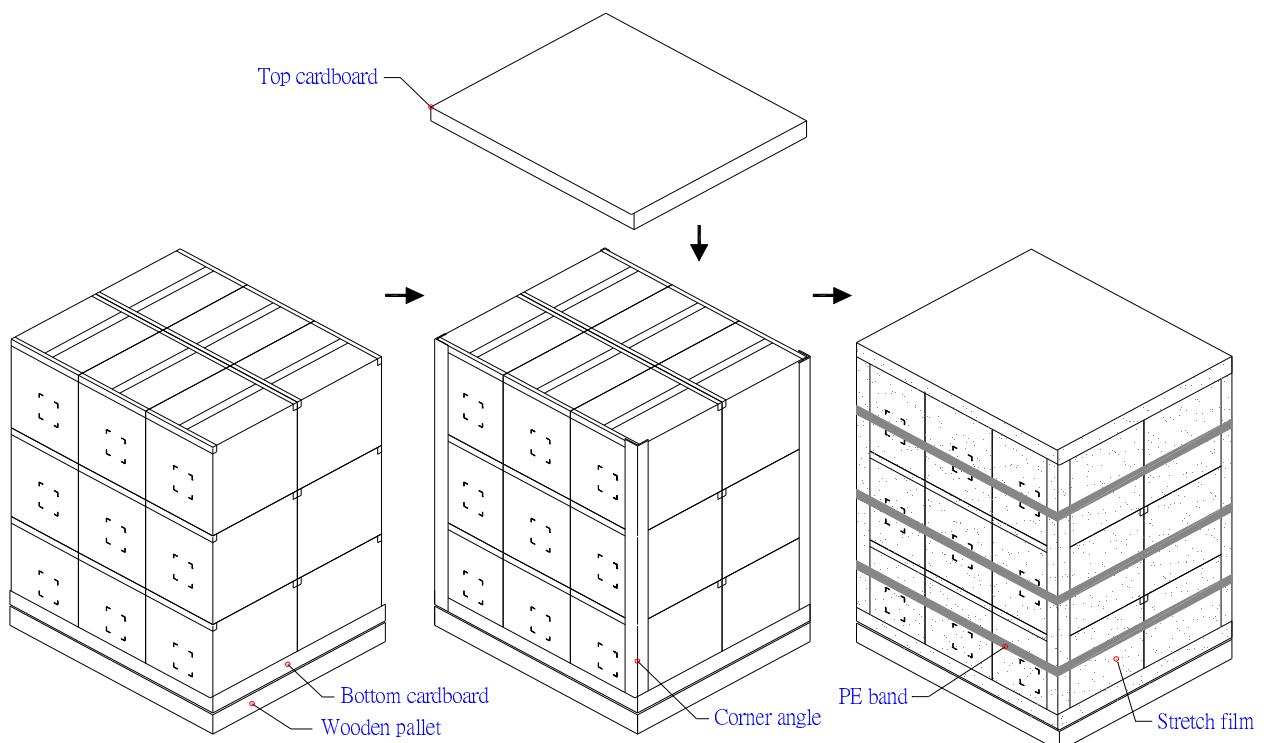


11.2. Carton package

The outside dimension of carton is 455 (L)mm x 380 (W)mm x 355 (H)mm



11.3 Shipping package of palletizing sequence



Note : Limit of box palletizing = Max 3 layers(ship and stock conditions)

12. Appendix: EDID description

Address	FUNCTION	B154PW04	Value	Value	Note
HEX	Header	HEX	BIN	DEC	
00		00	00000000	0	
01		FF	11111111	255	
02		FF	11111111	255	
03		FF	11111111	255	
04		FF	11111111	255	
05		FF	11111111	255	
06		FF	11111111	255	
07		00	00000000	0	
08	EISA Manuf. Code LSB Compressed ASCII Product code Product code	06	00000110	6	APP 0 00001(A) 10000(P) 10000(P) 9C66
09		10	00010000	16	
0A		66	01100110	102	
0B		9C	10011100	156	
0C	32-bit ser #	01	00000001	1	unused
0D		01	00000001	1	
0E		01	00000001	1	
0F		01	00000001	1	
10		28	00101000	40	
11	Week of manufacture	10	00010000	16	Week=40 Year=2006
12	Year of manufacture	01	00000001	1	
13	EDID Structure Ver.	03	00000011	3	
14	EDID revision #	80	10000000	128	
15	Video input definition	21	00100001	33	Digital Input
16	Max H image size	15	00010101	21	33cm
17	Max V image size	78	01111000	120	21cm
18	Display Gamma	0A	00001010	10	Gamma=2.2
19	Feature support	50	01010000	80	
1A	Red/green low bits	C5	11000101	197	no DPMS, Active off, RGB color
1B	Blue/white low bits	98	10011000	152	
1C	Red x/ high bits	58	01011000	88	Rx=0.595
1D	Red y	52	01010010	82	Ry=0.345
1E	Green x	8E	10001110	142	Gx=0.32
1F	Green y	27	00100111	39	Gy=0.555
20	Blue x	25	00100101	37	Bx=0.155
21	Blue y	50	01010000	80	By=0.145
22	White x	54	01010100	84	Wx=0.313
23	White y	00	00000000	0	Wy=0.329
24	Established timing 1	00	00000000	0	unused
	Established timing 2				

25	Manufacturer's Timing Standard timing #1 Standard timing #2 Standard timing #3 Standard timing #4 Standard timing #5 Standard timing #6 Standard timing #7 Standard timing #8 Pixel Clock/10,000 (LSB) Pixel Clock/10,000 (MSB) Horiz. Active pixels(Lower 8 bits) Horiz.Blinking (Lower 8 bits) Horiz. Active pixels:Horiz. Blanking (Upper4:4 bits) Vert. Active pixels:Vert. Blanking (Upper4:4 bits) Vert. Sync. Offset=xx lines, Sync Width=xx lines Horz. Ver. Sync/Width (upper 2 bits) Hori. Image size (Lower 8 bits) Vert. Image size (Lower 8 bits) Hori. Image size : Vert. Image size (Upper 4 bits)	00	00000000	0	unused
26		01	00000001	1	
27		01	00000001	1	
28		01	00000001	1	
29		01	00000001	1	
2A		01	00000001	1	
2B		01	00000001	1	
2C		01	00000001	1	
2D		01	00000001	1	
2E		01	00000001	1	
2F		01	00000001	1	
30		01	00000001	1	
31		01	00000001	1	
32		01	00000001	1	
33		01	00000001	1	
34		01	00000001	1	
35		01	00000001	1	
36		9E	10011110	158	Timing Descriptor #1 Pixel clock=96.3Mhz Horiz active=1440 pixels Horiz blanking=320 pixels
37		25	00100101	37	
38		A0	10100000	160	
39		40	01000000	64	
3A		51	01010001	81	
3B		84	10000100	132	
3C		0C	00001100	12	
3D		30	00110000	48	
3E	Vert. Active pixels:Vert. Blanking (Upper4:4 bits)	40	01000000	64	Vertical active=900 lines Vertical blanking=12 lines Horiz sync. Offset= 64 pixels Horiz sync. Pulse Width= 32 pixels Verti sync. Offset= 3 lines, Sync Width=3 lines
3F		20	00100000	32	
40		33	00110011	51	
41		00	00000000	0	
42		4C	01001100	76	
43		CF	11001111	207	
44		10	00010000	16	
45		00	00000000	0	
46		00	00000000	0	
47		18	00011000	24	
48	Detailed timing/monitor descriptor #2	00	00000000	0	Horizontal Border = 0 Vertical Border = 0
49		00	00000000	0	
4A		00	00000000	0	
4B		01	00000001	1	
					Customer reserved

4C	Detailed timing/monitor descriptor #3	Version	00	00000000	0	Customer reserved Customer reserved Customer reserved Customer reserved Customer reserved Customer reserved
4D		edid signature	06	00000110	6	
4E		edid signature	10	00010000	16	
4F		Link Type (LVDS Link,MSB justified)	30	00110000	48	
50		Pixel and link component format (6-bit panel interface)	00	00000000	0	
51		Panel features (No inverter)	00	00000000	0	
52			00	00000000	0	
53			00	00000000	0	
54			00	00000000	0	
55			00	00000000	0	
56			00	00000000	0	
57			00	00000000	0	
58			0A	00001010	10	
59			20	00100000	32	
5A			00	00000000	0	
5B			00	00000000	0	
5C			00	00000000	0	
5D			FE	11111110	254	
5E			00	00000000	0	
5F	Detailed timing/monitor descriptor #4		42	01000010	66	B 1 5 4 P W 0 4 V 0 Monitor Name: Color LCD
60			31	00110001	49	
61			35	00110101	53	
62			34	00110100	52	
63			50	01010000	80	
64			57	01010111	87	
65			30	00110000	48	
66			34	00110100	52	
67			20	00100000	32	
68			56	01010110	86	
69			30	00110000	48	
6A			0A	00001010	10	
6B			20	00100000	32	
6C			00	00000000	0	
6D			00	00000000	0	
6E			00	00000000	0	
6F			FE	11111110	254	
70			00	00000000	0	
71	Detailed timing/monitor descriptor #5		43	01000011	67	C o I o
72			6F	01101111	111	
73			6C	01101100	108	
74			6F	01101111	111	

75		72	01110010	114	r
76		20	00100000	32	
77		4C	01001100	76	L
78		43	01000011	67	C
79		44	01000100	68	D
7A	Extension Flag	0A	00001010	10	
7B		20	00100000	32	
7C		20	00100000	32	
7D		20	00100000	32	
7E		00	00000000	0	
7F		Checksum	87	10000111	135
	Checksum		SUM	6656	
			SUM to HEX	1A00	
			Check	00	