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Pro 6.5" ce

Product Specification 6.5" color TFT-LCD module

MODEL NAME: C065GW02 V1

(◆) Draft Specification(.....) Final Specification

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Note: The content of this specification is subject to change without prior notice.



Record of Revision

			record of revision
Version	Revise Date	Page	Content
0	5 Jul, 06		First draft version.
1	10 Jul, 06	12	Updated package flow
2	3 Oct, 06	8	Updated brightness and CIE value
3	9 Feb, 07	7	Modified T _{CPH}



Model

: C065GW02 V1

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A. Physical specifications

NO.	Item	Specification	Remark
1	Display resolution(dot)	400RGB(W)×234(H)	
2	Display Active area(mm)	143.4(W)×79.326(H)	
3	Screen size(inch)	6.5(Diagonal)	
4	Dot pitch(mm)	0.1195(W)×0.339 (H)	
5	Color configuration	R. G. B. stripe	
6	Overall dimension(mm)	155(W)×90.83(H)×8.15(D)	Note 1
7	Weight(g)	150±10(g)	
8	Surface treatment	AG(25%) with SWV film	
10	Backlight unit	L type CCFL lamp	

Note 1: Refer to Fig. 1



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B. Electrical specifications

1.Pin assignment

a. TFT-LCD panel driving section

Pin no	Symbol	I/O	Description	Remark
1	GND	-	Ground for logic circuit	
2	V_{CC}	ı	Supply voltage of logic control circuit for scan driver	
3	V_{GL}	I	Negative power for scan driver	
4	V_{GH}	I	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1,2
9	OEV	I	Output enable input for scan driver	
10	VCOM	I	Common electrode driving signal	
11	VCOM	Ι	Common electrode driving signal	
12	L/R	ı	LEFT/RIGHT scan control input	Note 1,2
13	MOD	I	Sequential sampling and simultaneous sampling setting	Note 3
14	OEH	ı	Output enable input for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	CPH3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	V_{CC}	I	Supply voltage of logic control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	I	Alternated video signal input(Red)	
23	VG	I	Alternated video signal input(Green)	
24	VB	I	Alternated video signal input(Blue)	
25	AV_DD	I	Supply voltage for analog circuit	
26	AV_SS	ı	Ground for analog circuit	

Note 1: Selection of scanning mode (please refer to the following table)

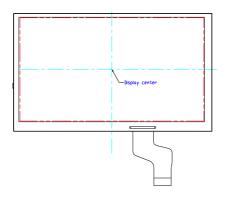
Setting of scan control input				T state t pulse		Scanning direction
U/D	L/R	STVR	STVL	STHR	STHL	
GND	V _{CC}	OUT	IN	OUT	IN	From up to down, and from left to right.
V _{CC}	GND	IN	OUT	IN	OUT	From down to up, and from right to left.
GND	GND	OUT	IN	IN	OUT	From up to down, and from right to left.
V _{CC}	V _{CC}	IN	OUT	OUT	IN	From down to up, and from left to right.

IN: Input; OUT: Output.



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Note 2: Definition of scanning direction. Refer to figure as below:



Note 3: MOD = H: Simultaneous sampling. MOD = L: Sequential sampling. Please set CPH2 and CPH3 to GND when MOD = H.

b. Backlight driving section (Refer to Fig. 1)

No.	Symbol	I/O	Description	Remark
1	HI	ı	Power supply for backlight unit (High voltage)	
2	GND	-	Ground for backlight unit	

Note 1: Connector Type: JST-03VS-1 (3pin type), please refer Page 21.

Note 2: Shrouded header: SM03(4.0)B-BHS-1-TB

2. Absolute maximum ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
	V _{CC}	GND=0	-0.3	7	V	
	AV_DD	AV _{SS} =0	-0.3	7	V	
Power voltage	V_{GH}	OND 0	-0.3	18	V	
	V_{GL}	GND=0	-15	0.3	V	
	$V_{GH} - V_{GL}$		-	33	V	
	V_{i}		-0.3	AV _{DD} +0.3	V	Note 1
Input signal voltage	V_{l}		-0.3	V _{CC} +0.3	V	Note 2
	VCOM		-2.9	5.2	V	

Note 1: VR, VG, VB.

Note 2: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

3. Environment condition

Ite	m	Condition	Spec.	Remark
Operating	Panel with	Panel surface	-30∼ 85℃	Note 1,2,3
temperature	lighting BLU	temperature		
Storage	Panel only	Ambient	-40 ~95°C	
temperature		temperature		

Note 1:Under the condition of the operating temperature, the panel would be function normal for the visual display only. For contrast, response time, and other factors related to display quality, determine temperature using the formula Ta=25°C

Note 2: Panel surface temperature is defined as the maximum temperature of panel surface. In general, the lamp side temperature is higher than non-lamp side around 10 $\,^{\circ}$ C

Note 3: The backlight has been turned on before running about condition.



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4. Electrical characteristics

a. Typical operating conditions (GND=AVss=0V, Note 4)

Item	1	Symbol	Min.	Тур.	Max.	Unit	Remark
		V _{CC}	3	3.3	5.2	V	
		AV_DD	4.8	5	5.2	V	
Power su	upply	V_{GH}	14.3	15	15.7	V	
		V_{GL}	-10.5	-10	-9.5	V	
Video si	_	V_{iA}	0.4	-	AV _{DD} -0.4	٧	Note 1
amplitu (VR,VG,		V_{iAC}	-	3	-	٧	AC component
(۷n, ۷G,	V D)	V_{iDC}	1	AV _{DD} /2	-	V	DC component
VCO	. 4	V_{CAC}	5.3	5.6	5.9	Vp-p	AC component
VCOM		$V_{\mathtt{CDC}}$	1.6	1.9	2.2	٧	DC component
Input	H Level	V_{IH}	0.8V _{CC}	-	V _{CC}	٧	Note 2
signal voltage	L Level	V_{IL}	0	-	0.2V _{CC}	V	Note 2

Note 1: Refer to Fig.4- (a).

Note 2: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

Note 3: Be sure to apply GND, V_{CC} and V_{GL} to the LCD first, and then apply V_{GH} .

b. Current consumption (GND=AVss=0V)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Current	I_{GH}	V _{GH} =15V	-	0.052	0.1	mA	
for driver	I_{GL}	V _{GL} =-10V	-	0.049	0.1	mA	
unver	I _{cc}	V _{CC} =3.3V	-	1.03	1.85	mA	
	I _{DD}	AV _{DD} =5V	-	5.05	6	mA	

c. Backlight driving conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp voltage	V_L	-	510	560	Vrms	
Lamp current	ΙL	ı	6	6.5	mArms	
Frequency	F_L	ı	60	80	kHz	Note 4
Lamp starting	V	-	-	1,000	Vrms	Note 1,5
Lamp starting voltage	Vs	-	-	1,300	Vrms	Note 2,5
voltago		-	-	1580	Vrms	Note 3,5
Lamp life time		10,000	-	-	Hr	Note 6

Note 1: Ta = 25° C.

Note 2: $Ta = 0^{\circ}C$.

Note 3: Ta = -30° C.

Note 4: The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference.

Note 5: For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.

Note 6: The" Lamp life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 $^{\circ}$ C , I_L=6.0mA.

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5. AC Timing

a. Timing conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
Rising time	t _r	-	-	20	ns	Note 1
Falling time	t _f	ı	-	20	ns	Note 1
High and low level pulse width	t _{CPH}	99	126	131	ns	CPH1~CPH3
CPH pulse duty	$t_{\sf CWH}$	40	50	60	%	CPH1~CPH3
CPH pulse delay	t _{C12} t _{C23} t _{C31}	30	t _{CPH} /3	t _{CPH} /2	ns	CPH1~CPH3
STH setup time	t _{SUH}	20	-	-	ns	STHR,STHL
STH hold time	t _{HDH}	20	-	-	Ns	STHR,STHL
STH pulse width	t _{STH}	-	1	-	t _{CPH}	STHR,STHL
STH period	t _H	61.5	63.5	65.5	μ s	STHR,STHL
OEH pulse width	t _{OEH}	-	1.22	-	μ s	OEH
Sample and hold disable	t _{DIS1}	-	8.28	-	μ s	
OEV pulse width	t_{OEV}	-	5.40	-	μ s	OEV
CKV pulse width	t_{CKV}	-	4.18	-	μ s	CKV
Clean enable time	t _{DIS2}	-	3.74	-	μ s	
Horizontal display start	t_{SH}	1	0	-	T _{CPH} /3	
Horizontal display timing range	t _{DH}	-	1200	-	T _{CPH} /3	
STV setup time	t _{suv}	400	-	-	ns	STVL,STVR
STV hold time	t_{HDV}	400	-	-	ns	STVL,STVR
STV pulse width	t _{STV}	-	-	1	t _H	STVL,STVR
Horizontal lines per field	t_V	256	262	268	t _H	Note 2
Vertical display start	t _{sv}		3	-	t _H	
Vertical display timing range	t_DV		234	-	t _H	
VCOM rising time	t_{rCOM}		-	5	μ s	
VCOM falling time	t_{fCOM}		-	5	μ s	
VCOM delay time	t _{DCOM}		-	3	μ s	
RGB delay time	t _{DRGB}		-	1	μ s	

Note 1: For all of the logic signals.

Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.

b. Timing diagram

Please refer to the attached drawing, from Fig.2 to Fig.6.



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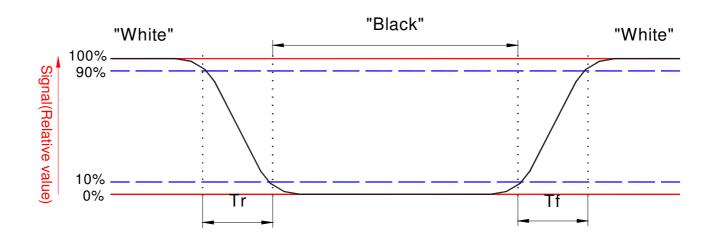
C. Optical specification (Note 1)

Item	1	Symb	Condition	Min.	Тур.	Max.	Unit	Remark
Response time	Rise Fall	Tr Tf	$\theta = 0^{\circ}$	- -	15 20	50 60	ms ms	Note 1,2,4
Contrast	ratio	CR	At optimized Viewing angle	200	300	-		Note 3,4
	Top Bottom Left Right		CR≧10	30 50 50 50	45 60 60 60	- - -	deg.	Note 4,6
Viewing angle	Top Bottom Left Right		CR≧5	40 60 60 60	50 70 70 70	- - -	deg.	Note 4,5
Brightn	ess	Y_L	I _L =6.0mA, 25°℃	500	550	-	nit	Note 6
Uniforn	nity		θ =0°			1.3		Note 7
White chroi	maticity	X	$\theta = 0^{\circ}$ $\theta = 0^{\circ}$	0.26 0.28	0.31 0.33	0.36 0.38		Note 6

Note 1 : Ambient temperature =25 $^{\circ}$ C. And lamp current I_L = 6.0 mArms. To be measured in the dark room and to be measured on the center area of panel with a viewing cone of 1 $^{\circ}$ by Topcon luminance meter BM-5, after 10 minutes operation.

Note 2. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.





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Note 3. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio (CR)= Photo detector output when LCD is at "White" state

Photo detector output when LCD is at "Black" state

Note 4. White $Vi=V_{i50} + 1.5V$

Black Vi=V_{i50} ± 2.0V

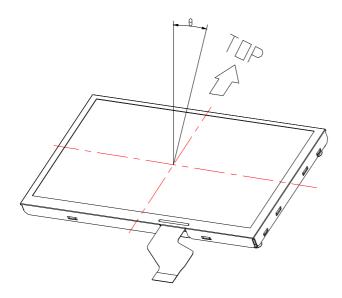
"±" means that the analog input signal swings in phase with V_{COM} signal.

" $\overline{+}$ " means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50} . The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 5. Definition of viewing angle, refer to figure as below.



Note 6. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 7. Uniformity value = (max. brightness)/(min. brightness), 9 points.



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D. Reliability test items(Note 2):

No.	Test items	Conditions	Remark
1	High temperature storage	Ta= 95°C 240Hrs	
2	Low temperature storage	Ta= -40°C 240Hrs	
3	High temperature operation	Ta= 85°C 240Hrs	
4	Low temperature operation	Ta= -30°C 240Hrs	
5	High temperature and high humidity	Ta= 60°C, 90% RH 240Hrs	Operation
6	Heat shock	-40°C ~85°C/240 cycles 1Hrs/cycle	Non-operatio
7	Electrostatic discharge	\pm 200V,200pF(0 Ω), once for each terminal	Non-operatio n
8	Vibration	Frequency range : 8~33.3Hz Stoke : 1.3mm Sweep : 2.9G,33.3~400Hz 2 hours for each direction of X, Z 4 hours for direction of Y	JIS D1601, A-10 condition A Note3
9	Mechanical shock	100G, 6ms, ±X,±Y,±Z 3 times for each direction	JIS C7021, A-7 condition C
10	Vibration (with carton)	Random vibration: 0.015G ² /Hz from 5~200Hz –6dB/octave from 200~500Hz	IEC 68-34
11	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202

Note 1: Ta: Ambient temperature.

Note 2: In the standard conditions, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.

Note 3: Cycle time for vibration is 15 minutes.



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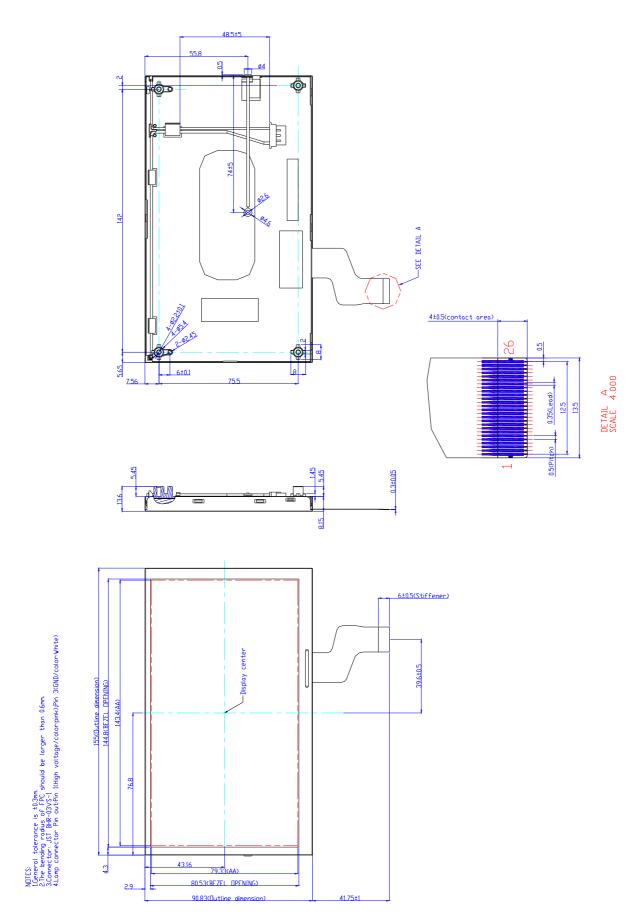


Fig.1 Outline dimension of TFT-LCD module



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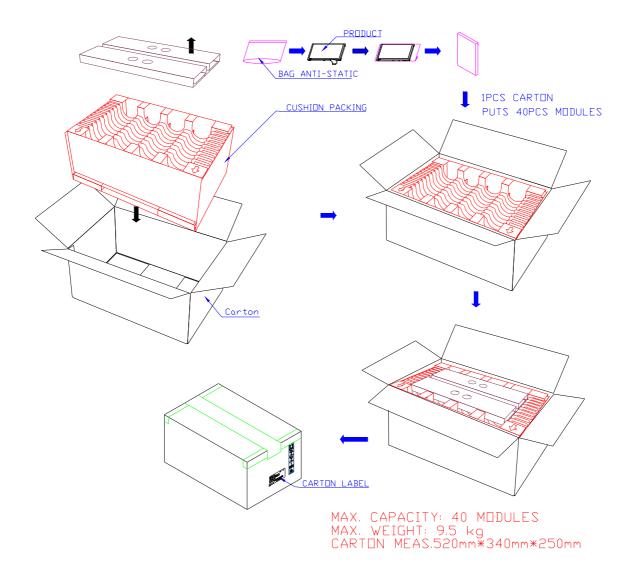
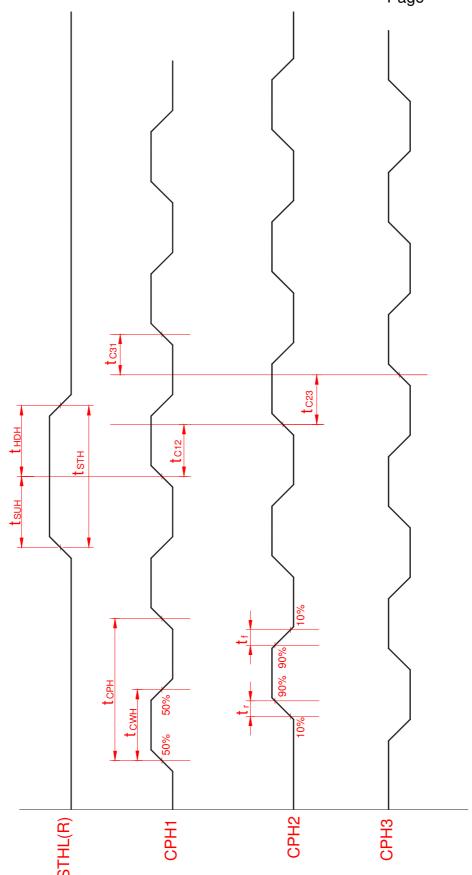


Fig. 2 Package drawing of TFT-LCD module





VNO



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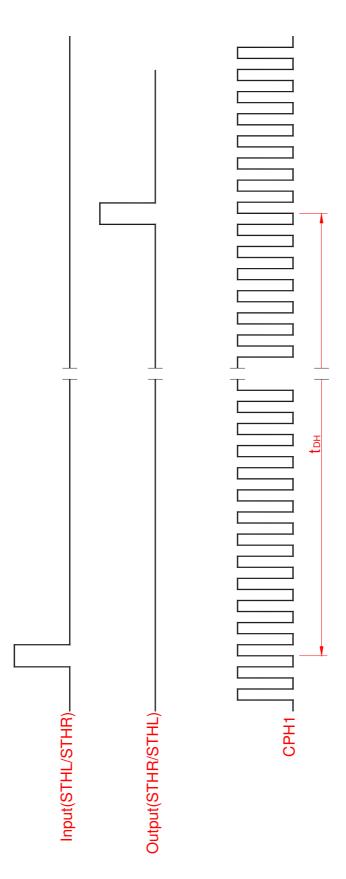
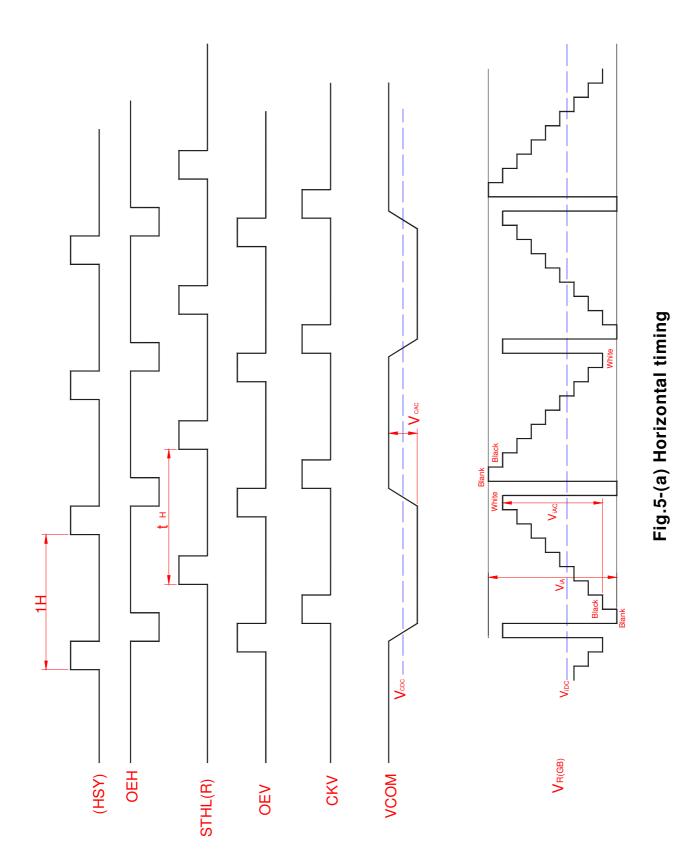


Fig.4 Horizontal display timing range



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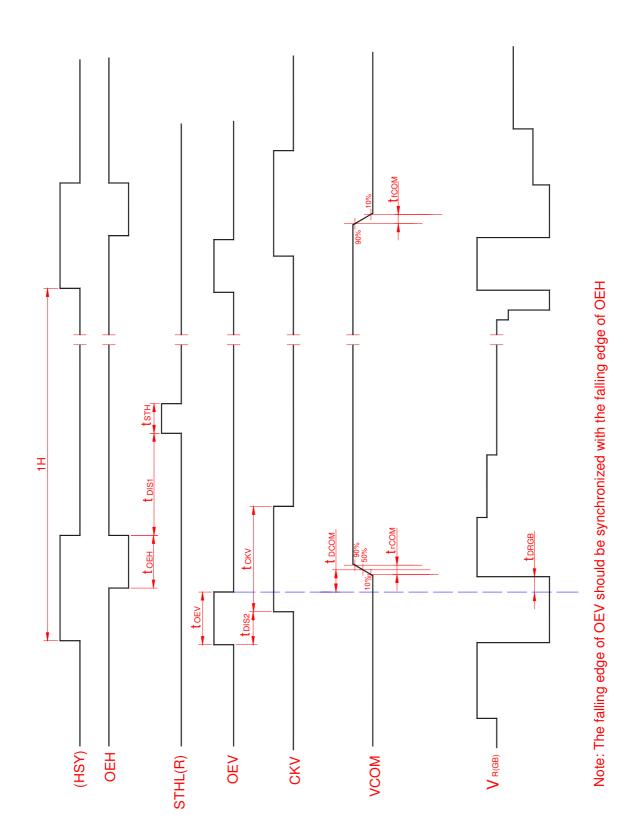


Fig.5-(b) Detail horizontal timing



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Model



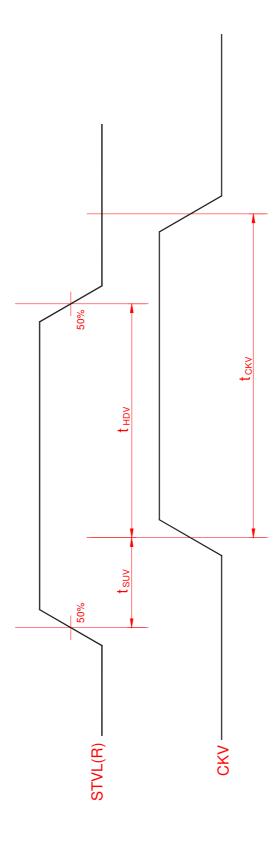


Fig.6 Vertical shift clock timing



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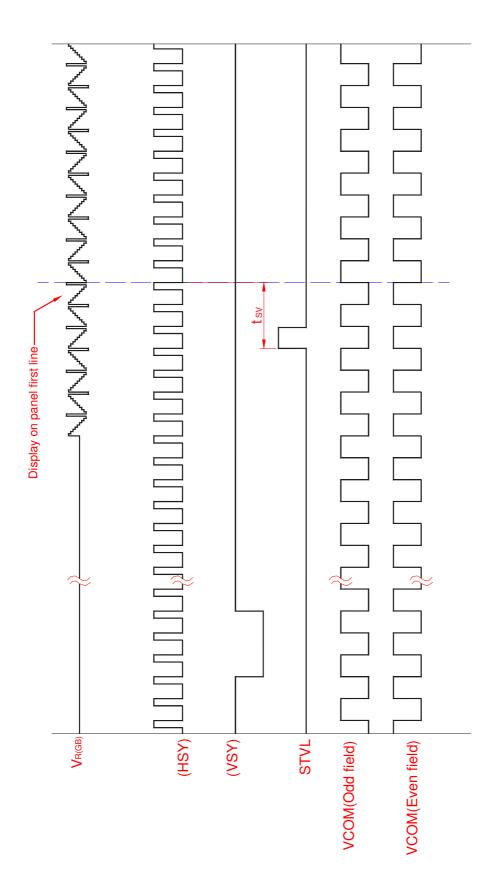


Fig.7-(a) Vertical timing (From up to down)



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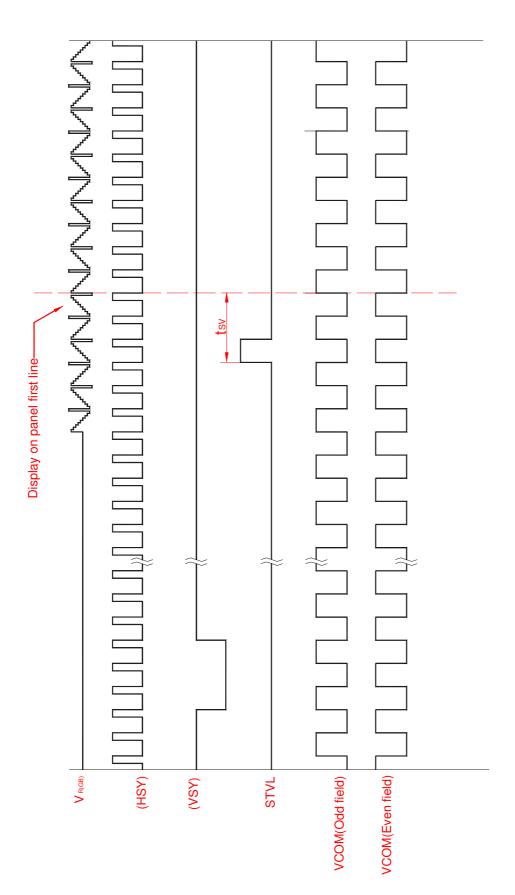


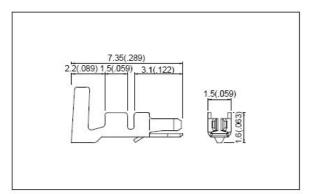
Fig.7-(b) Horizontal timing (From down to up)



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Appendix 1: CCFL connector - JST BHR(03)VS-1

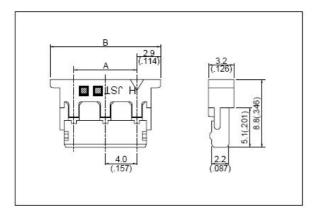
Contact ------



14-4-111-		011-11			
Model No.	mm²	AWG#	Insulation O.D. min(in.)	Q'ty / reel	
SBH-001T-P0.5	0.08 to 0.33	28 to 22	.1.6 to 2.4(063 to .094)	4,000	
	Materi	al and Fini	sh		

Note: Wire conductors should be tin-plated annealed copper strands.

Housing



Circuits	Pitch	Madel No.	Dimension	014	
	mm(in.)	Model No.	Α	В	Q'ty / bag
	4(.157)	BHR-02VS-1	4.0(.157)	9.8(.386)	1,000
2	8(.315)	BHR-03VS-1	8.0(.315)	13.8(.543)	1,000
	12(.472)	BHR-04VS-1	12.0(.472)	17.8(.701)	1,000
3	4(.157)	BHR-03VS-1	8.0(.315)	13.8(.543)	1,000
4	4(.157)	BHR-04VS-1	12.0(.472)	17.8(.701)	1,000

Nylon 66, UL94V-0, natural (white)