

Model Name: T240HW01 V0

Issue Date: 2010/07/09

(*)Preliminary Specifications

()Final Specifications

Customer Signature	Date	AUO	Date
Approved By		Approval By PM Director Frank Hsu	
Note		Reviewed By RD Director Eugene CC Chen Reviewed By Project Leader Lamy Chen Prepared By PM Claire Wang	



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Record of Revision

Version	Date	Page	Description
0.0	2010/06/14		First release
		4	Update the Haze from 11% to 3%
		6	Correct the model name and backlight source
0.1	2010/07/09	7, 16, 28	Correct the typo of backlight source
0	2010/01/00		Update the spec of color gamut
			Update the mechanical drawing
		23	Update the vibration spec
		<u> </u>	



1. General Description

This specification applies to the 24.0 inch Color TFT-LCD Module T240HW01 V0. This LCD module has a TFT active matrix type liquid crystal panel 1,920x 1,080 pixels, and diagonal size of 24.0 inch. This module supports 1,920x 1,080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T240HW01 V0 has been designed to apply the 8-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important. The LED Driver is combined into whole module.

* General Information

Items	Specification	Unit	Note
Active Screen Size	24.00	inch	
Display Area	531.36(H) x 298.89(V)	mm	
Outline Dimension	556.0(H) x 323.2 (V)	mm	Max: 16.25 Min: 8.3
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1,920x 1,080	Pixel	
Pixel Pitch	0.276 (H) x 0.276 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=3%



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

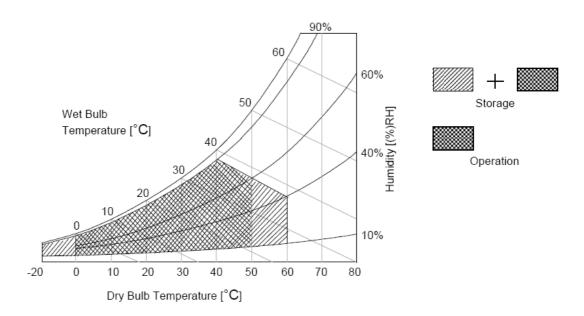
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39[°]C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at 50°C Dry condition





3. Electrical Specification

The T240HW01 V0 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input for BLU is to LED driver.

3.1 Electrical Characteristics

	Parameter	Symbol		Value		Unit	Note
	Farameter	Symbol	Min.	Тур.	Max	Offic	Note
LCD							
Power Supp	ly Input Voltage (12V model)	V_{DD}	10.8	12	13.2	V _{DC}	1
Power Supp	ly Input Current (by Product define)	I _{DD}		0.388		Α	2
Power Cons	sumption (by Product define)	Pc			4.66	Watt	2
Inrush Curre	ent (by Product define)	I _{RUSH}			0.708	Α	3
	Differential Input High Threshold Voltage	V _{TH}			+100	4	4
LVDS Interface	Differential Input Low Threshold Voltage	V _{TL}	-100		1	4	4
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V_{DC}	4
CMOS	Input High Threshold Voltage	V _{IH} (High)	2.7		3.3	V _{DC}	
Interface	Input Low Threshold Voltage	V _{IL} (Low)	0		0.6	V_{DC}	
Backlight Po	P _{BL}		28.08		Watt		
Life Time (M	ITTF)		30000			Hours	8

Note:

1. The ripple voltage should be controlled under 10% of V_{CC}

2. Test Condition:

(1) $V_{DD} = 12V$

(2) Fv = 60Hz

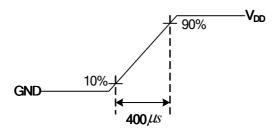
(3) $F_{CLK} = Max. Freq.$

(4) Temperature = 25 °C

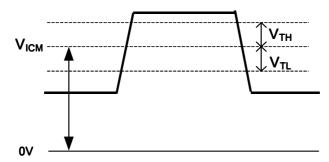
(5) Test Pattern: White Pattern

3. Measurement condition: Rising time = 400us

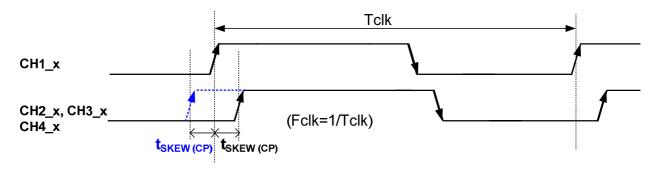




4. $V_{ICM} = 1.25V$



5. Input Channel Pair Skew Margin



- 6. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- 7. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at $Ta = 25\pm2^{\circ}C$]



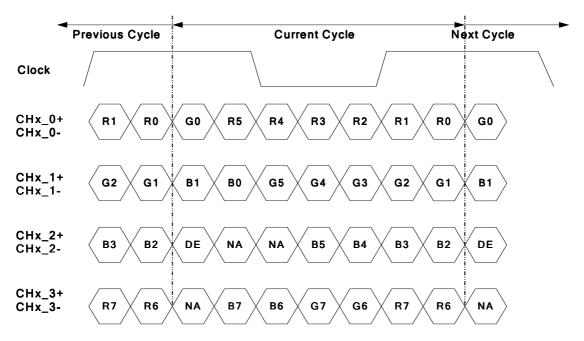
3.2 Interface Connections

• LCD connector: P-two 196334-51041-3 or equivalent

PIN	Symbol	Description	PIN	Symbol	Description
1	GND	Ground	26	GND	Ground
2	NC	No connection	27	GND	Ground
3	Reserved	AUO Internal Use Only	28	CH2_0-	LVDS Channel 2, Signal 0-
4	Reserved	AUO Internal Use Only	29	CH2_0+	LVDS Channel 2, Signal 0+
5	NC	No connection	30	CH2_1-	LVDS Channel 2, Signal 1-
6	Reserved	AUO Internal Use Only	31	CH2_1+	LVDS Channel 2, Signal 1+
7	LVDS_SEL	Open/High(3.3V) for NS, Low(GND) for JEIDA	32	CH2_2-	LVDS Channel 2, Signal 2-
8	Reserved	AUO Internal Use Only	33	CH2_2+	LVDS Channel 2, Signal 2+
9	Reserved	AUO Internal Use Only	34	GND	Ground
10	Reserved	AUO Internal Use Only	35	CH2_CLK-	LVDS Channel 2, Clock -
11	GND	Ground	36	CH2_CLK+	LVDS Channel 2, Clock +
12	CH1_0-	LVDS Channel 1, Signal 0-	37	GND	Ground
13	CH1_0+	LVDS Channel 1, Signal 0+	38	CH2_3-	LVDS Channel 2, Signal 3-
14	CH1_1-	LVDS Channel 1, Signal 1-	39	CH2_3+	LVDS Channel 2, Signal 3+
15	CH1_1+	LVDS Channel 1, Signal 1+	40	Reserved	AUO Internal Use Only
16	CH1_2-	LVDS Channel 1, Signal 2-	41	Reserved	AUO Internal Use Only
17	CH1_2+	LVDS Channel 1, Signal 2+	42	GND	Ground
18	GND	Ground	43	GND	Ground
19	CH1_CLK-	LVDS Channel 1, Clock -	44	GND	Ground
20	CH1_CLK+	LVDS Channel 1, Clock +	45	GND	Ground
21	GND	Ground	46	GND	Ground
22	CH1_3-	LVDS Channel 1, Signal 3-	47	NC	No connection
23	CH1_3+	LVDS Channel 1, Signal 3+	48	V_{DD}	Power Supply, +12V DC Regulated
24	Reserved	AUO Internal Use Only	49	V_{DD}	Power Supply, +12V DC Regulated
25	Reserved	AUO Internal Use Only	50	V_{DD}	Power Supply, +12V DC Regulated
			51	V_{DD}	Power Supply, +12V DC Regulated

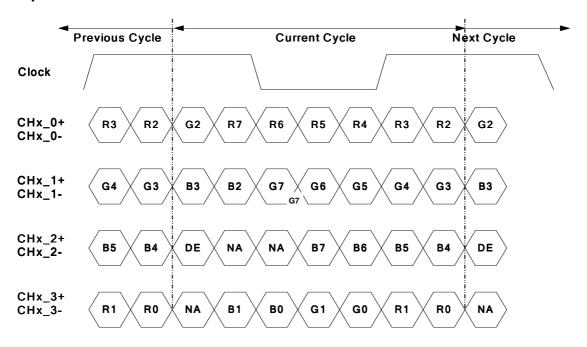


LVDS Option = High/Open→NS



Note: x = 1, 2, 3, 4...

LVDS Option = Low→JEIDA



Note: x = 1, 2, 3, 4...



3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table

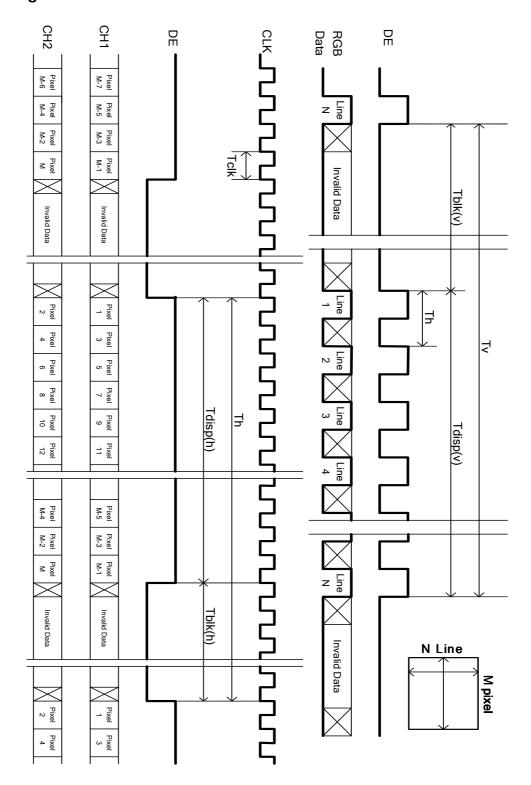
Signal	Item	Symbol	Min.	Тур.	Max	Unit		
	Period	Tv	1090	1125	1125 1480			
Vertical Section	Active	Tdisp (v)		1080				
	Blanking	Tblk (v)	10	45	400	Th		
	Period	Th	1030 1100		1325	Tclk		
Horizontal Section	Active	Tdisp (h)		Tclk				
	Blanking	Tblk (h)	70	140	365	Tclk		
Clock	Frequency	Fclk=1/Tclk	50	74.25	82	MHz		
Vertical Frequency	tical Frequency Frequency		47	60	63	Hz		
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz		

Notes:

- (1) Display position is specific by the rise of DE signal only.
 Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
- (2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3)If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.



3.4 Signal Timing Waveforms





3.5 Color Input Data Reference

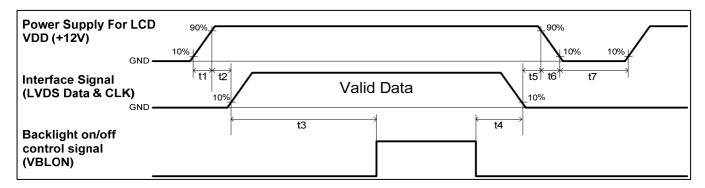
The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

		Input Color Data																							
	Color				RI	ΞD							GRI	EEN				BLUE							
	Coloi	MS	В					LS	SB	MS	В					LS	В	MS	В					LS	3B
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	ВЗ	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R																									
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
G																									
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,					90000000000000000000000000000000000000												,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			,	
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3.6 Power Sequence for LCD



		11.2			
Parameter	Min.	Type.	Max.	Unit	
t1	0.4		30	ms	
t2	0.1		50	ms	
t3	300			ms	
t4	0*1			ms	
t5	0			ms	
t6			*2 	ms	
t7	500			ms	

Note:

(1) T4=0 : concern for residual pattern before BLU turn off.

(2) T6: voltage of VDD must decay smoothly after power-off. (customer system decide this value)



3.7 Backlight Specification

The backlight unit contains 1pc light bar.

3.7.1 Electrical specification

	Item	Cum	ahal	Condition		Spec		Unit	Note
	item	Syli	nbol	Condition	Min	Тур	Max	Oilit	Note
1	Input Voltage	VD	DB	-	21.6	24	26.4	VDC	-
2	Input Current	I _D	DB	VDDB=24V	-	1.17	-	ADC	1
3	Input Power	Pc	DDB	VDDB=24V	-	28.08	-	W	1
4	Inrush Current	I _{RL}	JSH	VDDB=24V	-	-	4	ADC	2
5	On/Off control voltage	V	ON	VDDB=24V	2	-	5.5	VDC	-
5	On/Off control voltage	V_{BLON}	OFF	1 VDDD=24V	0	-	0.8	VDC	-
6	On/Off control current	I _{BL}	.ON	VDDB=24V	-	-	1.5	mA	-
7	Dimming Control Voltage	V DIM	MAX	VDDB=24V	3.0	-	3.3	VDC	-
/	Dimming Control Voltage	V_DIM	MIN	1 VDDD=24V	-	0	-	VDC	-
8	Dimming Control Current	I_DIM		VDDB=24V	-	-	2	mADC	-
9	External Dimming Ratio	DIM	/ <u>_</u> R	VDDB=24V	10	-	100	%	3

Note 1 : Dimming ratio= 100% (MAX) (Ta=25 \pm 5 $^{\circ}$ C , Turn on for 45minutes)

Note 2: Measurement condition Rising time = 20ms (VDDB : 10%~90%);

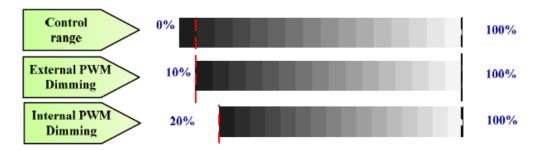
Note 3: Less than 10% dimming control is functional well and no backlight shutdown happened



3.7.2 Input Pin Assignment

LED Driver Connector: CI1114M1HR0-NH (Civilux)

Pin No	Symbol	Description
1	VDDB	Operating Voltage Supply, +24V DC regulated
2	VDDB	Operating Voltage Supply, +24V DC regulated
3	VDDB	Operating Voltage Supply, +24V DC regulated
4	VDDB	Operating Voltage Supply, +24V DC regulated
5	VDDB	Operating Voltage Supply, +24V DC regulated
6	BLGND	Ground and Current Return
7	BLGND	Ground and Current Return
8	BLGND	Ground and Current Return
9	BLGND	Ground and Current Return
10	BLGND	Ground and Current Return
11	DET	BLU status detection Normal : 0~0.8V L Abnormal : Open collector
12	VBLON	BL On-Off control: BL On: High/Open (3.3V~5.5V); BL off: Low (0~0.8V/GND)
13	NC	
14	PDIM	External PWM (10%~100% Duty, open for 100%)

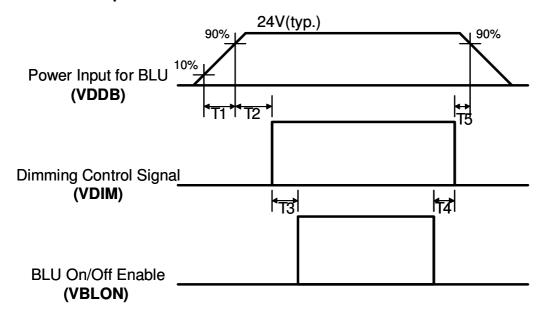


(Note*) IF External PWM function includes 10% dimming ratio. Judge condition as below:

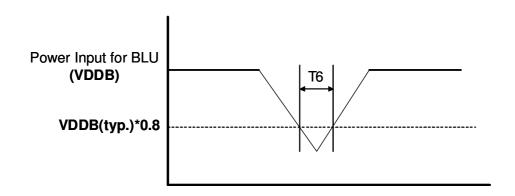
- (1) Backlight module must be lighted ON normally.
- (2) All protection function must work normally.
- (3) Uniformity and flicker could NOT be guaranteed



3.7.3 Power Sequence for LED Driver



Dip condition for LED Driver



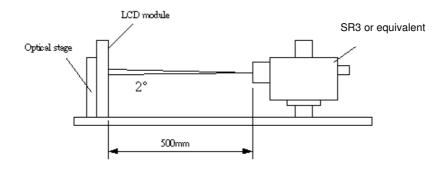
Parameter		Units		
Parameter	Min	Тур	Max	Offics
T1	20	-	-	ms
T2	500	-	-	ms
Т3	250	-	-	ms
T4	0	-	-	ms
T5	1	-	-	ms
T6	-	-	10	ms



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of ϕ and θ equal to 0° .

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter	Cumbal	Values			Unit	Notes	
Parameter	Symbol	Min.	Тур.	Max	Uffil	notes	
Contrast Ratio	CR	2400	3000			1	
Surface Luminance (White)	L _{WH}	240	300		cd/m ²	2	
Luminance Variation	δ _{WHITE(9P)}			1.3		3	
Response Time (G to G)	Тү		6.5		Ms	4	
Color Gamut	NTSC		68		%		
Color Coordinates							
Red	R_X		0.640				
	R_Y		0.330				
Green	G _X		0.281	Typ.+0.03			
	G_Y	Turn 0.00	0.590				
Blue	B _X	Typ0.03	0.150				
	B _Y		0.050				
White	W _X		0.280				
	W_{Y}		0.290				
Viewing Angle						5	
x axis, right(φ=0°)	$\theta_{\rm r}$		89		degree		
x axis, left(φ=180°)	θι		89		degree		
y axis, up(φ=90°)	θ_{u}		89		degree		
y axis, down (φ=270°)	$\theta_{\sf d}$		89		degree		

Note:



1. Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio=
$$\frac{\text{Surface Luminance of L}_{\text{on5}}}{\text{Surface Luminance of L}_{\text{off5}}}$$

- 2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When lamp current $I_H = 11 \text{mA}$. L_{WH} =Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

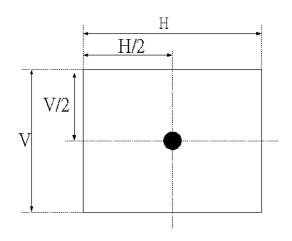
 $\delta_{WHITE(9P)} = Maximum(L_{on1},\ L_{on2}, \ldots, L_{on9}) /\ Minimum(L_{on1},\ L_{on2}, \ldots L_{on9})$

4. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_{ν} =60Hz to optimize.

Measured		Target					
Response Time		0%	25%	50%	75%	100%	
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%	
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%	
Start	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%	
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%	
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%		

4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

FIG. 2 Luminance



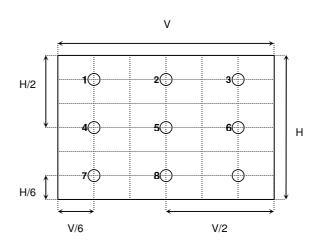




FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".

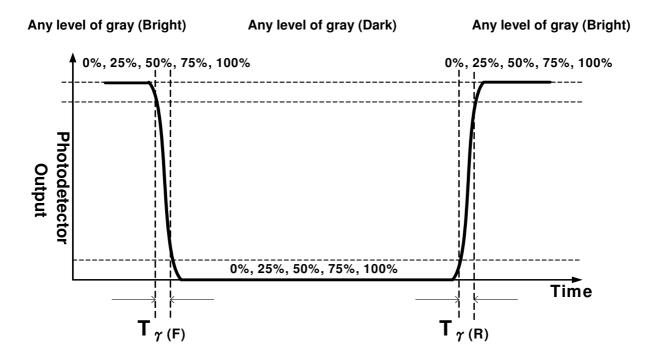
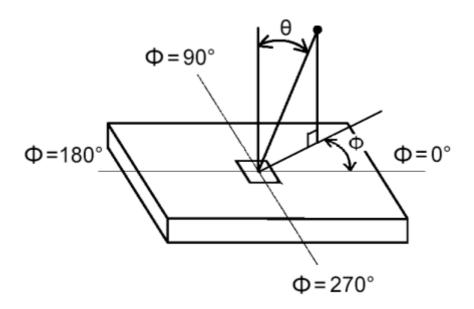


FIG.4 Viewing Angle





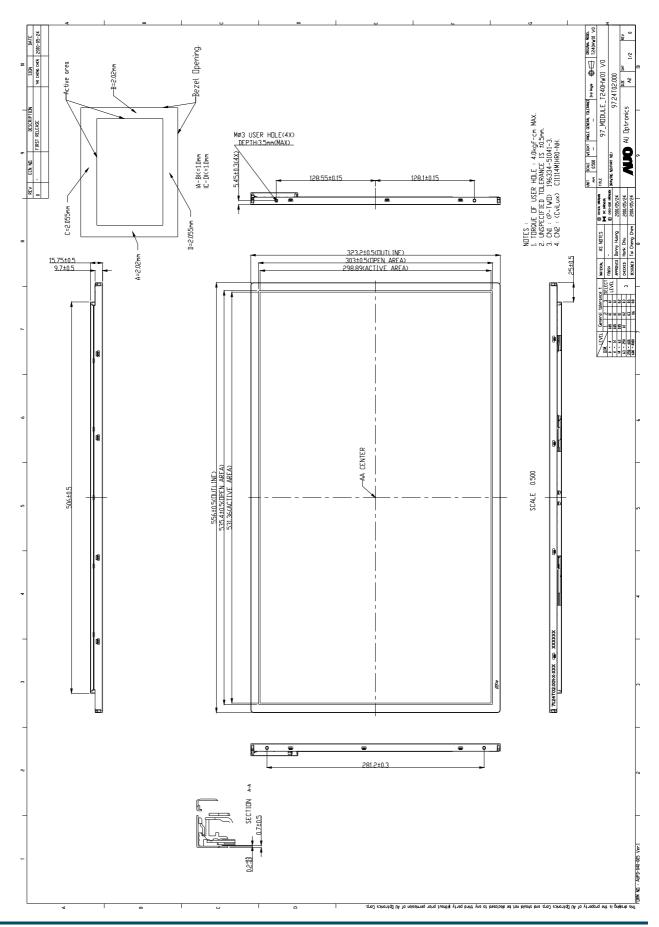
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T240HW01 V0. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	556.0 mm		
Outline Dimension	Vertical	323.2.0 mm		
	Depth	Max: 16.25mm Min: 8.3mm		
Daniel On a ring	Horizontal	535.36 mm		
Bezel Opening	Vertical	302.89 mm		
Active Display Area	Horizontal	531.36mm		
Active Display Area	Vertical	298.89 mm		
Weight	2050 g (Typ.) (TBD)			
Surface Treatment	Anti-Glare, 3H			

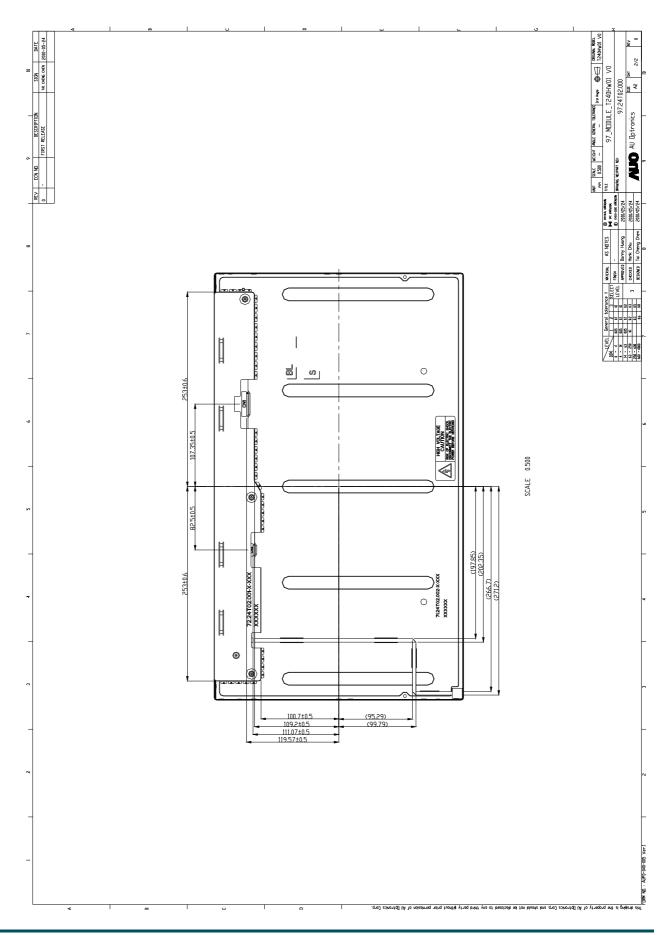


Front View





Back View





6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60°C, 300hrs
2	Low temperature storage test	3	-20°ℂ, 300hrs
3	High temperature operation test	3	50°C, 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
			Wave form: random
			Vibration level: 1.0G RMS
5	Vibration test (non-operation)	3	Bandwidth: 10-300Hz,
			Duration: X, Y, Z 30min
			One time each direction
			Shock level: 50G
6	Shock test (non-operation)	3	Waveform: half since wave, 11ms
			Direction: ±X, ±Y, ±Z, One time each direction
7	Vibration test (With carton)	9	Random wave (1.05G RMS, 10-200Hz) 30mins/ Per each X,Y,Z axes
			Height: 457mm
8	Drop test (With carton)	9	1 corner, 3 edges, 6 surfaces
			(ASTMD5276)



7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1: 2001, IEC 60065:2001; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998

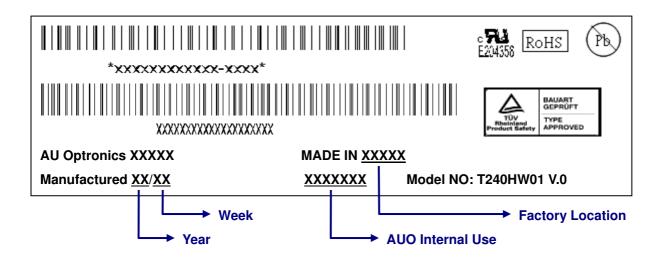


8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:





Green mark description

- (1) For Pb Free Product, AUO will add (Pb) for identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

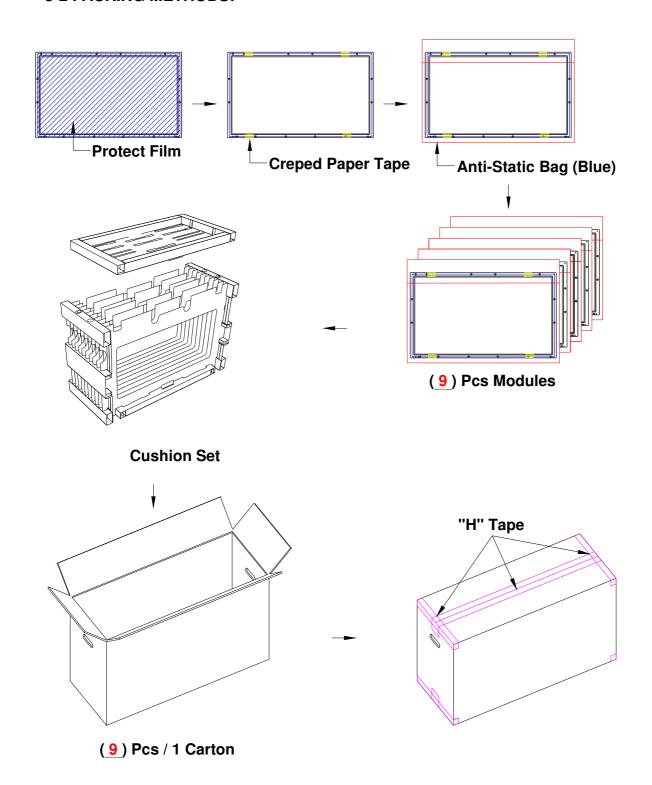
Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:





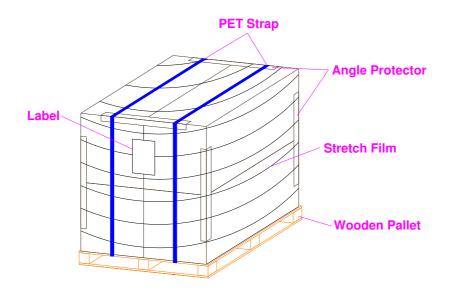
8-2 PACKING METHODS:





8-3 Pallet and Shipment Information

Item		Specification			Packing Remark	
	item	Qty.	Qty. Dimension		racking nemark	
1	Packing BOX	9 640(L) x 284(W) x 426(H) mm 25.44kg			Total Weight	
2	Pallet	1 1315(L) x 1150(W) x 132(H) mm 15kg				
3	Boxes per Pallet	40 Boxes / Pallet			(40ft HQ)	
4	Panala par Pallet				Upper Layer	
4 Panels per Pallet		216 Pcs / Pallet			Bottom Layer	
5	Pallet after packing	1315(L)	x 1150(W) x 984(H) mm	423 kg	Upper Layer	
5 Fallet	i allet after packing	1315(L) >	(1150(W) x 1410(H) mm	626 kg	Bottom Layer	





8. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of LED depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall



be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.