

Model Name: T260HW02 V1

Issue Date : 2012/01/20

()Preliminary Specifications

(*) Final Specifications

Customer Signature	Date	AUO	Date		
Approved By		Approval By PM Director			
Note		Reviewed By RD Director			
		Prepared By PM			



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		9-3	ELECTROSTATIC DISCHARGE CONTROL
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		9-5	STORAGE
9-6 HANDLING PRECAUTIONS FOR PROTECT FILM		9-6	HANDLING PRECAUTIONS FOR PROTECT FILM



Record of Revision

Version	Date	Page	Description				
0.0	2011/01/09		First release				
10	0011/01/00	4	1. Display Area:576.0(H)X324.0(V); Outline Dimension:16.2mm;				
1.0	2011/04/22	4	Pixel Pitch 0.3(H)x0.3(W)				
			3.11 Power Supply Input Current: Typ 0.52, Max 0.57.				
		6	Power Consumption: Typ 6.24, Max 6.84.				
			Inrush Current: Max 3.				
		9	3.2 Interface Connections-LCD connector: 107C51-0000RA-G4				
		9	3.2 Interface Connections-Pin 24/25/40/41 change AUO Internal use				
		5	only.				
		15	3.7.1 Backlight Specification- Input Voltage: Min 22.8, Max 25.2, Input Current: Typ 1.12, Max 1.19, Input Power: Typ 26.88, Max 28.46 On/Off control current: Max 2 V EPWM Max: Max 5.5				
		16	3.7.2 Input Pin Assignment: LED Driver Connector: CI1114M1HR0-NH				
		21	5. Mechanical Characteristics: Outline Dimension Depth 16.2mm,				
		<u></u>	Active Display Area: 576.0mm x 324.0mm				
		22	Add Front view 2D drawing.				
		23	Add Black view 2D drawing.				
			6. Reliability Test Items				
		24	Vibration test (With carton): Random wave (1.05Grms 10~200Hz),				
			Duration : X,Y,Z 10min per axes				
			Drop test (With carton): Height: 381mm (ASTM D4169)				
		28	8.3 Pallet and Shipment Information a Specification., Packing Remark , 1 Packing BOX. Bpcs/kox., 705(L)*318(W)*435(H)., 3.26., Box = 1.50kg, , 2 Pallet 1 980(L)*740(W)*132(H)., 12.4., Cushion = 1.77kg , , 3 Boxes per Pallet 6 boxes/pallet				
2.0	2011/11/25	6	3.11 Power Supply Input Current: Typ 0.31, Max 0.42.Power Consumption: Typ 3.72, Max 5.412.Backlight Power Consumption: Typ 26.88				
		16	3.7.1 Electrical specification: On/Off control voltage: 3.3; On/Off control				
		15	current:1.5; External PWM Control Voltage:3.3.				
			6. Reliability Test Items				
		24	Vibration test (non-operation): Duration: X, Y, Z 10min				
			Shock test (non-operation): Waveform: half since wave, 20ms				

友達光電
AU Optronics

T260HW02 V1 Product Specification Rev. 4.0

			nev. 4
3.0	2011/12/21	10	3.2Interface Connections
4.0	0010/01/00		Modify PIN 5,8,9,10: N.C- AUO Internal Use Only.
4.0	2012/01/20		Final spec release.
		12	Timing Table
			Period: 1090->1096; Blanking: 10->16
L			

1. General Description

This specification applies to the 26.0 inch Color TFT-LCD Module T260HW02 V1. This LCD module has a TFT active matrix type liquid crystal panel 1,920x 1,080 pixels, and diagonal size of 26.0 inch. This module supports 1,920x 1,080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T260HW02 V1 has been designed to apply the 8-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important. The LED Driver is combined into whole module.

* General Information

Items	Specification	Unit	Note
Active Screen Size	26.00	inch	
Display Area	576.0(H)X324.0(V)	mm	
Outline Dimension	613(H) X 361 (V)X16.2(D)	mm	1
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1,920x 1,080	Pixel	
Pixel Pitch	0.3 (H) × 0.3 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=11%

Note:

(1) Dmax:16.2mm (Front bezel to back bezel boss); Dmin: 10.3mm (Front bezel to Bezel back)



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

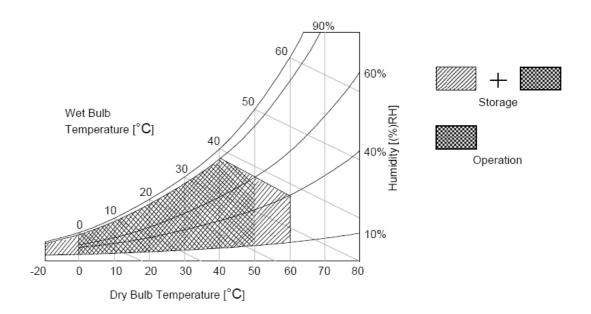
Item	Symbol	Min	Мах	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be $39^\circ\!\mathrm{C}$ and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.

Note 3: Surface temperature is measured at $50^\circ\!\mathrm{C}\,$ Dry condition





3. Electrical Specification

The T260HW02 V1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input for BLU is to LED driver.

3.1.1 Electrical Characteristics

	Parameter	Symbol		Value		Unit	Note
	Farameter	Symbol	Min.	Тур.	Max	Unit V _{DC} A Watt A mV _{DC} V _{DC} V _{DC} V _{DC} V _{DC}	NOLE
LCD							
Power Sup	pply Input Voltage	V _{DD}	10.8	12	13.2	V _{DC}	1
Power Sup	oply Input Current	I _{DD}		0.31	0.41	А	2
Power Cor	nsumption	Pc		3.72	5.412	Watt	2
Inrush Cur	rent	I _{RUSH}			3	А	3
	Differential Input High Threshold Voltage	V_{TH}	+100		+300	mV_{DC}	4
LVDS Interface	Differential Input Low Threshold Voltage	V _{TL}	-300		-100	mV_{DC}	4
	Input Common Mode Voltage	V _{ICM}	1.1	1.25	1.4	V_{DC}	4
CMOS	Input High Threshold Voltage	V _{IH} (High)	2.7		3.3	V _{DC}	
Interface	Input Low Threshold Voltage	V _{IL} (Low)	0		0.6	V _{DC}	
Backlight I	Backlight Power Consumption			26.88		Watt	
Life Time (MTTF)		30000			Hours	7

3.1.2: AC Characteristics

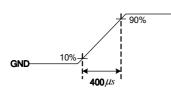
Parameter		Symbol		Value	Linit	Note	
	Falameter	Symbol	Min.	Тур.	Max	- Unit MHz KHz ns	NOLE
	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%		Fclk +3%	MHz	8
LVDS	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30		200	KHz	8
Interface	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	-0.4 -0.5		0.4 0.5	ns	9

Note :

- 1. The ripple voltage should be controlled under 10% of $V_{\mbox{\tiny CC}}$
- 2. Test Condition:
 - (1) $V_{DD} = 12V$
 - (2) Fv = 60Hz
 - (3) $F_{CLK} = Max$. Freq.
 - (4) Temperature = 25 $^{\circ}C$
 - (5) Test Pattern : White Pattern

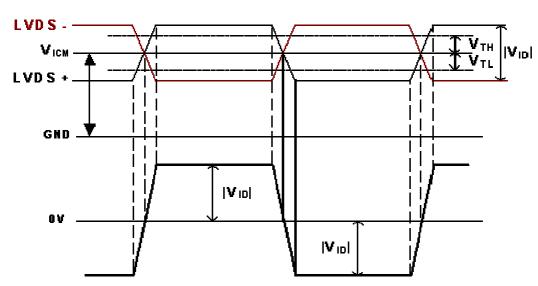


3. Measurement condition : Rising time = 400us



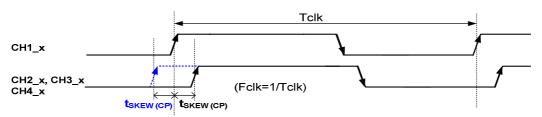
4. V_{ICM}

1.25V

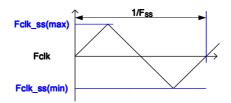


VDD

5. Input Channel Pair Skew Margin



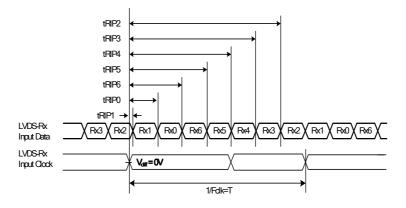
- 6. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- 7. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at Ta = $25\pm2^{\circ}$ C]
- 8. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures





9. Receiver Data Input Margin

Parameter	Cymphol		Rating		Unit	Note
Parameter	Symbol	Min	Туре	Max	Unit	Note
Input Clock Frequency	Fclk	Fclk (min)		Fclk (max)	MHz	T=1/Fclk
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns	
Input Data Position1	tRIP0	T/7- tRMG	T/7	T/7+ tRMG	ns	
Input Data Position2	tRIP6	2T/7- tRMG	2T/7	2T/7+ tRMG	ns	
Input Data Position3	tRIP5	3T/7- tRMG	3T/7	3T/7+ tRMG	ns	
Input Data Position4	tRIP4	4T/7- tRMG	4T/7	4T/7+ tRMG	ns	
Input Data Position5	tRIP3	5T/7- tRMG	5T/7	5T/7+ tRMG	ns	
Input Data Position6	tRIP2	6T/7- tRMG	6T/7	6T/7+ tRMG	ns	





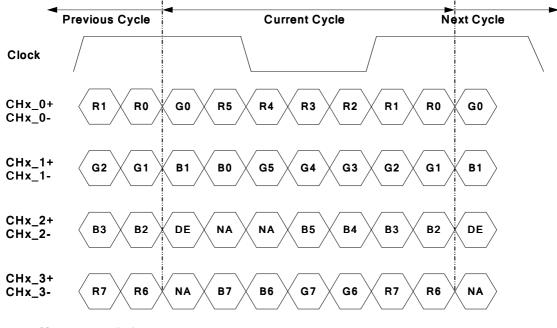
3.2 Interface Connections

• LCD connector: 107C51-0000RA-G4

PIN	Symbol	Description	PIN	Symbol	Description
1	N.C.	AUO Internal Use Only	26	GND	Ground
2	N.C.	AUO Internal Use Only	27	GND	Ground
3	N.C.	AUO Internal Use Only	28	CH2_0-	LVDS Channel 2, Signal 0-
4	N.C.	AUO Internal Use Only	29	CH2_0+	LVDS Channel 2, Signal 0+
5	N.C.	AUO Internal Use Only	30	CH2_1-	LVDS Channel 2, Signal 1-
		Panel Rotation Display Control			
6	ROTATE	High(3.3V) : Rotate Enable	31	CH2_1+	LVDS Channel 2, Signal 1+
		Open/Low(GND) : Rotate Disable			
7	LVDS_SEL	Open/High(3.3V) for NS,	32	CH2_2-	LVDS Channel 2, Signal 2-
'	LVDO_OLL	Low(GND) for JEIDA	52	0112_2-	
8	N.C.	AUO Internal Use Only	33	CH2_2+	LVDS Channel 2, Signal 2+
9	N.C.	AUO Internal Use Only	34	GND	Ground
10	N.C.	AUO Internal Use Only	35	CH2_CLK-	LVDS Channel 2, Clock -
11	GND	Ground	36	CH2_CLK+	LVDS Channel 2, Clock +
12	CH1_0-	LVDS Channel 1, Signal 0-	37	GND	Ground
13	CH1_0+	LVDS Channel 1, Signal 0+	38	CH2_3-	LVDS Channel 2, Signal 3-
14	CH1_1-	LVDS Channel 1, Signal 1-	39	CH2_3+	LVDS Channel 2, Signal 3+
15	CH1_1+	LVDS Channel 1, Signal 1+	40	N.C.	AUO Internal Use Only
16	CH1_2-	LVDS Channel 1, Signal 2-	41	N.C.	AUO Internal Use Only
17	CH1_2+	LVDS Channel 1, Signal 2+	42	GND	Ground
18	GND	Ground	43	GND	Ground
19	CH1_CLK-	LVDS Channel 1, Clock -	44	GND	Ground
20	CH1_CLK+	LVDS Channel 1, Clock +	45	GND	Ground
21	GND	Ground	46	GND	Ground
22	CH1_3-	LVDS Channel 1, Signal 3-	47	N.C.	No connection
23	CH1_3+	LVDS Channel 1, Signal 3+	48	V _{DD}	Power Supply, +12V DC Regulated
24	N.C.	AUO Internal Use Only	49	V _{DD}	Power Supply, +12V DC Regulated
25	N.C.	AUO Internal Use Only	50	V _{DD}	Power Supply, +12V DC Regulated
			51	V _{DD}	Power Supply, +12V DC Regulated

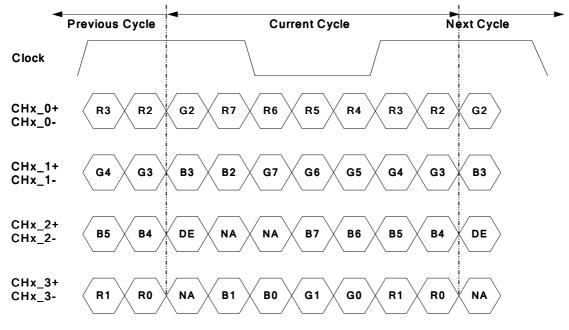


LVDS Option = High/Open→NS



Note: x = 1, 2, 3, 4...

LVDS Option = Low->JEIDA



Note: x = 1, 2, 3, 4...



3.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table

Signal	Item	Symbol	Min.	Тур.	Max	Unit
Vertical Section	Period	Τv	1096	1125	1480	Th
	Active	Tdisp (v)		1080		Th
	Blanking	Tblk (v)	16	45	400	Th
	Period	Th	1030	1100	1325	Tclk
Horizontal Section	Active	Tdisp (h)	960			Tclk
	Blanking	Tblk (h)	70	140	365	Tclk
Clock	Frequency	Fclk=1/Tclk	50	74.25	82	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz

Notes:

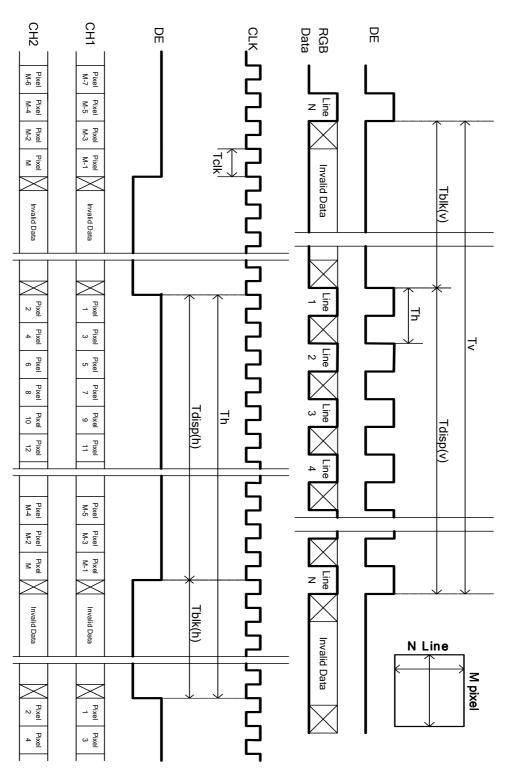
(1) Display position is specific by the rise of DE signal only.

Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

- (2)Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3) If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.



3.4 Signal Timing Waveforms





3.5 Color Input Data Reference

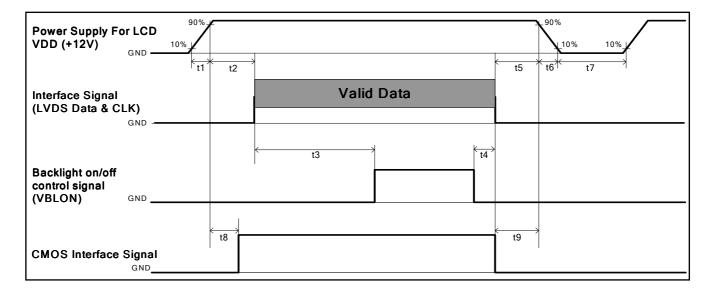
The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

											I	npu	t Co	lor	Data	a									
	Color				R	ED							GRI	EEN							BL	UE			
	00101		В					LS	SB	MS	В					LS	BB	MS	В					LS	SB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R																									
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
G																									
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В																									
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

COLOR DATA REFERENCE



3.6 Power Sequence for LCD



Deveneeter		11		
Parameter	Min.	Туре.	Max.	Unit
t1	0.4		30	ms
t2	0.1		50	ms
t3	450			ms
t4	0 ^{*1}			ms
t5	0			ms
t6			*2	ms
t7	500			ms
t8	10 ^{*3}		50	ms
t9	0			ms

Note:

(1) T4=0 : concern for residual pattern before BLU turn off.

(2) T6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)



3.7 Backlight Specification

The backlight unit contains 1pc light bar.

3.7.1 Electrical specification

	Itom	Item Symbol		Condition	-	Spec		Unit	Note
	nem			Condition	Min	Тур	Мах	Onit	Note
1	Input Voltage	VDDB		-	22.8	24	25.2	VDC	-
2	Input Current	I _D	DB	VDDB=24V	-	1.12	1.19	ADC	1
3	Input Power	Pc	DB	VDDB=24V	-	26.88	28.46	W	1
4	Inrush Current	I _{RL}	JSH	VDDB=24V			4	ADC	2
5		V	ON	VDDB=24V	2	-	3.3	VDC	-
5	On/Off control voltage	V _{BLON}	OFF		0	-	0.8	VDC	3-
6	On/Off control current	I _{BLON}		VDDB=24V	-	-	1.5	mA	-
7	External PWM	V EPWM	MAX	VDDB=24V	2	-	3.3	VDC	-
<i>'</i>	Control Voltage		MIN	VDDB=24V	0	-	0.8	VDC	-
8	External PWM Control Current	I_EF	WM	VDDB=24V	-	-	2	mADC	-
9	External PWM Duty ratio	D_EI	PWM	VDDB=24V	5	-	100	%	4
10	External PWM Frequency	F_EF	F_EPWM		140	180	240	Hz	-
11		DET	н	VDDB=24V	Ope	en Colle	ctor	VDC	5
	DET status signal	DET	Lo	VUUB=24V	0	-	0.8	VDC	5
12	Input Impedance	R	in	VDDB=24V	300			Kohm	-

Note 1 : Dimming ratio= 100% (MAX) (Ta=25±5°C, Turn on for 45minutes)

Note 2: Measurement condition Rising time = 20ms (VDDB : 10%~90%);

Note 3: When BLU off (VDDB = 24V , VBLON = 0V) , IDDB (max) = 0.02A

Note 4: Less than 5% dimming control is functional well and no backlight shutdown happened

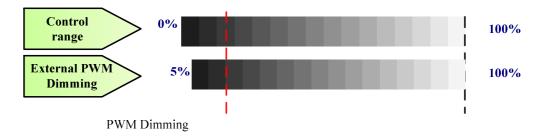
Note 5: Normal : 0~0.8V ; Abnormal : Open collector



3.7.2 Input Pin Assignment

LED Driver Connector: CI1114M1HR0-NH

Pin	Symbol	Description
1	VDDB	Operating Voltage Supply, +24V DC regulated
2	VDDB	Operating Voltage Supply, +24V DC regulated
3	VDDB	Operating Voltage Supply, +24V DC regulated
4	VDDB	Operating Voltage Supply, +24V DC regulated
5	VDDB	Operating Voltage Supply, +24V DC regulated
6	BLGND	Ground and Current Return
7	BLGND	Ground and Current Return
8	BLGND	Ground and Current Return
9	BLGND	Ground and Current Return
10	BLGND	Ground and Current Return
11	DET	BLU status detection: Normal : 0~0.8V ; Abnormal : Open collector (Recommend Pull high R > 10K, VDD = 3.3V)
12	VBLON	BLU On-Off control: High/Open (2~5.5V) : BL On ; Low (0~0.8V/GND) : BL Off
13	NC	NC
14	PDIM(*)	External PWM (5%~100% Duty, open for 100%)

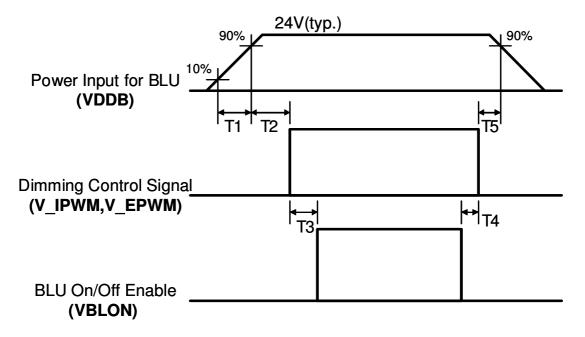


(Note*) IF External PWM function less than 5 % dimming ratio. Judge condition as below:

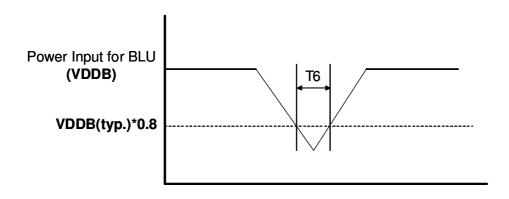
- (1) Backlight module must be lighted ON normally.
- (2) All protection function must work normally.
- (3) Uniformity and flicker could NOT be guaranteed



3.7.3 Power Sequence for LED Driver



Dip condition for LED Driver



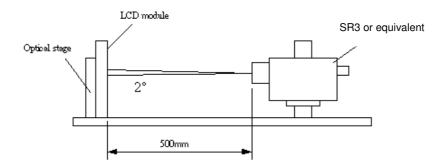
Dexemptor		Value		Units
Parameter	Min	Тур	Мах	Units
T1	20	-	-	ms
T2	500	-	-	ms
Т3	250	-	-	ms
T4	0	-	-	ms
T5	1	-	-	ms
Т6	-	-	10	ms



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of ϕ and θ equal to 0 °.

Fig.1 presents additional information concerning the measurement equipment and method.



	Devemeter	Cumhal		Values	- Unit	Notes		
	Parameter	Symbol	Min.	Тур.	Max	Unit	NOLES	
Contras	t Ratio	CR	2400	3000			1	
Surface	Luminance (White)	L _{WH}	240	300		cd/m ²	2	
Luminar	nce Variation	δ _{WHITE(9P)}			1.33		3	
Respon	se Time (G to G)	Тγ		8		Ms	4	
Color Ga	amut	NTSC		68		%		
Color Co	oordinates							
	Red	R _X		0.62				
		R _Y		0.33				
	Green	G _X		0.33				
		G _Y	T . 0.00	0.62	T . 0.00			
	Blue	B _X	Тур0.03	0.15	Тур.+0.03			
		B _Y		0.04				
	White	W _X		0.28			• • • • • • • • • • • • • • • • • • • •	
	\	W _Y		0.29				
Viewing	Angle						5	
	x axis, right(φ=0°)	θ _r		89		degree		
	x axis, left(φ=180°)	θι		89		degree		
	y axis, up(φ=90°)	θ _u		89		degree		
	y axis, down (φ=270°)	θ _d		89		degree		





Note:

1. Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio= $\frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$

- Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED input current =1.07A. L_{WH}=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance, δ WHITE is defined (center of Screen) as:

 $\delta_{WHITE(9P)} = Maximum(L_{on1}, L_{on2}, ..., L_{on9}) / Minimum(L_{on1}, L_{on2}, ... L_{on9})$

4. Response time T γ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_v=60Hz to optimize.

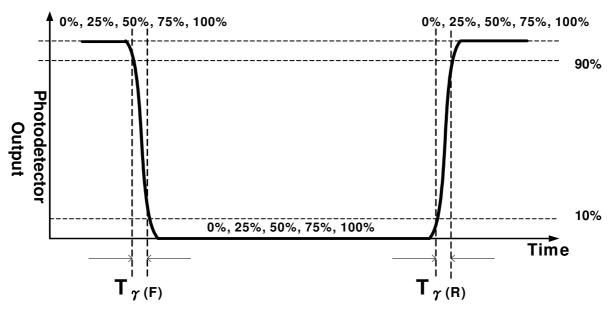
Measured				Target		
Response Time		0%	25%	50%	75%	100%
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
Start	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".

Any level of gray (Bright)

Any level of gray (Dark)

Any level of gray (Bright)



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.



FIG. 2 Luminance

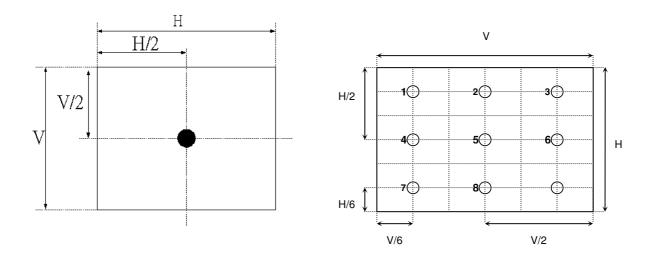
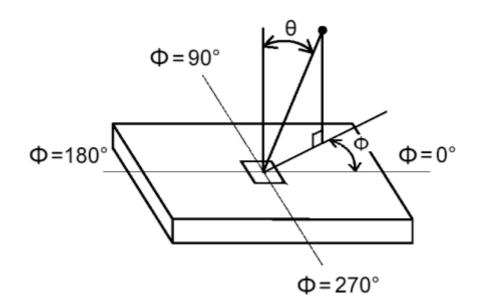


FIG.3 Viewing Angle





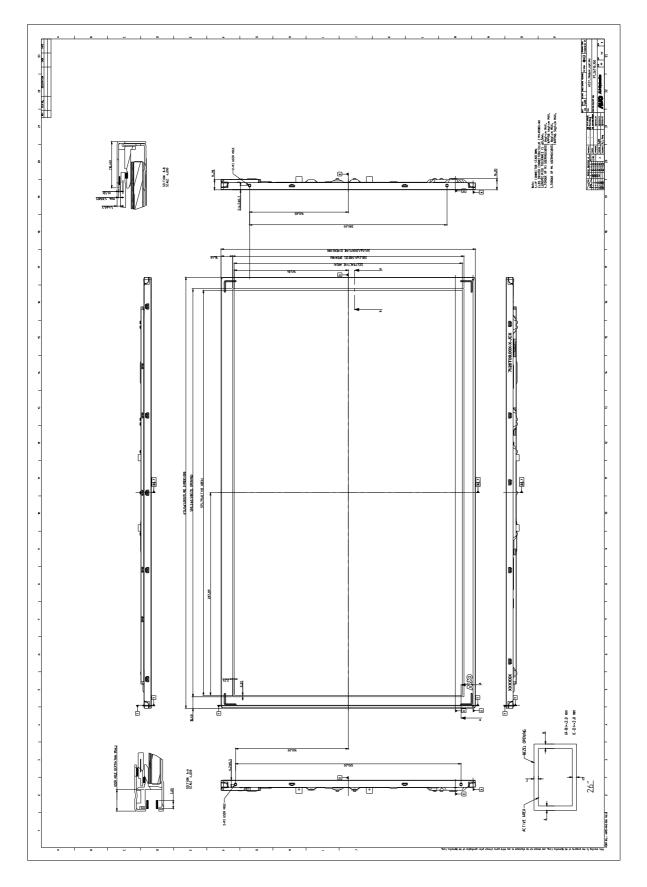
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T260HW02 V1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	613 mm			
	Vertical	361 mm			
Outline Dimension	Depth	16.2 mm (Front bezel to back bezel boss)			
Densi Orenian	Horizontal	580.2mm			
Bezel Opening	Vertical	328.2mm			
Active Display Area	Horizontal	576.0mm			
Active Display Area	Vertical	324.0mm			
Weight	3400 g				
Surface Treatment	Anti-Glare, 3H				

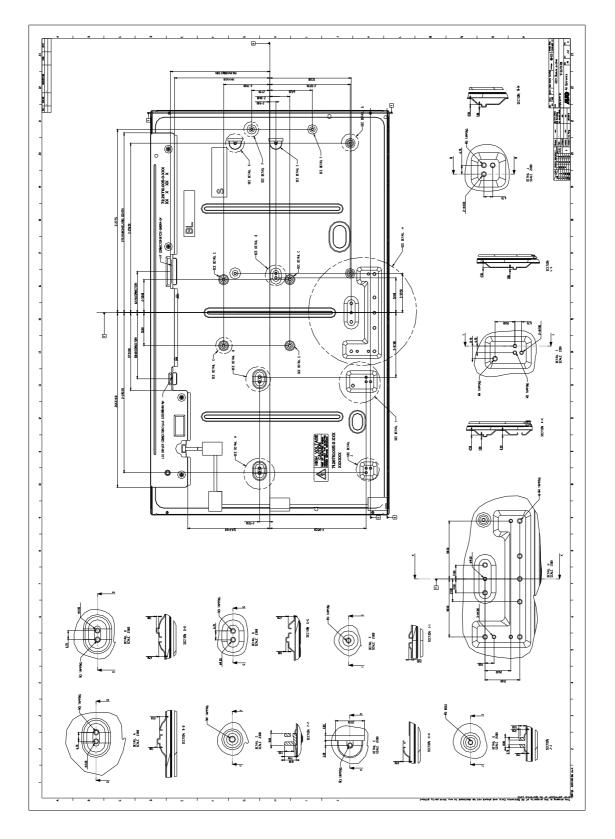


Front View





Back View





6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60℃, 300hrs
2	Low temperature storage test	3	-20℃, 300hrs
3	High temperature operation test	3	50°C , 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
			Wave form: random
			Vibration level: 1.0G RMS
5	Vibration test (non-operation)	3	Bandwidth: 10-300Hz,
			Duration: X, Y, Z 10min
			One time each direction
			Shock level: 50G
6	Shock test (non-operation)	3	Waveform: half since wave, 20ms
			Direction: $\pm X$, $\pm Y$, $\pm Z$, One time each direction
			Random wave (1.05Grms 10~200Hz)
7	Vibration test (With carton)	1CTN/8PCS	Duration : X,Y,Z 10min per axes
			Height: 381mm (ASTM D4169)
0	Drop toot (With corton)		,
8	Drop test (With carton)		1 corner, 3 edges, 6 surfaces
			(ASTM D5276)



7. International Standard

7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1 : 2001, IEC 60065:2001 ; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7.2 EMC

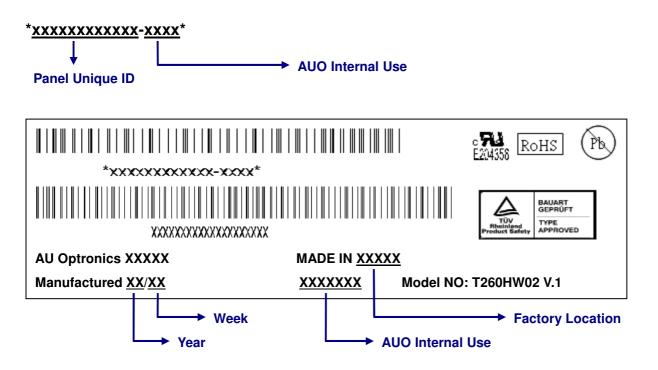
- ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



8. Packing

8-1 DEFINITION OF LABEL:

A. Panel Label:

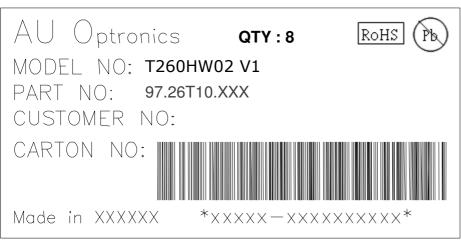


Green mark description

- (1) For Pb Free Product, AUO will add (Pb) for identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

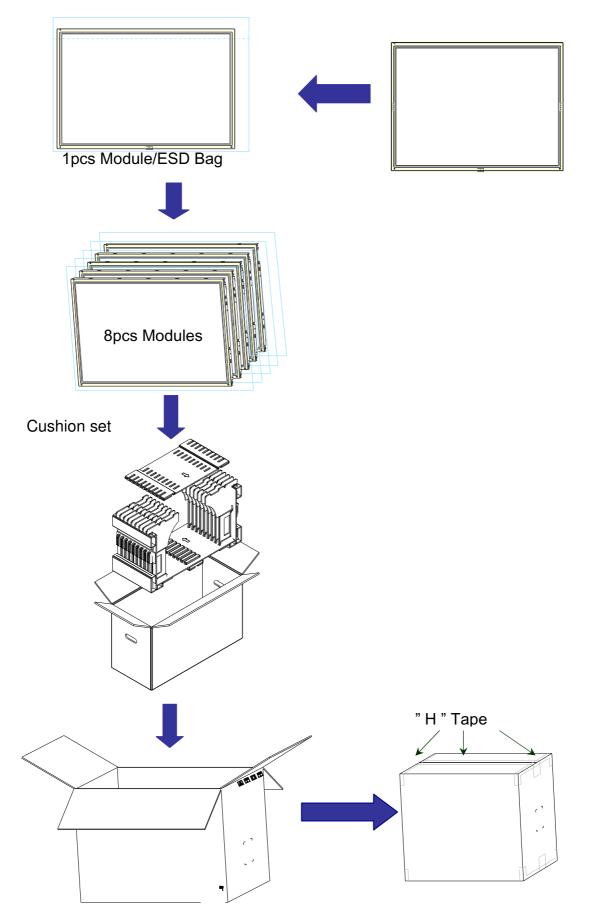
Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:





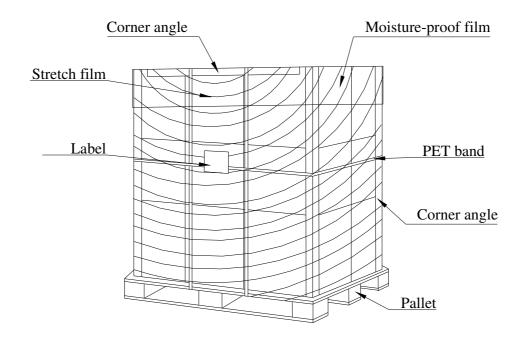
8-2 PACKING METHODS:





8-3 Pallet and Shipment Information

	ltem		Packing Remark			
	nem	Qty.	Dimension	Weight (kg)		
1	Packing BOX	8pcs/box	705(L)*318(W)*435(H)	3.26	Box = 1.50kg	
2	Pallet	1	980(L)*740(W)*132(H)	12.4	Cushion = 1.77kg	
3	Boxes per Pallet	6 boxes/pallet				
4	Panels per Pallet	48pcs/pallet				
	Pallet after packing (40' container)	72	980(L)*740(W)*1002(H)	193.5		





9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of LED depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall



be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5° C and 35° C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.