Product Description:							
AUO Model Name: T315XW01 V2							
Customer Part No/Project N	Customer Part No/Project Name:						
Customer Signature	Date	AUO	Date				
Customer Signature	Date	AUO Approved By:	Date				
Customer Signature	Date		Date				

Please return one copy with your signature and comments for

our confirmation.

AU Optronics Corporation

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Product Specifications

31.5" WXGA Color TFT-LCD Module Model Name: T315XW01 V2

() Preliminary Specifications(*) Final Specifications

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Version	Date	No	Old Description	New Description	Remark
0.1	2005/3/14	All		All	First Issued
1.0	2005/5/4	1	Preliminary specs	Final specs	Update
1.1	2005/6/6	15	G/G response time max : 35ms	20ms	Update

Record of Revision

1. General Description

This specification applies to the 31.51 inch Color TFT-LCD Module T315XW01 V2. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 31.51 inch. This module supports 1366x768 XGA-WIDE mode (Non-interlace). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T315XW01 V2 has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

Items	Specification	Unit	Note
Active Screen Size	31.51 inches		
Display Area	697.68 (H) x 392.26(V)	mm	
Outline Dimension	Dimension 760.0(H) x 450.0(V) x 47(D)		With inverter
Driver Element	a-Si TFT active matrix		
Display Colors	16.7M	Colors	
Number of Pixels	1366 x 768	Pixel	
Pixel Pitch	0.51075	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Surface Treatment	AG, 3H		

* General Information

2. Absolute Maximum Ratings

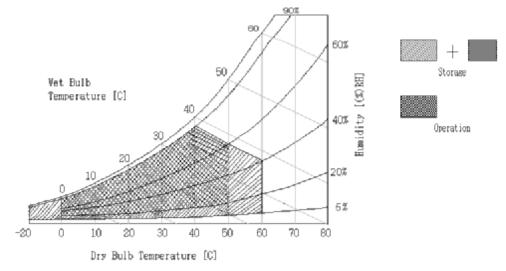
The following are maximum values which, if exceeded, may cause permanent damage to the unit.

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	5.5	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	3.6	[Volt]	Note 1
BLU Input Voltage	VDDB	-0.3	27	[Volt]	Note 1
BLU Brightness Control Voltage	BLON	-0.3	7.0	[Volt]	Note 1
Operating Temperature	ТОР	0	+50	[°C]	Note 2
Operating Humidity	НОР	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2

Note 1 : Duration = 50msec

Note 2 : Maximum Wet-Bulb should be 39° C and No condensation.

Note 3 :The relative humidity must not exceed 90% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C.



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3. Electrical Specification

The T315XW01 V2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the BLU, is to power inverter..

Parameter	Symbol		Values			Notes
		Min	Тур	Max		
LCD:						
Power Supply Input Voltage	Vcc	4.5	5.0	5.5	Vdc	
Power Supply Input Current	Icc	-	1.5	1.8	А	1
Power Consumption	Pc	-	7.5	9	Watt	1
Inrush Current	I _{RUSH}	2.5	-	4.5	Apeak	2
Backlight Power Consumption	Pbl		115	132	Watt	
Life Time		60,000			Hours	3

3-1 Electrical Characteristics

Note :

- 1. Vcc=5.0V, $f_v = 60$ Hz, fCLK=81.5Mhz, 25°C,
- **2.** Duration = 1 ms
- 3. The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.
- 4. Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.
- 5. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of CCFL will drop and the life time of CCFL will be reduced.

3-2 Interface Connections

- LCD connector (CN3): JAE FI-E30S or equivalent
- LVDS Transmitter: SN75LVDS83(Texas Instruments) or equivalent

Note:

1. All GND (ground) pins should be connected together and should also be connected to the LCD's metal frame. All Vcc (power input) pins should be connected together.

No	Signal	No	Signal
1	<i>N.C.</i>	16	GND
2	<i>N.C.</i>	17	RIN3-
3	N.C.	18	RIN3+
4	GND	19	GND
5	RIN0-	20	Reserved.
6	RIN0+	21	LVDS Option
7	GND	22	Rotate Option
8	RIN1-	23	GND
9	RIN1+	24	GND
10	GND	25	GND
11	RIN2-	26	Vdd (+5V)
12	RIN2+	27	Vdd (+5V)
13	GND	28	Vdd (+5V)
14	RxCLK-	29	Vdd (+5V)
15	RxCLK+	30	Vdd (+5V)

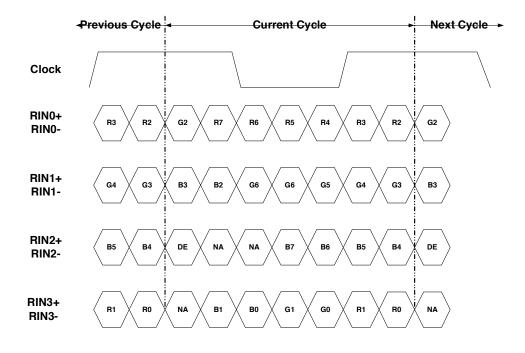
** LVDS Option : H (3.3V) or NC Ł NS

L(GND) Ł JEIDA

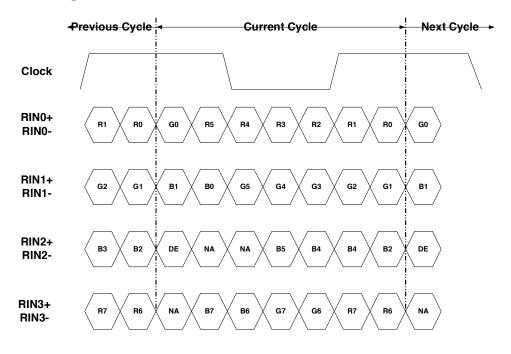
** Rotate Option : H (3.3V) Ł U/D, R/L rotate

L(GND) Ł Normal

LVDS Option = L (GND)



LVDS Option = H(3.3V)



BACKLIGHT CONNECTOR	PIN CONFIGURATION -
----------------------------	---------------------

Item	Sym	h	Condition	Spec			Unit	
Item	Sym	D.	Condition	Min	Тур	Max	Om	
Input Voltage	VDDB			21.6	24.0	26.4	VDC	
Input Current	IDD	В	VDDB=24V Dimming MAX	4.3	4.8	5.3	ADC	
Input Power	PDD	В	VDDB=24V Dimming MAX		115	132	W	
Inrush Current, 0.3ms	IRUSH		VDDB=24V Dimming MAX			6.0	ADC	
Output Frequency	FBI	FBL		63.0	66.0	69.0	KHz	
On/Off Control Voltage	VBLON	ON	VDDB=24V	3.0		5.0	VDC	
On/On Control Voltage	VBLON	OFF	VDDB=24V	-0.3		0.8	VDC	
On/Off Control Current	IBLO	N	VDDB=24V	-1.0		1.5	mADC	
Dimming Control Voltage	I VPWM	MAX	VDDB=24V		0		VDC	
Dimining Control Voltage		MIN	VDDB=24V		5.0		VDC	
Dimming Control Current	I_DIM	MIN	VDDB=24V			1.5	mADC	
External PWM Control Voltage	E PWM	MAX		3.0		5.0	VDC	
External P w M Control Voltage	E_P w W	MIN		-0.3		0.8	VDC	
External PWM Control Current	E_IPW	M				1.5	mADC	
External PWM Duty Ratio	E_DPV	VM		50		100	%	
External PWM Frequency	E_FPV	VM		150		300	Hz	

1 · Electrical specification

(Ta=25 \pm 5°C, Turn on for 45minutes)

2 · Input specification

CN1: JST JST PHR-14

No	Function	Description
1	VDDB (Main Power)	DC input 24.0 VDC
2	VDDB (Main Power)	DC input 24.0 VDC
3	VDDB (Main Power)	DC input 24.0 VDC
4	VDDB (Main Power)	DC input 24.0 VDC
5	VDDB (Main Power)	DC input 24.0 VDC
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
10	GND	Ground
11	VDIM (LCD Bright)	Brightness control signal input (0~5.0V)
		(GND : Maximum brightness)
12	BLON (Enable voltage)	TTL, H : ON, L : Off
13	E_PWM	External PWM input for dimming control
14	GND	Ground

3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

* Timing Table

DE only Mode

Vertical Frequency Range A

Signal	Item	Symbol	Min	Туре	Max	Unit
	Period	Τv	789	806	822	Th
		Tdisp				
Vertical	Active	(v)		768		Th
Section	Blanking	Tblk (v)	21	38	54	Th
	Period	Th	1414	1560	1722	Tclk
		Tdisp				
Horizontal	Active	(h)		1366		Tclk
Section	Blanking	Tblk (h)	48	194	356	Tclk
Clock	Frequency	1/Tclk	65	76	88	MHz
Vertical Frequency	Frequency	Freq	58	60	62	Hz
Horizntal Frequency	Frequency	Freq	47.32		49.32	KHz

Vertical Frequency Range B

Signal	Item	Symbol	Min	Туре	Max	Unit
	Period	Τv	789	806	822	Th
ΙΓ		Tdisp				
Vertical	Active	(v)		768		Th
Section	Blanking	Tblk (v)	21	38	54	Th
	Period	Th	1414	1560	1722	Tclk
Ι Γ		Tdisp				
Horizontal	Active	(h)		1366		Tclk
Section	Blanking	Tblk (h)	48	194	356	Tclk
Clock	Frequency	1/Tclk	54	63	74	MHz
Vertical Frequency	Frequency	Freq	48	50	52	Hz
Horizntal Frequency	Frequency	Freq	39.45		41.10	KHz

1.) Display position is specific by the rise of DE signal only.

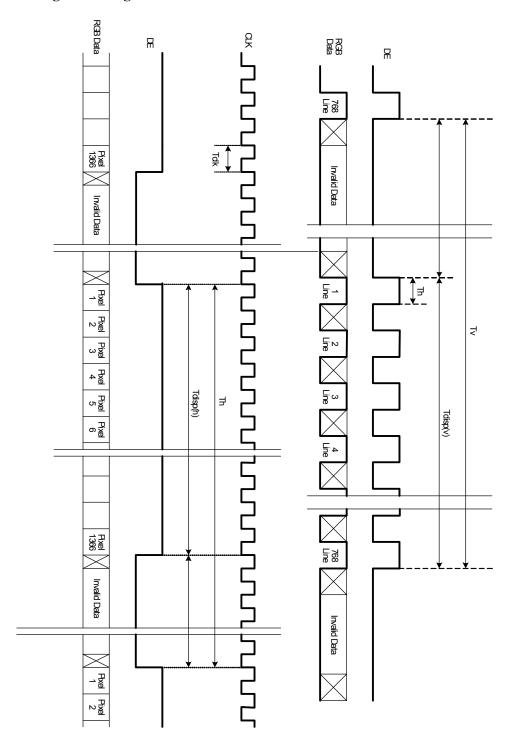
Horizontal display position is specified by the falling edge of 1st DCLK right after the rise of ENAB, is displayed on the left edge of the screen.

Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise the of ENAB is displayed at the top line of screen.

3.) If a period of DEB "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.

4.) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

3-4 Signal Timing Waveforms



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3-5 Color Input Data Reference

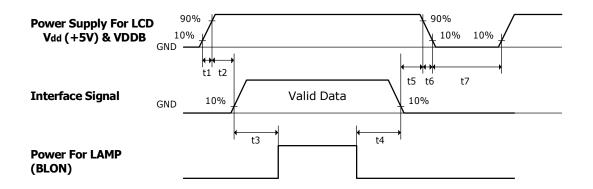
The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

												Inpu	ıt Co	olor l	Data	ı									
Color					RF	ED							GRI	EEN	[BL	JUE			
		MS	В					Ι	SB	MS	В					Ι	LSB	MS	В					Ι	SB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B 4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

COLOR DATA REFERENCE

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3-6 Power Sequence

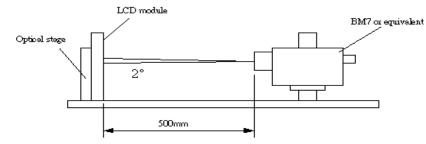


Parameter		Values	Units	
	Min.	Тур.	Max.	
t1	470	-	1000	us
t2	50	-	100	ms
t3	800	-	-	ms
t4	200	-	-	ms
t5	50	-	-	ms
t6	0.47	-	30	ms
t7	1	-	-	S

(1) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

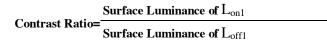


Via 1.1 procents additional information concerning the measurement equipment of	
	and mathod
Fig.1 1 presents additional information concerning the measurement equipment a	and memou.

Parameter		Sym	bol		Values		Units	Notes
				Min.	Тур.	Max.		
Contrast Ratio		Cl	R	650	800			1
Surface Luminance, white		LW	Ή	400	500		cd/m ²	2
Luminance Variation		$\delta_{ ext{white}}$	9 p			1.40		3
Response Rise Time		Tr	R		8	20	ms	4
Time Decay Time		Tr	D		8	20	ms	Gray to Gra
Color Coo	rdinates			1				
	RED	R	x		0.640			
		R	Y		0.330	-		
	GREEN	G	x		0.270	-		
		G	Y	T 0.02	0.600			
	BLUE	B	x	Тур0.03	0.150	• Typ.+0.03		
		В	Y		0.060			
	WHITE	W	x		0.280			
	L	W	Ϋ́Υ		0.290	-		
Viewing A	ngle							
x axis, right($\varphi = 0^\circ$)		θ	r	1	85		Degree	5
x axis, left($\varphi = 180^{\circ}$)		θ	1	1	85			
y axis, up($\varphi = 90^{\circ}$)		θ	u	1	85			
y axis	s, down ($\varphi = 0^\circ$)	θ	d	1	85			

Note:

1. Contrast Ratio (CR) is defined mathematically as:



- Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When VDDB = 24V, IDDB = 5A. L_{WH}=Lon1 Where Lon1 is the luminance with all pixels displaying white at center 1 location.
- 3. The variation in surface luminance, δ WHITE is defined (center of Screen) as:

 $\delta_{\text{WHITE}(9P)}$ = Maximum(L_{on1}, L_{on2},...,L_{on9})/Minimum(L_{on1}, L_{on2},...L_{on9})

- 4. Response time is the time required for the display to transfer between any two gray level(Average) For additional information see FIG3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

FIG. 2 Luminance

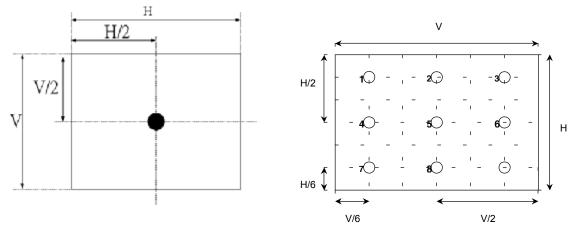


FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal.

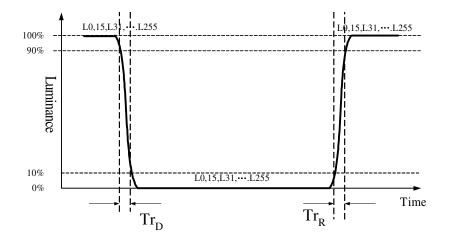
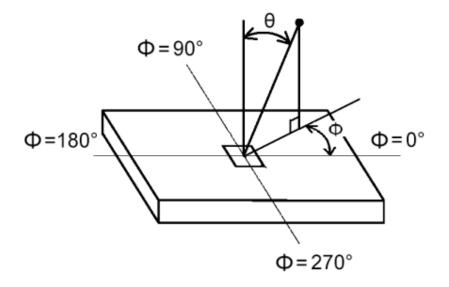


FIG.4 Viewing angle

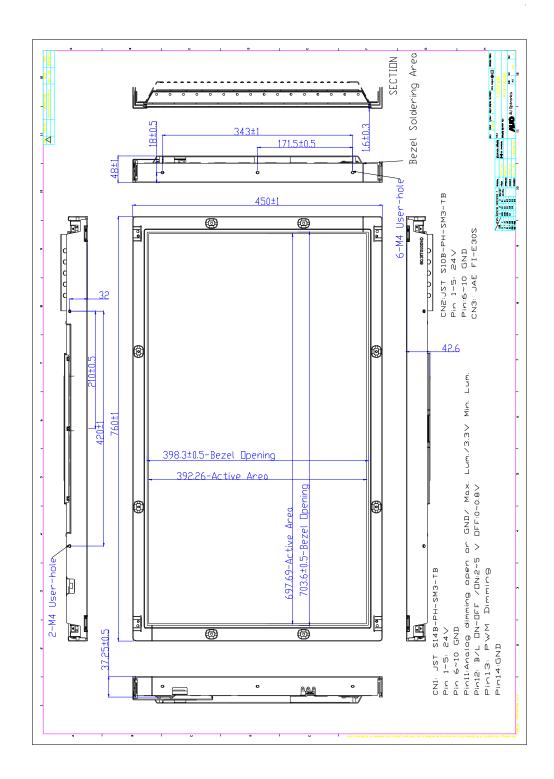


5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T315XW01 V2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

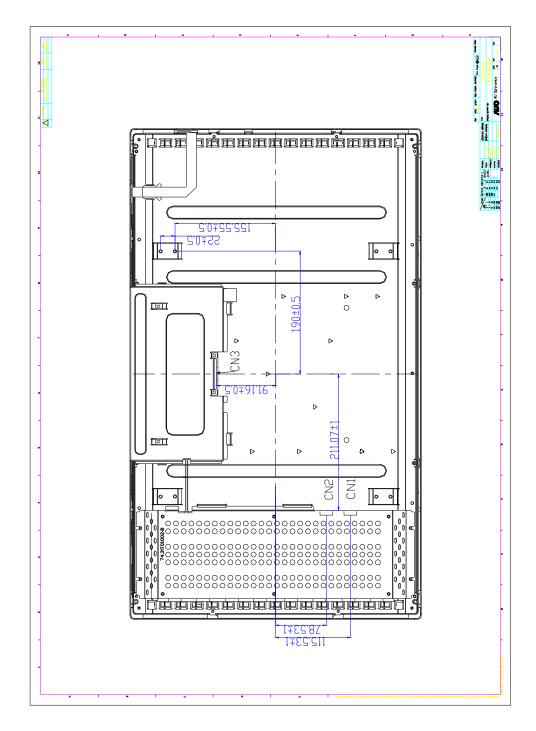
	Horizontal	760.0mm			
Outline Dimension	Vertical	450.0mm			
	Depth	47mm			
Bezel Opening	Horizontal	703.6mm			
	Vertical	398.3mm			
Active Display Area	Horizontal	697.68mm			
	Vertical	392.26mm			
Weight	6000g Typ.				
Surface Treatment	AG, 3H				

Front View



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Rear View



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6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta=60°C 240h
2	Low temperature storage test	Ta=-20°C 240h
3	High temperature operation test	Ta=50°C 80%RH 240h
4	Low temperature operation test	Ta=0°C 240h
5	Vibration test (non-operating)	Wave form: random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration: X, Y, Z 20min One time each direction
6	Shock test (non-operating)	Shock level: 50G Waveform: half since wave, 11ms Direction: ±X, ±Y, ±Z One time each direction
7	Vibration test (with carton)	Random Vibration: 10~200Hz,1.5G,30minutes in each X,Y,Z direction
8	Drop test (with carton)	Height: 53.3cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)
9	Altitude Storage/shipment	50,000 feet (12Kpa)

7. International Standard

7-1. Safety

(1) UL1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995

Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) CAN/CSA C22.2 No. 950-95/60950 Third Edition, Canadian Standards Association,

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

(3) EN60950 : 1992+A2: 1993+A2: 1993+C3: 1995+A4: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+C3: 1995+A4:1996

European Committee for Electrotechnical Standardization (CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

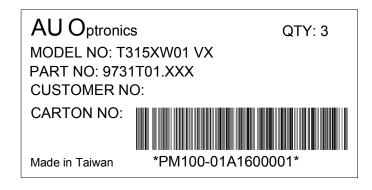
- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998

8. Packing

Label : 83mm * 23mm



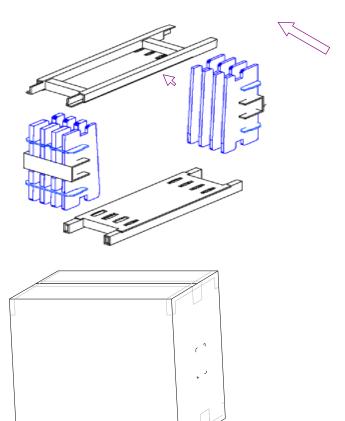
Carton Label : 80mm * 40mm



Carton :

3pcs Modules





947(L)mm * 290(W)mm * 595(H)mm

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.

(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.