

Product Description:						
AUO Model Name: T315XV	V01 VC (Q	D32HL01)				
Customer Part No/Project Name:						
Customer Signature	Date	AUO	Date			
Customer Signature	Date	AUO	Date			
Customer Signature	Date	AUO	Date			



Document Version: 02
Date: 200611/20

Product Specifications

31.5" WXGA Color TFT-LCD Module Model Name: T315XW01 VC (QD32HL01)

() Preliminary Specifications (*) Final Specifications



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The device listed in these technical literature sheets was designed and manufactured for use in OA equipment.

In case of using the device for applications such as control and safety equipment for transportation (aircraft, trains, automobiles, etc.), rescue and security equipment and various safety related equipment which require higher reliability and safety, take into consideration that appropriate measures such as fail-safe functions and redundant system design should be taken.

Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

AUO assumes no responsibility for any damage resulting from the use of the device, which does not comply with the instructions, and the precautions specified in these technical literature sheets.

Contact and consult with an AUO sales representative for any questions about this device.



Revision History						
VER.	Date	ECN NO.	Change Content			
1	08/01/2006	N/A	Preliminary Specification Initiate			
2	11/20/2006		 Update Produce spec document format to AUO standard. Update AUO model name QD32HL01 T315XW01 VC Update Serial label format. 			



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1. Application

This specification applies to a color TFT-LCD module T315XW01VC (QD32HL01).

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel; driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 1366x3x768 dots, Wide XGA mode, with 16,777,216 colors by using 8-bit 1 channel LVDS (Low Voltage Differential Signaling) to interface and +5V DC supply voltage for TFT-LCD panel driving.

The TFT-LCD panel used for this module has wide view angles and fast response time. Display mode is normally black. Each pixel is divided into Red, Green and Blue sub-pixels, which are arranged in vertical strips. A low-reflection surface treatment and high-color-saturation type color filter are also used for this panel. Therefore, this module is suitable for the multimedia use. Especially TV can be obtained by using this module.

[Features]

- 1) High contrast image. (800:1)
- 2) High brilliant image. (500 nits)
- 3) Wide viewing angle. (88/88/88)
- 4) Fast response time. (On/Off 16ms)
- 5) High color saturation. (NTSC 72%)
- 6) WXGA resolution. (1366x768)
- 7) LVDS interface. (8-bit 1 channel)

3. General Specification

Parameter	Specifications	Unit
Display size	80.04 (31.5") Diagonal	mm
Active area	697.685 (H) × 392.256 (V)	mm
Pixel format	$1366 \text{ (H)} \times 768 \text{ (V)} ; (1 \text{ pixel} = R + G + B \text{ dots})$	Pixel
Pixel size	0.5107 (H) × 0.5107 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Color depth	16.7M colors	
Luminance (Center point typ.)	500	Cd/m ²
Contrast Ratio	1000:1	
View angle	88/88/88	
Display mode	Normally Black	
Unit outline dimensions (typ.)*1	760.0×450.0×48.0	mm
Weight (Max.)	6000	g
Surface treatment	Anti-glare and hard-coating 3H	
Lamp Quantity	Direct 16 Lamps	pcs

^{*1.}Note: excluding backlight cables.



4. Absolute Maximum Ratings

LCD module

Parameter	Symbol	Condition	Ratings	Unit	Remark
Panel Input Voltage	V_{CC}	Ta=25°C	-0.3 ~ +5.5	V_{DC}	
Storage temperature	Tstg	_	-20 ~ + 60	$^{\circ}\mathbb{C}$	[Note1]
Operating temperature (Ambient)	Тора	_	0 ~ +50	$^{\circ}\!\mathbb{C}$	

Note 1. Humidity : 90%RH Max. at Ta \leq 40 °C . Maximum wet-bulb temperature at 39 °C or less at Ta>40 °C . No condensation .

5. Electrical Specifications

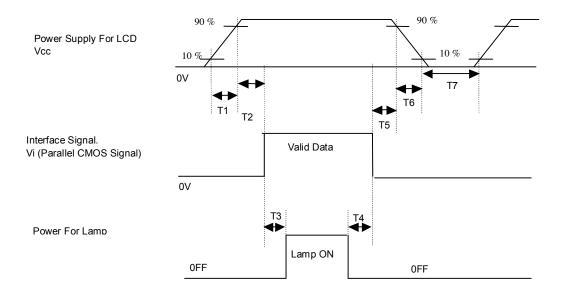
5-1. TFT-LCD Module Driving

	Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Vcc	Supply voltage		Vcc	+4.5	+5	+5.5	V	Note 2.
	Current dissipation		Icc		1.4	1.9	A	Note 3.
	Rush current		Iccs			4.0	A	
	Permissive Input R	ipple Voltage	Vrp			120	mV	
Different	ial input	High	V_{TH}			+100	mV	V _{CM} =+1.2V
Threshol	d voltage	Low	V_{TL}	-100			mV	Note 1.
Input cur	Input current (High)		IOH			+/- 10	μ A	V _I =2.4V
								Vcc=3.6V
Input current (Low)		IOL			+/- 10	μ A	V _I =0V	
							Vcc=3.6V	
Terminal resistor		RT		100		Ω	Differential	
								input

Note 1. V_{CM}: Common mode voltage of LVDS driver.

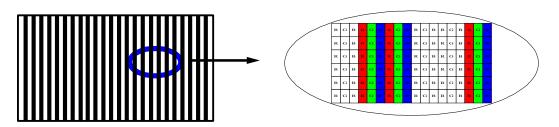
Note 2. On-off conditions for supply voltage





0.1 \leq t1 \leq 10 ms ; 0 < t2 \leq 50 ms ; 200 ms \leq t3 ; 200 ms \leq t4 ; 0 < t5 \leq 50 ms ; 0 < t6 \leq 10 ms ; 400 ms < t7

Note 3. Typical current condition: 2-line vertical stripe pattern (0,255GS). V_{CC} =+5V



Max current condition: 1x1dot Checker Board Pattern (0, 255GS). V_{CC}=+5V









5-2. TFT-LCD Pin Assignment

CN1 (LVDS signals and +5V DC power supply)

Using connector: FI-E30S (JAE)

Pin No.	Symbol	Function	Remark
1	NC	NC	
2	NC	NC	
3	NC	NC	
4	GND	Ground	
5	RX0-	Low Voltage Differential Signal Input Data (-)	LVDS
6	RX0+	Low Voltage Differential Signal Input Data (+)	LVDS
7	GND	Ground	
8	RX1-	Low Voltage Differential Signal Input Data (-)	LVDS
9	RX1+	Low Voltage Differential Signal Input Data (+)	LVDS
10	GND	Ground	
11	RX2-	Low Voltage Differential Signal Input Data (-)	LVDS
12	RX2+	Low Voltage Differential Signal Input Data (+)	LVDS
13	GND	Ground	
14	RXClk-	Low Voltage Differential Signal Input Clock (-)	LVDS
15	RXClk+	Low Voltage Differential Signal Input Clock (+)	LVDS
16	GND	Ground	
17	RX3-	Low Voltage Differential Signal Input Data (-)	LVDS
18	RX3+	Low Voltage Differential Signal Input Data (+)	LVDS
19	GND	Ground	
20	Reserved	Reserved	
21	LVDS option	NC or H(3.3V) for NS, Low(GND) for JEIDA	
22	GND	GND	
23	GND	GND	
24	GND	GND	
25	GND	GND	
26	VDD	+5V DC Voltage Input	
27	VDD	+5V DC Voltage Input	
28	VDD	+5V DC Voltage Input	
29	VDD	+5V DC Voltage Input	
30	VDD	+5V DC Voltage Input	

Note 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCDs metal frame.

Note 2. Relation between LVDS signals and actual data shows below section (7-1).

Note 3. All VDD (power supply) pins should be connected together.



5-3. Backlight driving

The backlight system is a direct-lighting type with 16 CCFT (Cold Cathode Fluorescent Tube). The characteristics of the lamp are shown in the following table.

Parameter	Parameter		Symbol		Values		Unit	Notes	
1 at atticted			Symbol	Min	Тур	Max	Ont	Notes	
			In	verter					
Power Supply Input Voltage			VDDB	22.8	24.0	25.2	Vdc		
Power Supply Input Current			IDDB	4.2	4.6	5.0	A		
Power Consumption			PB	-	110	126	W		
	BRTI Sign	al	VBI	0		3.8	V		
Input Voltage for Control System	BRTC	Low	VBCL	0		0.8	V		
Signals		High	VCBH	2.0	3.5	5.0	V		
Lamp current			IL		5.0		mA		
Lamp voltage			VL	(1215)	(1350)	(1485)	Vrms	25℃	
Lamp power consumption			PL		(6.53)		W	Note 2. IL=	5mA
Lamp frequency	Lamp frequency				(58)		kHz	Note 3	
Established starting voltage			Vs			(1700)	Vrms	$Ta = 25^{\circ}C$	
			V S			(1900)	Vrms	$Ta = 0^{\circ}C$	Note 4
Lamp life time			LL	50000			hour	Note 5.	

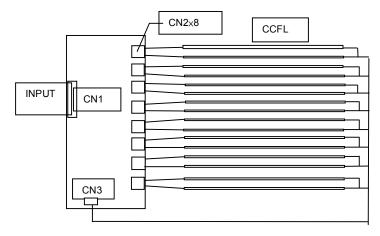
- Note 1. Lamp current is measured with current meter for high frequency.
- Note 2. Calculated Value for reference ($IL \times VL$)
- Note 3. Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- Note 4. The voltage above this value should be applied to the lamp for more than 1 second to start-up. Otherwise the lamp may not be turned on.
- Note 5. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2 °C.
- Note 6. The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.
- Note 7. Protection function: if one lamp cannot light on well, the inverter will shut down all lamps.



5-4. Backlight Inverter Connection

5-4-1. Inverter Connector:

Connector	Туре	Manufactured
CN1	S14 B-PH-SM3 TB	JST
CN2	SM02(12B)-BHS-1-TB	JST
CN3	S2B-ZR-SM3A-TF	JST



5-4-2. Inverter Connector Pin Assignment:

CN1: PHR-14(JST) or Equivalent

Pin No.	Symbol	Description	Remark
1	VDDB	+24V DC	
2	VDDB	+24V DC	
3	VDDB	+24V DC	
4	VDDB	+24V DC	
5	VDDB	+24V DC	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	GND	Ground	
10	GND	Ground	
11	NC	Non Connection	
12	BRTC	Backlight On/OFF signal	On: High, Off: Low
13	BRTI	Luminance by voltage method	Note.1
14	SGND	Signal Ground	

Note.1 Luminance ratio is linearly controllable in the range of the following table.

BTRI Voltage (VBI)	Luminance ratio
0V	20% (Minimum)
3.3V	100% (Maximum)

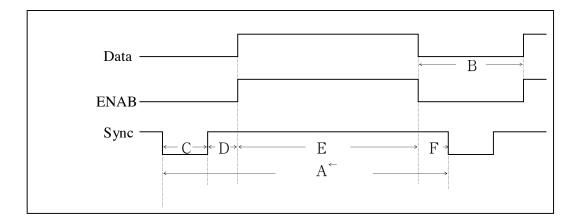


5-5. Signal Timing Spec.

	Item	Symbol	Min	Тур	Max	Unit	Notes
DCLK	Frequency	F_{CLK}		80	82	MHz	
DCLK	Period	t_{CLK}	12.2	12.5		ns	
	Period	t _{HA}	1512	1648	1780	4	
Hsync	Width-Active	t_{HC}	8	16		t_{CLK}	
	Frequency	fH	44	48.54	52	kHz	
	Frequency	fv	47	60	63	Hz	
Vsync	Period	t _{VA}	774	810		4	
	Width-Active	t_{VC}	2	6		t _{HA}	
	Horizontal back porch	t _{HD}	8	80		t_{CLK}	
	Horizontal front porch	$t_{ m HF}$	16	186		t_{CLK}	
	Horizontal active	t _{HE}	1366	1366	1366	t_{CLK}	
Data Enable	Horizontal blanking	t _{HB}	146	282		t_{CLK}	
Data Eliable	Vertical back porch	$t_{ m VD}$	2	20		t _{HA}	
	Vertical front porch	$t_{ m VF}$	2	16		t _{HA}	
	Vertical active	$t_{ m VE}$	768	768	768	t _{HA}	
	Vertical blanking	t_{VB}	6	42		t _{HA}	

Note 1. The performance of electro-optical characteristics may be influenced by variance of the vertical refresh rates.

- Note 2. Hsync period shall be a double number of character (8).
- Note 3. Signal Timing Waveform as below.





5-6. Reference of Data Signal and Color.

Ī	Colors &	Data signal																								
	Gray	Gray	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	В1	B2	В3	B4	B5	В6	В7
	scale	Scale																								
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Be	Green		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	_	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
윽	Magenta	_	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ଦ୍ର		GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Sca																										
le c																										
ς R	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ed		GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ìray	Davidson	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale																										
of Green	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gree	Drigittei	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
en l	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G		GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Sca																										
Gray Scale of Blue			1																							
of B	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
Ē		GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
ወ																										

0 : Low level voltage, 1 : High level voltage

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16,777,216-color display can be achieved on the screen.



6. Optical Specifications

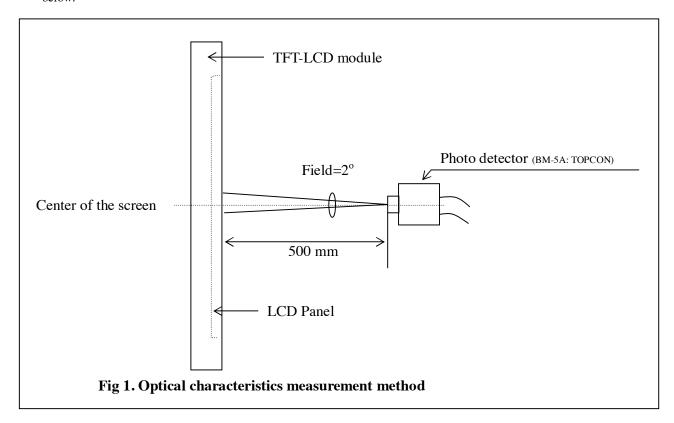
Ta=25°C , V_{CC} =+5V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing	L/R	θ 21, θ 22	CR>10	85	88	_	Deg.	Note 1,4
angle	U	θ 11		85	88	_	Deg.	
range	D	θ 12		85	88	_	Deg.	
Contr	Contrast ratio		$\theta = 0^{\circ}$	600	1000	_		Note 2,4
Response	Response Time (G-G)			_	_	_		
Respo	onse time	τ		_	21	_	ms	Note 3,4
Rise tim	e τr			_	16	_	ms	
Fall time	· τ d			_	5	_	ms	
Chromatic	Chromaticity of			0.246	0.276	0.306		Note 4
White (CII	White (CIE 1931)			0.266	0.296	0.326		
Chromatic	Chromaticity of			0.611	0.641	0.671		Color
Red (CIE 1	1931)	Ry		0.308	0.338	0.368		Temperature
Chromatic	Chromaticity of			0.255	0.285	0.315		10,000K
	Green (CIE 1931)			0.580	0.610	0.640		NTSC 72 %
Chromatic	Chromaticity of			0.115	0.145	0.175		
Blue (CIE 1931)		Ву		0.045	0.075	0.105		
Luminance of white		ΥL		_	500		Cd/m ²	Note 4
White U	Jniformity	δ W (5P)		_	_	1.3		Note 5
Cros	ss Talk	HDsha%				1		Note 6
		VDsha%				1		

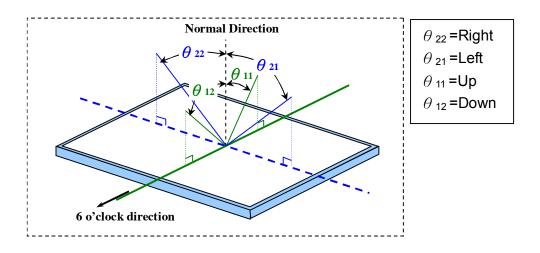
^{*} The measurement shall be operated 30 minutes after lighting at rating. (typical condition: IL = TBD mArms)



The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.1 below.



Note 1. Definitions of viewing angle range:



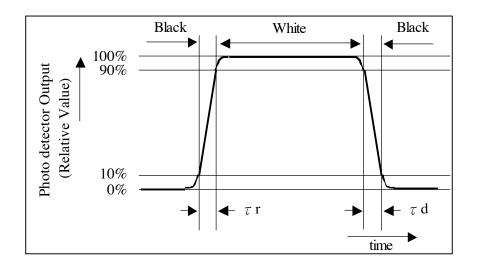


Note 2. Definition of contrast ratio:

The contrast ratio is defined as the following.

Note 3. Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



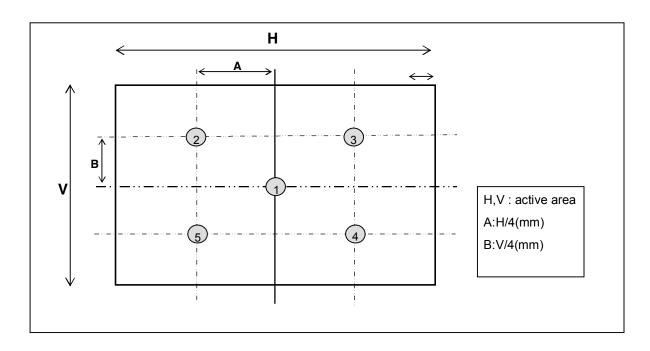
Note 4. This shall be measured at center of the screen.

Note 5. Definition of white uniformity:

White uniformity is defined as the following the number of measurement points within active area. formula are $\delta w(5)$ and $\delta w(13)$.

$$\delta w(5P) = \frac{\text{Maximum Luminance of 5 points}}{\text{Minimum Luminance of 5 points}}$$





Note 6. Definition of Shadow:

Horizontal Shadow:

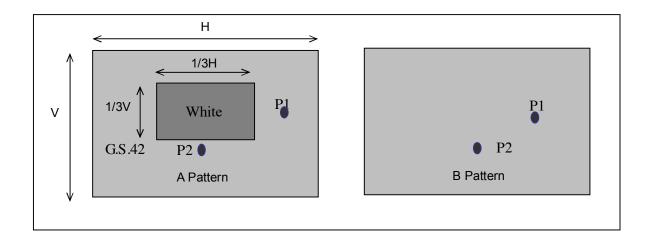
Ywh is the brightness of point P1 when module display B pattern Yers is the brightness of point P1 when module display A pattern

H-Shadow (Dsha%) = (
$$| Ywh-Ycrs | / Ywh) x100$$

Vertical Shadow:

Ywh is the brightness of point P2 when module display B pattern Ycrs is the brightness of point P2 when module display A pattern

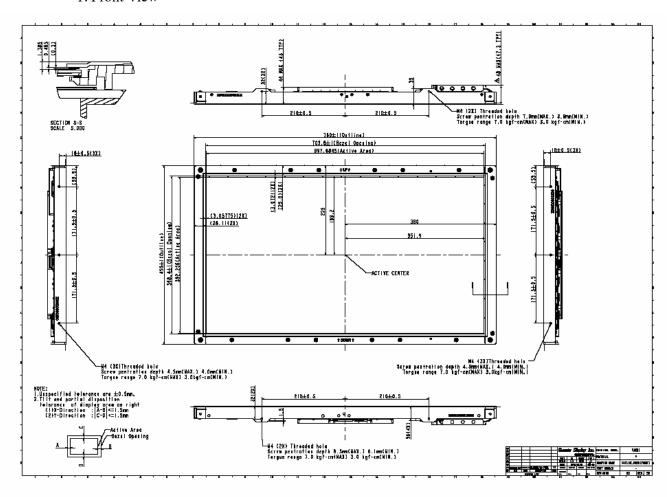
V-Shadow (Dsha%) =
$$(| Ywh-Ycrs | / Ywh) x 100$$





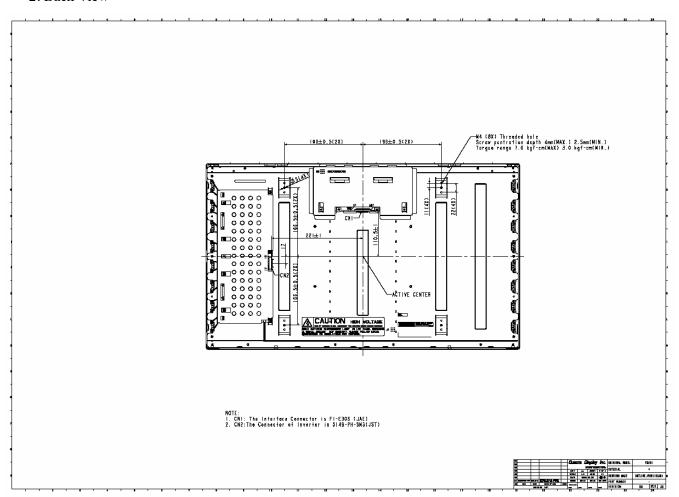
7. Mechanical Specifications

1. Front View





2. Back View





8. Reliability Test

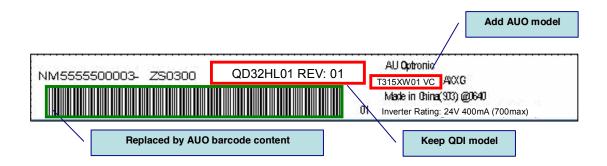
8-1. Reliability Items

240h
240h
30 %RH 240h
240h
240h
$0\sim$ 500Hz, 1.0G, 20 min/each axis
J
11ms, half sine wave
:X,±Y,±Z
nce for each direction.
ting/Non-Operating
KV
ating/Non-Operating
0KV
3



9. Others

1. LCD Module Label:



- 2. Adjusting volume has been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3. Disassembling the module can cause permanent damage and should be strictly avoided.
- 4. Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5. If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.