

Product Description: T420HW01 V0 TFT-LCD PANEL										
AUO Model Name: T42	0HW01 V0									
Customer Part No/Proje	ect Name:									
Customer Signature	Date	AUO	Date							
Customer Signature	Date	AUO Approved By: PL Chen	Date							
Customer Signature	Date		Date							



Document Version: 1.0 Date:2006/8/01

Product Functional Specification

42" Full-HD Color TFT-LCD Module Model Name: T420HW01 V0

> () Preliminary Specification (*) Final Specification

Note : This specification is subject to change without notice.



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Record of Revision

Version	Date	No	Old Description	New Description	Remark
1.0	2006/08/01		First release		



1. General Description

This specification applies to the 42 inch Color TFT-LCD Module T420HW01 V0. This LCD module has a TFT active matrix type liquid crystal panel 1920x1080 pixels, and diagonal size of 42 inch. This module supports 1920x1080 Full-HD mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T420HW01 V0 has been designed to apply the 8-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

Items	Specification	Unit	Note
Active Screen Size	42.02	inches	
Display Area	930.24(H) x 523.26(V)	mm	
Outline Dimension	983.0(H) x 576.0(V) x 52.3(D)	mm	With inverter
Driver Element	a-Si TFT active matrix		
Display Colors	16.7M	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Pitch	0.4845	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Lamp quantity, type	20pcs, Straight type	pcs	
Surface Treatment	AG, 3H		

* General Information



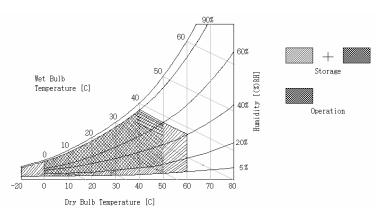
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Item	Symbo	Min	Max	Unit	Note
	I				
Power Supply Input Voltage	Vdd	-0.3	13.2	[Volt]	1
Logic Input Voltage	Vin	-0.3	3.6	[Volt]	1
BLU Input Voltage	Vddb	-0.3	26.4	[Volt]	1
BLU Brightness Control Voltage	BLON	-0.3	3.6	[Volt]	1
Ambient Operating Temperature	Тор	0	+50	[°C]	2
Ambient Operating Humidity	Нор	10	80	[%RH]	2
Storage Temperature	Тѕт	-20	+60	[°C]	2
Storage Humidity	Нѕт	10	80	[%RH]	2
Shock (non-operation)		-	50	G	3
Vibration (non-operation)		-	1.5	G	4
Thermal shock		-20	60	С	5
Altitude test	50000fee	et (12Kpa)			

Note 1 : Duration = 50msec

- Note 2 : Maximum Wet-Bulb should be $50^\circ\!\mathrm{C}$ $\,$ and No condensation.
- Note 3 : Half sine wave, shock level : 50G(11ms), direction : ±x, ±y, ±z (one time each direction)
- Note 4 : Wave form : Sin, vibration level : 1.5G RMS, Bandwidth : 10~500Hz Duration : X,Y,Z 30min (one time each direction)
- Note 5 : -20C/1hr ~ 60C/1hr, 100 cycles



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3. Electrical Specification

The T420HW01 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input, which powers the CCFL, is typically generated by an inverter.

F	Parameter	Symbol		Values		Unit	Notes
			Min	Тур	Max		
LCD:							
Power St	upply Input Voltage	Vdd	10.8	12	13.2	Vdc	
Power St	upply Input Current	ldd	-	1		A	1
Power Co	onsumption	Pc	-	12		Watt	1
Inrush Cu	urrent	I _{RUSH}	-	-	4	А	5
LVDS Interface	Differential Input High Threshold Voltage	Vтн			+100	mV	4
	Differential Input Low Threshold Voltage	Vtl	-100			mV	4
	Common Input Voltage	VCIM	0.6	1.2	1.8	V	
CMOS	Input High	Vін	6.5		13.2	Vdc	
Interface	Threshold Voltage	(High)					
	Input Low	VIL	0		4	Vdc	
	Threshold Voltage	(Low)					
			-	(155)	-	Watt	2
Backlight Power Consumption			-	-	(195)	Watt	120% dimming
Life Time			50000	60000		Hours	3

3-1 Electrical Characteristics

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by



the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.

The relative humidity must not exceed 80% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C. When operate at low temperatures, the brightness of CCFL will drop and the lifetime of CCFL will be reduced.

Note :

- 1. Vdd=12.0V, fv=60Hz, fcLk=80 Mhz , 25°C, Vdd Duration time= 470 μs , Test pattern : white pattern
- The Backlight power consumption shown above does include loss of external inverter at 25°C.
 The used lamp current is the lamp typical current
- **3.** The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25±2°C.
- 4. VCIM = 1.2V

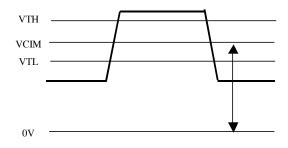
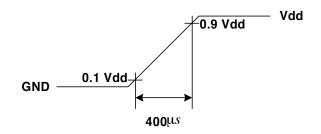


Figure : LVDS Differential Voltage

5. Measurement Condition: Rising time = 400 μ s





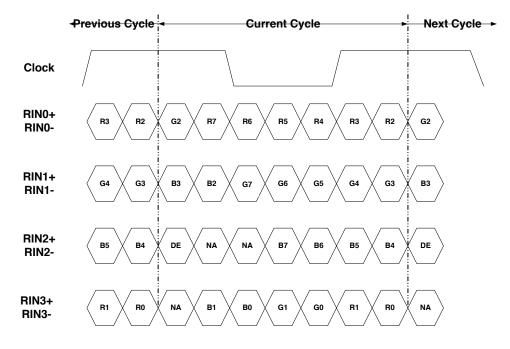
3-2 Interface Connections

No Symbol Description No Symbol Description 1 GND Ground 27 GND Ground 2 NC No connection 28 REON SECOND CHANNEL 0- 3 NC No connection 29 REOP SECOND CHANNEL 1+ 5 NC No connection 31 RE1P SECOND CHANNEL 1+ 6 Reserved 32 RE2N SECOND CHANNEL 2- 7 LVDS SEL LVDS order 33 RE2P SECOND CHANNEL 2- 8 Reserved 35 RECLKN SECOND CLOCK CHANNEL C+ 10 Reserved 36 RECLKN SECOND CLOCK CHANNEL C+ 11 GND Ground 37 GND Ground 12 ROON FIRST CHANNEL 0- 38 RE3N SECOND CHANNEL 3- 13 ROOP FIRST CHANNEL 1- 40 NC No connection 15 RO1P FIRST CHANNEL 2- 42 GND <td< th=""><th></th><th></th><th></th><th></th><th></th><th></th></td<>						
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7 LWDS SEL LWDS order 33 RE2P SECOND CHANNEL 2+ 8 Reserved 34 GND Ground 9 Reserved 35 RECLKN SECOND CLOCK CHANNEL C- 10 Reserved 36 RECLKP SECOND CLOCK CHANNEL C- 11 GND Ground 37 GND Ground 12 ROON FIRST CHANNEL 0- 38 RE3N SECOND CHANNEL 3- 13 ROOP FIRST CHANNEL 0+ 39 RE3P SECOND CHANNEL 3+ 14 RO1N FIRST CHANNEL 1- 40 NC No connection 15 RO1P FIRST CHANNEL 1+ 41 NC No connection 16 RO2N FIRST CHANNEL 2- 42 GND Ground 17 RO2P FIRST CHANNEL 2+ 43 GND Ground 18 GND Ground 44 GND Ground 20 ROCLKN FIRST CHANNEL C- 45 GND Ground 21 GND Ground 47 NC No connection <td>5</td> <td>NC</td> <td>No connection</td> <td>31</td> <td>RE1P</td> <td>SECOND CHANNEL 1+</td>	5	NC	No connection	31	RE1P	SECOND CHANNEL 1+
8 Reserved 34 GND Ground 9 Reserved 35 RECLKN SECOND CLOCK CHANNEL C- 10 Reserved 36 RECLKP SECOND CLOCK CHANNEL C- 11 GND Ground 37 GND Ground 12 ROON FIRST CHANNEL 0- 38 RE3N SECOND CHANNEL 3- 13 ROOP FIRST CHANNEL 0+ 39 RE3P SECOND CHANNEL 3+ 14 ROIN FIRST CHANNEL 1+ 40 NC No connection 15 ROIP FIRST CHANNEL 2- 42 GND Ground 16 RO2N FIRST CHANNEL 2- 42 GND Ground 18 GND Ground 44 GND Ground 20 ROCLKN FIRST CLOCK CHANNEL C- 45 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power	6	Reserved		32	RE2N	SECOND CHANNEL 2-
9 Reserved 35 RECLKN SECOND CLOCK CHANNEL C- 10 Reserved 36 RECLKP SECOND CLOCK CHANNEL C+ 11 GND Ground 37 GND Ground 12 ROON FIRST CHANNEL 0- 38 RE3N SECOND CHANNEL 3- 13 ROOP FIRST CHANNEL 0+ 39 RE3P SECOND CHANNEL 3- 14 RO1N FIRST CHANNEL 1- 40 NC No connection 15 RO1P FIRST CHANNEL 2- 42 GND Ground 16 RO2N FIRST CHANNEL 2- 42 GND Ground 18 GND Ground 44 GND Ground 19 ROCLKN FIRST CLOCK CHANNEL C- 45 GND Ground 20 ROCLKP FIRST CLOCK CHANNEL C- 46 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3- 48	7	LVDS SEL	LVDS order	33	RE2P	SECOND CHANNEL 2+
10Reserved36RECLKPSECOND CLOCK CHANNEL C+11GNDGround37GNDGround12ROONFIRST CHANNEL 0-38RE3NSECOND CHANNEL 3-13ROOPFIRST CHANNEL 0+39RE3PSECOND CHANNEL 3+14RO1NFIRST CHANNEL 1-40NCNo connection15RO1PFIRST CHANNEL 1+41NCNo connection16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C-46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1 All GND(ground) pin should be connector	8	Reserved		34	GND	Ground
11GNDGround37GNDGround12ROONFIRST CHANNEL 0-38RE3NSECOND CHANNEL 3-13ROOPFIRST CHANNEL 0+39RE3PSECOND CHANNEL 3+14RO1NFIRST CHANNEL 1-40NCNo connection15RO1PFIRST CHANNEL 1+41NCNo connection16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3+49VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connected together to the LCD module's metal frame2.All VLCD (power input) pins should be connector	9	Reserved		35	RECLKN	SECOND CLOCK CHANNEL C-
12 ROON FIRST CHANNEL 0- 38 RE3N SECOND CHANNEL 3- 13 ROOP FIRST CHANNEL 0+ 39 RE3P SECOND CHANNEL 3+ 14 RO1N FIRST CHANNEL 1- 40 NC No connection 15 RO1P FIRST CHANNEL 1+ 41 NC No connection 16 RO2N FIRST CHANNEL 2- 42 GND Ground 17 RO2P FIRST CHANNEL 2+ 43 GND Ground 18 GND Ground 44 GND Ground 19 ROCLKN FIRST CLOCK CHANNEL C- 45 GND Ground 20 ROCLKP FIRST CLOCK CHANNEL C+ 46 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC <td>10</td> <td>Reserved</td> <td></td> <td>36</td> <td>RECLKP</td> <td>SECOND CLOCK CHANNEL C+</td>	10	Reserved		36	RECLKP	SECOND CLOCK CHANNEL C+
13ROOPFIRST CHANNEL 0+39RE3PSECOND CHANNEL 3+14ROINFIRST CHANNEL 1-40NCNo connection15ROIPFIRST CHANNEL 1+41NCNo connection16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connector	11	GND	Ground	37	GND	Ground
14RO1NFIRST CHANNEL 1-40NCNo connection15RO1PFIRST CHANNEL 1+41NCNo connection16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connector	12	ROON	FIRST CHANNEL 0-	38	RE3N	SECOND CHANNEL 3-
15RO1PFIRST CHANNEL 1+41NCNo connection16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connected together to the LCD module's metal frame2.All VLCD (power input) pins should be connector.	13	ROOP	FIRST CHANNEL 0+	39	RE3P	SECOND CHANNEL 3+
16RO2NFIRST CHANNEL 2-42GNDGround17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connector	14	RO1N	FIRST CHANNEL 1-	40	NC	No connection
17RO2PFIRST CHANNEL 2+43GNDGround18GNDGround44GNDGround19ROCLKNFIRST CLOCK CHANNEL C-45GNDGround20ROCLKPFIRST CLOCK CHANNEL C+46GNDGround21GNDGround47NCNo connection22RO3NFIRST CHANNEL 3-48VLCDPower Supply +12V23RO3PFIRST CHANNEL 3+49VLCDPower Supply +12V24NCNo connection50VLCDPower Supply +12V25NCNo connection51VLCDPower Supply +12V26GNDGroundNote:1.All GND(ground) pin should be connected together to the LCD module's metal frame2.All VLCD (power input) pins should be connector.	15	RO1P	FIRST CHANNEL 1+	41	NC	No connection
18 GND Ground 44 GND Ground 19 ROCLKN FIRST CLOCK CHANNEL C- 45 GND Ground 20 ROCLKP FIRST CLOCK CHANNEL C+ 46 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector.	16	RO2N	FIRST CHANNEL 2-	42	GND	Ground
19 ROCLKN FIRST CLOCK CHANNEL C- 45 GND Ground 20 ROCLKP FIRST CLOCK CHANNEL C+ 46 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector. U	17	RO2P	FIRST CHANNEL 2+	43	GND	Ground
20 ROCLKP FIRST CLOCK CHANNEL C+ 46 GND Ground 21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector.	18	GND	Ground	44	GND	Ground
21 GND Ground 47 NC No connection 22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector.	19	ROCLKN	FIRST CLOCK CHANNEL C-	45	GND	Ground
22 RO3N FIRST CHANNEL 3- 48 VLCD Power Supply +12V 23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector. -	20	ROCLKP	FIRST CLOCK CHANNEL C+	46	GND	Ground
23 RO3P FIRST CHANNEL 3+ 49 VLCD Power Supply +12V 24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector.	21	GND	Ground	47	NC	No connection
24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector. -	22	RO3N	FIRST CHANNEL 3-	48	VLCD	Power Supply +12V
24 NC No connection 50 VLCD Power Supply +12V 25 NC No connection 51 VLCD Power Supply +12V 26 GND Ground - - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector. -	23	RO3P	FIRST CHANNEL 3+	49	VLCD	Power Supply +12V
26 GND Ground - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame - - 2.All VLCD (power input) pins should be connector. - -	24	NC	No connection	50	VLCD	Power Supply +12V
26 GND Ground - - Note:1.All GND(ground) pin should be connected together to the LCD module's metal frame 2.All VLCD (power input) pins should be connector.	25	NC	No connection	51	Vlcd	· · · · ·
2.All VLCD (power input) pins should be connector.	26	GND	Ground	-	-	-
2.All VLCD (power input) pins should be connector.	Note:1.	All GND(grou	nd) pin should be connected togeth	er to the LC	D module's me	tal frame
			· •			
					ndard.	

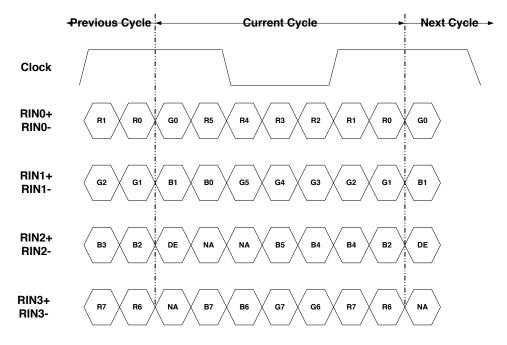
- LCD connector: FI-RE51S-HF (JAE) or equivalent



LVDS Option = High Ł JEIDA



LVDS Option = Low/Open Ł NS





Backlight Connector Pin Configuration

1. Electrical specification

No	ITEM	SYME	BOL	CONDITION	MIN	TYP	MAX	UNIT	Note
1	Input Voltage	VDD)B		22.8	24.0	26.4	V _{DC}	
2	Input Current	I _{DD}	В	V _{DDB} =24V 100% Brightness	6.1	6.4	6.7	A _{DC}	1
3	Input Power	PDD	B	V _{DDB} =24V 100% Brightness		(155)		w	1
4	Input inrush current	I _{RUS}	ŝH	V _{DDB} =24V 100% Brightness			12	A _{DC}	2
5	Output Frequency	FBI	L	V _{DDB} =24V		42		kHz	
6	ON/OFF Control	V _{BLON}	ON	V _{DDB} =24V	2.0		3.3	V _{DC}	
0	Voltage	V BLON	OFF	V _{DDB} =24V	0.0		0.8	V _{DC}	
7	ON/OFF Control Current	I _{BLC}	N	V _{DDB} =24V	0		2	mA _{DC}	
8	External PWM	EV _{PWM}	MAX		2.0		3.3	V _{DC}	
0	Control Voltage	⊏ ∨ PWM	MIN		0		0.8	V _{DC}	
9	External PWM	EI _{PWM}	MAX	PWM=100%	0		2	mA_DC	
9	Control Current	⊂ IPWM	MIN	PWM=30%	0		2	mA_DC	
10	External PWM Duty Ratio	EDPI	ŴМ		30		100	%	
11	External PWM Frequency	EF₽\	WM		140	180	300	Hz	
12	Internal PWM Control Voltage	IV _{PV}	VM	V _{DDB} =24V	0		3.3	V_{DC}	

 $(\,\text{Ta=25\pm5}^\circ\!\text{C}\,,\,\text{Turn on for 45minutes}\,)$

Note 1 : VDIM/Open = 1.6V; PDIM = Open/High

Note 2 : Duration = 20 ms



Connector 1: S14B-PH-SM3-TB(JST) or equivalent

Pin No Symbol Description 1 VDDB Operating Voltage Supply, +24V DC regulated 2 VDDB Operating Voltage Supply, +24V DC regulated 3 VDDB Operating Voltage Supply, +24V DC regulated 4 VDDB Operating Voltage Supply, +24V DC regulated 5 VDDB Operating Voltage Supply, +24V DC regulated 6 BLGND Ground and Current Return 7 BLGND Ground and Current Return 8 BLGND Ground and Current Return 9 BLGND Ground and Current Return 10 BLGND Ground and Current Return GND: 80%; Open/1.6V: 100%; High (3.3V) 120%, Luminance VDIM⁽¹⁾ 11 (optional function) 12 VBLON BL On-Off: Open/High (3.3V) for BL On as default External PWM (AC Signal Control Duty); PDIM⁽²⁾ Internal PWM (DC Power Control Duty, 0~3.3V); 13 Open/High (+3.3V, 100% Duty) for 100% GND: External PWM dimming; PDIM 14 Selection^(3,4) Open/High (3.3V): Internal PWM dimming.

- Note (1) VDIM is control signal for Inverter's output power to back light lamp bulb. Input signal should be able to control amplitude of Inverter output voltage. From 0V to 3.3V, Inverter output voltage should be able to vary to control brightness of lamp from 80% to 120% luminance variation. Approximate 1.6V might be 100% luminance control point. (optional function)
- Note (2) PDIM is PWM duty control input for +3.3V TTL level signal or DC voltage by Pin 14 input. This input signal is (a) continuous pulse signal with +3.3V, TTL level signal spec, or (b) DC power with 0~3.3V. If this is Open or +3.3V, 100% duty (i.e. +3.3V, DC level), backlight should perform 100% luminance. Duty ratio of this input signal should be proportional relationship in certain range of control without any kind of inherent side effect like waterfall effect on screen. Guaranteed duty range and dimming ratio should be specified with supplementary measurement result.
- Note (3) Pin 14 is the selection pin for PWM control method; if this pin is connected to GND, PDIM input of Pin 13 should have logic level duty signal for PWM control. If this is set to High or Open, Pin 13 should have DC level signal therefore the Inverter should have Saw Tooth Wave Generator to generate internal PWM signal. Default setting is "Not Connected", Pin 13 of PWM control should have DC Level signal for PWM.
- Note (4) Pin 14 selection vs. Pin 11/13 control function table:

	Pin 11 (DC Power Control Duty Amplitude) Function Always Turn On Default: Open/1.6V: 100%	Pin 13 Default: Open/High: 100%				
Pin 14 = GND	GND: 80%; Open/1.6V: 100%;	External PWM (AC Signal Control Duty)				
Pin 14 = Open/High	High (3.3V) 110%, Luminance	Internal PWM (DC Power Control Duty)				



3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

Vertical Frequency Range A (60Hz)

Signal	Item	Symbol	Min	Туре	Max	Unit
	Period	Τv	1090	1130	1200	Th
	Active	Tdisp (v)		1080		Th
Vertical Section	Blanking	Tblk (v)	10	50	120	Th
	Period	Th	1030	1100	1180	Tclk
	Active	Tdisp (h)			Tclk	
Horizontal Section	Blanking	Tblk (h)	70	140	220	Tclk
Clock	Period	CLK		13.41		ns
CIUCK	Frequency	Freq	67.36	74.58	84.96	MHz
Vertical Frequency	Frequency	Vs	58	58 60		Hz
Horizntal Frequency	Frequency	Hs	65.4	67.8	72	KHz

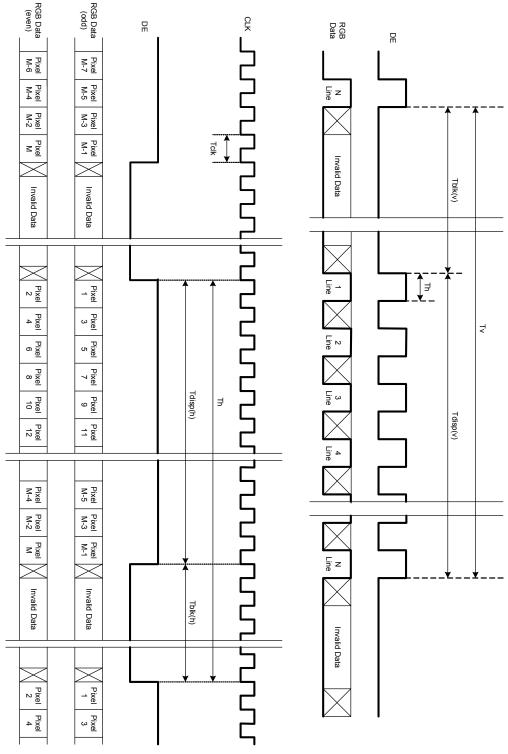
Vertical Frequency Range B (50Hz)

Signal	Item	Symbol	Min	Туре	Max	Unit	
	Period	Τv	1316	1356	1426	Th	
	Active	Tdisp (v)		1080		Th	
Vertical Section	Blanking	Tblk (v)	236	276	346	Th	
	Period	Th	1100	1414	1936	Tclk	
	Active	Tdisp (h)			Tclk		
Horizontal Section	Blanking	Tblk (h)	70	140	220	Tclk	
Clock	Period	CLK		13.41	13.41		
CIUCK	Frequency	Freq	67.77	74.58	84.13	MHz	
Vertical Frequency	Frequency	Vs	48 50		52	Hz	
Horizntal Frequency	Frequency	Hs	65.8	67.8	71.3	KHz	





3-4 Signal Timing Waveforms



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3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

											I	npu	t Co	lor	Data	a									
Color					R	ED							GRI	EEN	I			BLUE							
		MS	В					L	SB	MS	В				-	L	SB	MS	В					L	SB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	В5	B4	В3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
,	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
-	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN		-																							
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

COLOR DATA REFERENCE

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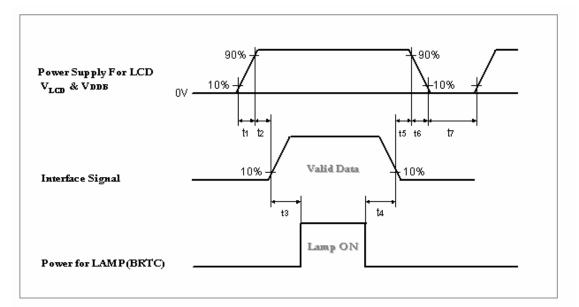
T420HW01 V0

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3-6 Power Sequence

1. Power sequence of panel



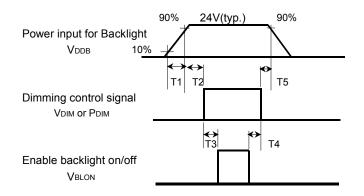
	Values			Units	
Parameter	Min.	Тур.	Max.	Onits	
t1	470	-	1000	us	
t2	20	-	35	ms	
t3	500	-	-	ms	
t4	200	-	-	ms	
t5	5	-	-	ms	
t6	-	-	30	ms	
t7	1	-	-	S	

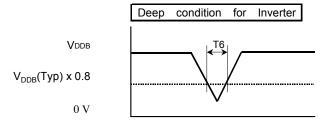
Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution : The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



2. Power sequence of inverter





Parameter	Values			Units
	Min.	Тур.	Max.	
T1	20	-	-	ms
T2	500	-	-	ms
Т3	250	-	-	ms
T4	0	-	-	ms
T5	1	-	-	ms
Т6	-	-	10	ms



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

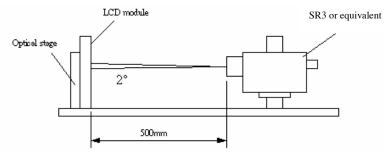


Fig.4-1 Optical measurement equipment and method

Parameter		Symbol	Values			Units	Notes
			Min.	Тур.	Max.		
Contrast Ratio		CR	1000	1500			1
Surface Luminanc	e, white	LWH	400	500		cd/m²	2
Luminance Variation	on	δ wніте 5р			1.3		3
Response Time (A	verage)	Τγ		8		ms	4,5 (Gray to Gray)
Ŗ	ise Time	Tr		15		ms	4
D	ecay Time	Tf		5		ms	4
Color Coordinates							
	RED	R _x		0.640			
		R _Y		0.330			
	GREEN	G _X		0.290		-	
		G _Y	Typ0.03	0.600	Turo 10.02		1
	BLUE	B _X	1 yp0.03	0.150	-Typ.+0.03		
		B _Y		0.060			1
	WHITE	W _X		0.280		-	T
		W _Y		0.290			1
Viewing Angle							Contrast Ratio>10
x axis, right($\varphi = 0^{\circ}$)		$ heta_{ m r}$		89		Degree	6
x axis, left(φ =180°)		θ_1		89		-	Τ
y axis, up(φ =90°)		θu		89			1
y axis, dov	vn (φ=0°)	θ_{d}		89			1

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Note:

1. Contrast Ratio (CR) is defined mathematically as:

Contrast ratio (CR)= <u>Brightness on the "white" state</u> Brightness on the "black" state

 Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When VDDB = 24V, IDDB = 6.4A.
 L_{WH}=L_{on1}, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

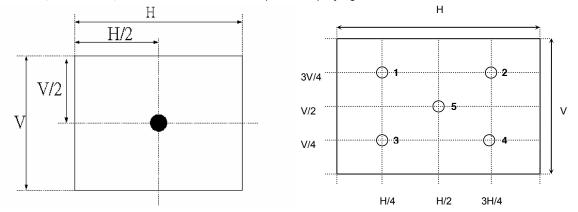
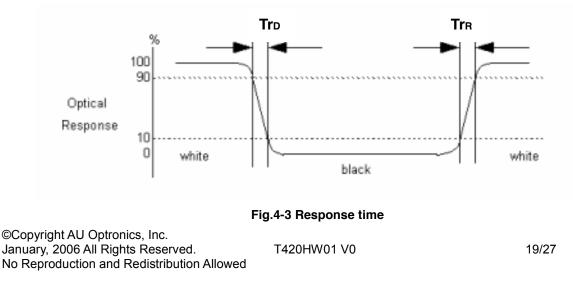


Fig.4-2 Optical measurement point

- 3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as: $\delta_{\text{WHITE(5P)}}$ =Maximum(L_{on1}, L_{on2},...,L_{on5})/Minimum(L_{on1}, L_{on2},...L_{on5})
- 4. Response time is the time required for the display to transition from white(L255) to black(L0) (Decay Time, Tr_D=Tf) and from black(L0) to white(L255) (Rise Time, Tr_R=Tr). For additional information see Fig. 4-3.





5. The response time is defined as the following figure and shall be measured by switching the input signal for 0, 63, 127, 191, 255 different gray level. For additional information see Fig. 4-4.

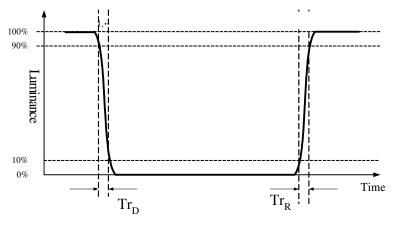


Fig.4-4 Response time

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-5. (Optical measurement by SR3)

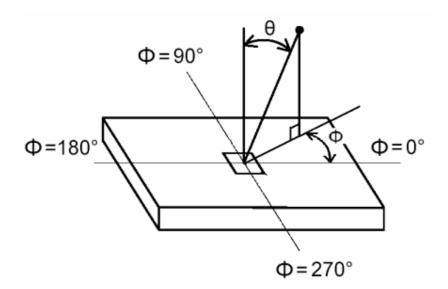


Fig.4-5 Viewing Angle Definition

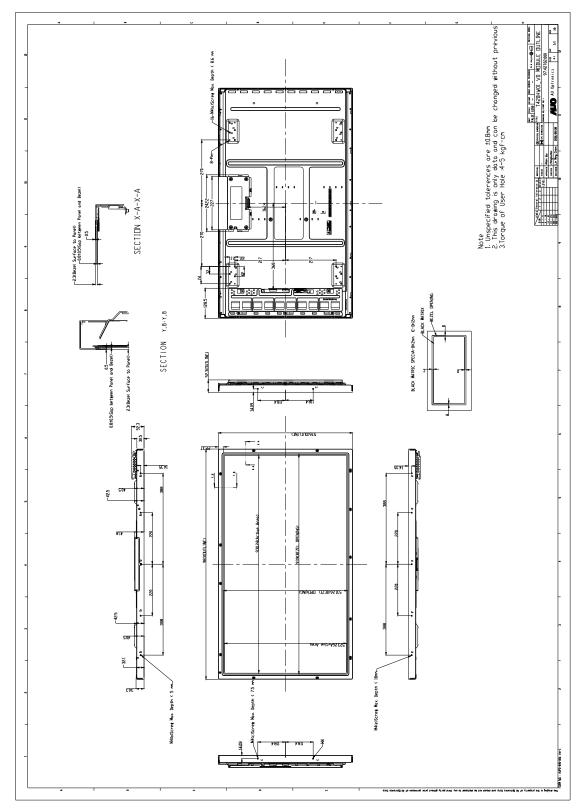


5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T420HW01. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal (typ.)	983.0mm		
Outline Dimension	Vertical (typ.)	576.0mm		
	Depth (typ.)	52.3mm (with inverter)		
Bezel Area	Horizontal (typ.)	939.0mm		
	Vertical (typ.)	531.26mm		
Active Display Area	Horizontal	930.25mm		
	Vertical	523.26mm		
Weight	12600g (Max.)			
Surface Treatment	AG, 3H			





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Environment test condition

No	Test Item	Condition	
1	High temperature storage test	Ta=60 $^\circ\!\mathrm{C}$, 300hr judge	
2	Low temperature storage test	Ta=-20℃, 300h judge	
3	High temperature/High humidity test	Ta=50℃, 80%RH, 300hr judge	
4	High temperature operation test	Ta=50 $^\circ\!\mathrm{C}$, 300hr judge	
5	Low temperature operation test	Ta=-5℃, 300hr judge	
		Wave form: SIN	
	Vibration test	Vibration level: 1.5G RMS	
6		Bandwidth: 10-500Hz	
	(non-operating)	Duration: X, Y, Z	
		30min each direction	
		Shock level: 50G	
7	Shock test	Waveform: half since wave, 11ms	
	(non-operating)	Direction: ±X, ±Y, ±Z	
		One time each direction	
	Vibration test (with carton)	Wave form: Random	
		Vibration level: 2.16G RMS	
8		Bandwidth: 5~500Hz	
		Duration: X, Y, Z	
		120min each direction	
	Drop test (with carton)	Height: 46cm	
9		1 corner, 3 edges, 6 surfaces	
		(ASTMD4169-I)	



7. International Standard

7-1. Safety

- UL60065, Underwriters Laboratories, Inc. (AUO file number : E204356)
 Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) CSA E60065, Canadian Standards Association
 Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- (3) IEC 60065 ver. 7th, European Committee for Electro technical Standardization (CENELEC) EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

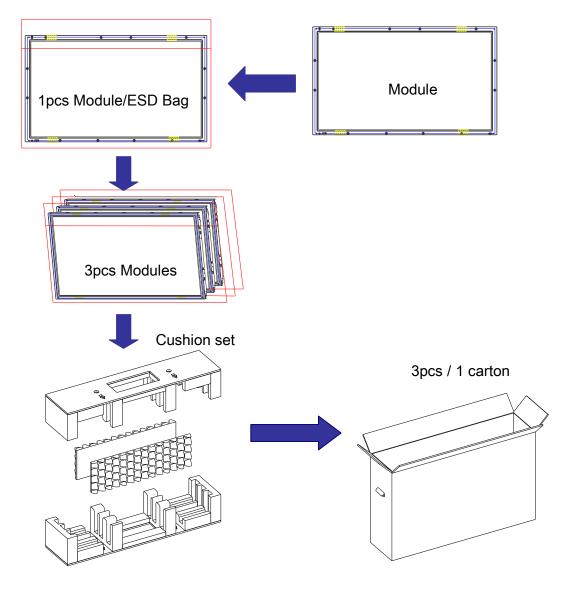
7-2. EMC

- ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



(2) Packing

Packing Instruction



Package information:

Carton outside dimension : 1087x285x716mm

Carton/Package weight : 3kg

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Shipping label

-rvv4@222000 %30-LAAAD2* Reting: XV _{rm.} ; XA	Marvabachunai XXXXX Model No: T420HW01 All Optonics MADE IN TALVIANQUA)	VX IIZXXX	

Green Mark Description:

For Pb Free products, AUO will add 🕑 for identification.

For RoHS compatible products, AUO will add https://www.add.com/addition.

Note: The Green Mark will be present only when the green documents have been ready by AUO

Internal Green Team. (The definition of green design follows the AUO green design checklist.)

Carton label



Pallet information

By air cargo : : (4x1) x2 layers, one pallet put 8 boxes, total 24 pcs module.

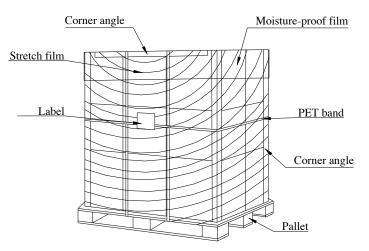
By sea : (4x1) x3 layers, one pallet put 12 boxes, total 36 pcs module.

Pallet dimension : 1150x1100x120mm

Pallet weight : 10kg

By air total weight : 40.8 kg/box X 8 boxes=326.4 kg (with pallet weight 336.4kg)

By sea total weight : 40.8 kg/box X 12 boxes=489.6 kg (with pallet weight 499.6kg)





(3) PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged on back side of panel.
- (2) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (3) You should adopt radiation structure to satisfy the temperature specification.
- (4) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (5) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (6) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference



shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.