

| Product Description: T420XW01 V7 TFT-LCD PANEL | | | | | |
|--|-----------------------------|-----------------------------|------|--|--|
| | | | | | |
| AUO Model Name: T42 | AUO Model Name: T420XW01 V7 | | | | |
| Customer Part No/Project Name: | | | | | |
| | | | | | |
| Customer Signature | Date | AUO | Date | | |
| Customer Signature | Date | AUO Approved By: PL Chen | Date | | |
| Customer Signature | Date | | Date | | |



Document Version: 1.0 Date:2006/11/24

Product Functional Specification

42" WXGA Color TFT-LCD Module Model Name: T420XW01 V7

() Preliminary Specification (*) Final Specification

Note : This specification is subject to change without notice.



Contents

| No | ITEM |
|---------|------------------------------|
| | COVER |
| | CONTENTS |
| | RECORD OF REVISIONS |
| 1 | GENERAL DESCRIPTION |
| 2 | ABSOLUTE MAXIMUM RATINGS |
| 3 | ELECTRICAL SPECIFICATIONS |
| | ELECTRICAL CHARACTREISTICS |
| 3-2 | INTERFACE CONNECTIONS |
| 3-3 | SIGNAL TIMING SPECIFICATIONS |
| | SIGNAL TIMING WAVEFORMS |
| | COLOR INPUT DATA REFERNECE |
| | POWER SEQUENCE |
| 4 | OPTICAL SFECIFICATIONS |
| 5 | MECHANICAL CHARACTERISTICS |
| 6 | RELIABILITY |
| 7 | INTERNATIONAL STANDARDS |
| 7-1 | SAFETY |
| 7-2 | EMC |
| 8 | PACKING |
| 9 | PRECAUTIONS |



Record of Revision

| Version | Date | No | Old Description | New Description | Remark |
|---------|------------|----|-----------------|-----------------|--------|
| 1.0 | 2006/11/24 | | First release | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |



1. General Description

This specification applies to the 42 inch Color TFT-LCD Module T420XW01 V7. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 42 inch. This module supports 1366x768 WXGA mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T420XW01 V4 has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

| Items | Specification | Unit | Note |
|---------------------|-------------------------------|--------|---------------|
| Active Screen Size | 42.02 | inches | |
| Display Area | 930.25(H) x 523.01(V) | mm | |
| Outline Dimension | 983.0(H) x 576.0(V) x 52.7(D) | mm | With inverter |
| Driver Element | a-Si TFT active matrix | | |
| Display Colors | 16.7M | Colors | |
| Number of Pixels | 1366 x 768 | Pixel | |
| Pixel Pitch | 0.681 | mm | |
| Pixel Arrangement | RGB vertical stripe | | |
| Display Mode | Normally Black | | |
| Lamp quantity, type | 20pcs, Straight type | pcs | |
| Surface Treatment | AG, 3H | | |

* General Information



2. Absolute Maximum Ratings

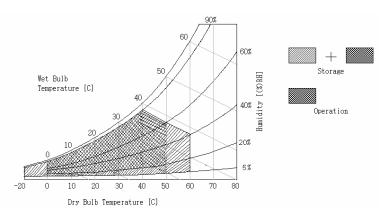
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

| Item | Symbo | Min | Max | Unit | Note |
|--------------------------------|-------------------|------|------|--------|------|
| | I | | | | |
| Power Supply Input Voltage | Vdd | -0.3 | 8.0 | [Volt] | 1 |
| Logic Input Voltage | Vin | -0.3 | 5.0 | [Volt] | 1 |
| BLU Input Voltage | Vddb | -0.3 | 27.0 | [Volt] | 1 |
| BLU Brightness Control Voltage | BLON | -0.3 | 5.5 | [Volt] | 1 |
| Ambient Operating Temperature | Тор | 0 | +50 | [°C] | 2 |
| Ambient Operating Humidity | Нор | 10 | 80 | [%RH] | 2 |
| Storage Temperature | Tst | -20 | +60 | [°C] | 2 |
| Storage Humidity | Нѕт | 10 | 80 | [%RH] | 2 |
| Shock (non-operation) | | - | 50 | G | 3 |
| Vibration (non-operation) | | - | 1.5 | G | 4 |
| Thermal shock | | -20 | 60 | С | 5 |
| Altitude test | 50000feet (12Kpa) | | | | |

Note 1 : Duration = 50msec

- Note 2 : Maximum Wet-Bulb should be 50 $^\circ\mathrm{C}$ and No condensation. 40 $^\circ\mathrm{C}$ /95%Humidity is for reference
- Note 3 : Half sine wave, shock level : 50G(11ms), direction : ±x, ±y, ±z (one time each direction)
- Note 4 : Wave form : random, vibration level : 1.5G RMS, Bandwidth : 10~500Hz Duration : X,Y,Z 60min (one time each direction)

Note 5 : -20C/1hr ~ 60C/1hr, 100 cycles



©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



3. Electrical Specification

The T420XW01 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input, which powers the CCFL, is typically generated by an inverter.

3-1 Electrical Characteristics

| | Parameter | Symbol | | Values | | Unit | Notes |
|-----------|----------------------|-------------------|-------|--------|------|-------|-------|
| | | | Min | Тур | Max | | |
| LCD: | | | | | | | |
| Power S | Supply Input Voltage | Vdd | 4.5 | 5 | 5.5 | Vdc | |
| Power S | Supply Input Current | ldd | - | 1.8 | 2.2 | А | 1 |
| Power 0 | Consumption | Pc | - | 9.0 | 10.0 | Watt | 1 |
| Inrush C | Current | I _{RUSH} | - | - | 4.5 | А | 5 |
| LVDS | Differential Input | Vтн | | | +100 | mV | |
| Interface | High Threshold | | | | | | 4 |
| | Voltage | | | | | | |
| | Differential Input | VTL | -100 | | | mV | |
| | Low Threshold | | | | | | 4 |
| | Voltage | | | | | | |
| | Common Input | VCIM | 1.10 | 1.25 | 1.40 | V | |
| | Voltage | | | | | | |
| CMOS | Input High | VIH | 2.4 | | 3.3 | Vdc | |
| Interface | Threshold Voltage | (High) | | | | | |
| | Input Low Threshold | VIL | 0 | | 0.7 | Vdc | |
| | Voltage | | | | | | |
| Backlight | Power Consumption | | - | 175 | 184 | Watt | 2 |
| Life Time | | | 50000 | 60000 | | Hours | 3 |

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you



confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.

The relative humidity must not exceed 80% non-condensing at temperatures of 40° C or less. At temperatures greater than 40° C, the wet bulb temperature must not exceed 39° C. When operate at low temperatures, the brightness of CCFL will drop and the lifetime of CCFL will be reduced.

Note :

- 1. Vdd=5.0V, fv=60Hz, fcLk=81.5 Mhz , 25°C, Vdd Duration time= 400 μs , Test pattern : white pattern
- The Backlight power consumption shown above does include loss of external inverter at 25℃. The used lamp current is the lamp typical current
- **3.** The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25±2°C.
- 4. VCIM = 1.2V

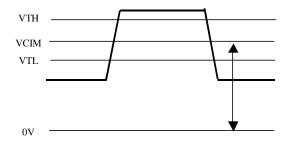
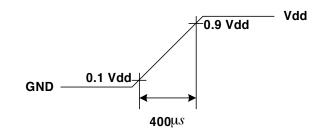


Figure : LVDS Differential Voltage

5. Measurement Condition: Rising time = 400 μ s





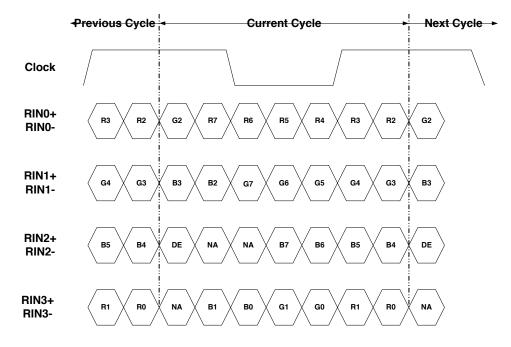
3-2 Interface Connections

- LCD connector: FI-X30SSL-HF (JAE)
- Mating connector: FI-30C2L (JAE)

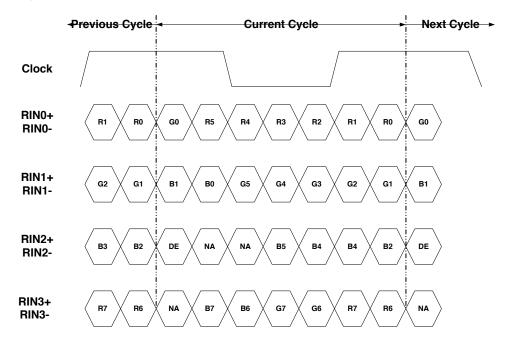
| Pin No | Symbol | Description | Note |
|--------|-------------|--|---------------------|
| 1 | VCC | +5V, DC, Regulated | |
| 2 | VCC | +5V, DC, Regulated | |
| 3 | VCC | +5V, DC, Regulated | |
| 4 | VCC | +5V, DC, Regulated | |
| 5 | GND | Ground and Signal Return | |
| 6 | GND | Ground and Signal Return | |
| 7 | GND | Ground and Signal Return | |
| 8 | GND | Ground and Signal Return | |
| 9 | LVDS Option | Low for Normal (NS), High/Open for JEIDA | Default: JEIDA mode |
| 10 | Reserved | Open or High | AUO internal test |
| 11 | GND | Ground and Signal Return for LVDS | |
| 12 | RXIN0- | LVDS Channel 0 negative | |
| 13 | RXIN0+ | LVDS Channel 0 positive | |
| 14 | GND | Ground and Signal Return for LVDS | |
| 15 | RXIN1- | LVDS Channel 1 negative | |
| 16 | RXIN1+ | LVDS Channel 1 positive | |
| 17 | GND | Ground and Signal Return for LVDS | |
| 18 | RXIN2- | LVDS Channel 2 negative | |
| 19 | RXIN2+ | LVDS Channel 2 positive | |
| 20 | GND | Ground and Signal Return for LVDS | |
| 21 | RXCLKIN- | LVDS Clock negative | |
| 22 | RXCLKIN+ | LVDS Clock positive | |
| 23 | GND | Ground and Signal Return for LVDS | |
| 24 | RXIN3- | LVDS Channel 3 negative | |
| 25 | RXIN3+ | LVDS Channel 3 positive | |
| 26 | GND | Ground and Signal Return for LVDS | |
| 27 | GND | Open or Ground | |
| 28 | GND | Open or Ground | |
| 29 | GND | Open or Ground | |
| 30 | GND | Open or Ground | |



LVDS Option = High/Open Ł JEIDA



LVDS Option = Low Ł NS



9/28



Backlight Connector Pin Configuration

1. Electrical specification

| No | ITEM | SYME | BOL | CONDITION | MIN | TYP | MAX | UNIT | Note |
|----|---------------------------------|-------------------|-----|--|-------|------|------|------------------|------|
| 1 | Input Voltage | V _{DD} | B | | 22.8 | 24.0 | 26.4 | V _{DC} | |
| 2 | Input Current | I _{DD} | В | V _{DDB} =24V 100% Brightness | 6.935 | 7.3 | 7.66 | A _{DC} | 1 |
| 3 | Input Power | P _{DD} | В | V _{DDB} =24V 100% Brightness | | 175 | | w | 1 |
| 4 | Input inrush current | I _{RUS} | ίΗ | V _{DDB} =24V 100% Brightness | | | 12 | A _{DC} | 2 |
| 5 | Output Frequency | F _{BI} | - | V _{DDB} =24V | | 58 | | kHz | |
| 6 | ON/OFF Control | V _{BLON} | ON | V _{DDB} =24V | 2.0 | | 3.3 | V _{DC} | |
| 0 | Voltage | ▼ BLON | OFF | V _{DDB} =24V | 0.0 | | 0.8 | V _{DC} | |
| 7 | ON/OFF Control Current | I _{BLC} | N | V _{DDB} =24V | 0 | | 2 | mA _{DC} | |
| 8 | External PWM | EVpwm | MAX | | 2.0 | | 3.3 | V _{DC} | |
| ö | Control Voltage | EVPWM | MIN | | 0 | | 0.8 | V _{DC} | |
| 9 | External PWM | EI _{PWM} | MAX | PWM=100% | 0 | | 2 | mA_DC | |
| 9 | Control Current | LIPWM | MIN | PWM=30% | 0 | | 2 | mA_DC | |
| 10 | External PWM Duty Ratio | ED _{P\} | ŴМ | | 30 | | 100 | % | |
| 11 | External PWM Frequency | EF _{PWM} | | | 140 | 180 | 300 | Hz | |
| 12 | Internal PWM Control Voltage | IV _{PV} | /M | V _{DDB} =24V | 0 | | 3.3 | V_{DC} | |

 $(\,\text{Ta=25\pm5}^\circ\!\text{C}\,,\,\text{Turn on for 45minutes}\,)$

Note 1 : ADIM=Open or 1.6V; PDIM = Open/High

Note 2 : Duration = 20 ms



Master Board:

Connector 1: S14B-PH-SM3-TB(JST) or equivalent

| Pin No | Symbol | Description |
|--------|------------------------------------|--|
| 1 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 2 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 3 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 4 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 5 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 6 | BLGND | Ground and Current Return |
| 7 | BLGND | Ground and Current Return |
| 8 | BLGND | Ground and Current Return |
| 9 | BLGND | Ground and Current Return |
| 10 | BLGND | Ground and Current Return |
| 11 | ADIM ⁽¹⁾ | GND: 80%; Open/1.6V: 100%; High (3.3V) 110%, Luminance |
| 12 | VBLON | BL On-Off: Open/High (3.3V) for BL On as default |
| 13 | Pdim ⁽²⁾ | External PWM (AC Signal Control Duty); Internal PWM (DC Power Control Duty, 0~3.3V); Open/High (+3.3V, 100% Duty) for 100% |
| 14 | PDIM Selection ^(3,4) | GND: External PWM dimming; Open/High (3.3V): Internal PWM dimming. |

- Note (1) ADIM (amplitude dimming) is control signal for Inverter's output power to back light lamp bulb. Input signal should be able to control amplitude of Inverter output voltage. From 0V to 3.3V, Inverter output voltage should be able to vary to control brightness of lamp from 80% to 110% luminance variation. Approximate 1.6V might be 100% luminance control point.
- Note (2) PDIM is PWM duty control input for +3.3V TTL level signal or DC voltage by Pin 14 input. This input signal is (a) continuous pulse signal with +3.3V, TTL level signal spec, or (b) DC power with 0~3.3V. If this is Open or +3.3V, 100% duty (i.e. +3.3V, DC level), back light should perform 100% luminance. Duty ratio of this input signal should be proportional relationship in certain range of control without any kind of inherent side effect like waterfall effect on screen. Guaranteed duty range and dimming ratio should be specified with supplementary measurement result.
- Note (3) Pin 14 is the selection pin for PWM control method; if this pin is connected to GND, PDIM input of Pin 13 should have logic level duty signal for PWM control. If this is set to High or Open, Pin 13 should have DC level signal therefore the Inverter should have Saw Tooth Wave Generator to generate internal PWM signal. Default setting is "Not Connected", Pin 13 of PWM control should have DC Level signal for PWM.



| Note (4) | Pin 14 selection vs. | Pin 11/13 | control function table: |
|----------|----------------------|-----------|-------------------------|
| | | | |

| | Pin 11 (DC Power Control Duty Amplitude) Function Always Turn On Default: Open/1.6V: 100% | Pin 13 Default: Open/High: 100% |
|---------------------|--|---|
| Pin 14 = GND | GND: 80%; Open/1.6V: 100%; | External PWM (AC Signal Control Duty) |
| Pin 14 = Open/High | High (3.3V) 110%, Luminance | Internal PWM (DC Power Control Duty) |

Slave Board:

Connector 2: S12B-PH-SM3-TB(JST) or equivalent

| Pin No | Symbol | Description |
|--------|--------|---|
| 1 | Vddb | Operating Voltage Supply, +24V DC regulated |
| 2 | VDDB | Operating Voltage Supply, +24V DC regulated |
| 3 | VDDB | Operating Voltage Supply, +24V DC regulated |
| 4 | VDDB | Operating Voltage Supply, +24V DC regulated |
| 5 | VDDB | Operating Voltage Supply, +24V DC regulated |
| 6 | BLGND | Ground and Current Return |
| 7 | BLGND | Ground and Current Return |
| 8 | BLGND | Ground and Current Return |
| 9 | BLGND | Ground and Current Return |
| 10 | BLGND | Ground and Current Return |
| 11 | NC | |
| 12 | NC | |



3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

Vertical Frequency Range A (60Hz)

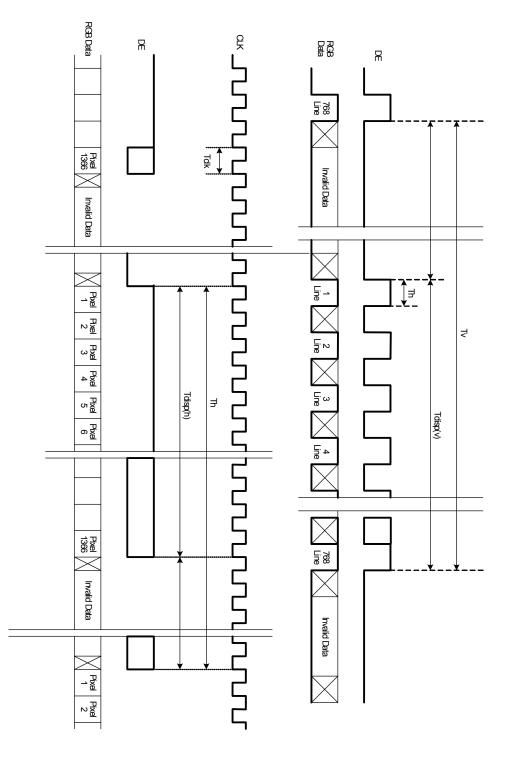
| Signal | Item | Symbol | Min | Туре | Max | Unit |
|---------------------|-----------|-----------|-------|------|-------|------|
| | Period | Τv | 778 | | 980 | Th |
| | Active | Tdisp (v) | _ | 768 | _ | Th |
| Vertical Section | Blanking | Tblk (v) | 10 | | 120 | Th |
| | Period | Th | 1414 | | 1936 | Tclk |
| | Active | Tdisp (h) | _ | 1366 | — | Tclk |
| Horizontal Section | Blanking | Tblk (h) | 48 | | 570 | Tclk |
| Clock | Period | CLK | 11.36 | | 15.38 | ns |
| CIUCK | Frequency | Freq | 60 | | 88 | MHz |
| Vertical Frequency | Frequency | Vs | 58 | 60 | 62 | Hz |
| Horizntal Frequency | Frequency | Hs | 45.76 | | 50.96 | KHz |

Vertical Frequency Range B (50Hz)

| | | | 1 | | | |
|---------------------|-----------|-----------|-------|------|-------|------|
| Signal | Item | Symbol | Min | Туре | Max | Unit |
| | Period | Τv | 778 | | 980 | Th |
| | Active | Tdisp (v) | _ | 768 | _ | Th |
| Vertical Section | Blanking | Tblk (v) | 10 | | 120 | Th |
| | Period | Th | 1414 | | 1936 | Tclk |
| | Active | Tdisp (h) | _ | 1366 | _ | Tclk |
| Horizontal Section | Blanking | Tblk (h) | 48 | | 570 | Tclk |
| Clock | Period | CLK | 13.51 | — | 18.52 | ns |
| CIOCK | Frequency | Freq | 54 | | 88 | MHz |
| Vertical Frequency | Frequency | Vs | 48 | 50 | 52 | Hz |
| Horizntal Frequency | Frequency | Hs | 37.87 | | 42.74 | KHz |



3-4 Signal Timing Waveforms



©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

| | | Input Color Data | | | | | | | | | | | | | | | | | | | | | | | |
|-------|------------|------------------|----|----|----|----|----|-------|----|----|----|------|----|----|----|----|----|----|----|----|----|----|----|----|----|
| Color | | RED | | | | | | GREEN | | | | BLUE | | | | | | | | | | | | | |
| | | MS | В | | | | | L | SB | MS | В | | | | | L | SB | MS | В | | | | | L | SB |
| | | R7 | R6 | R5 | R4 | R3 | R2 | R1 | R0 | G7 | G6 | G5 | G4 | G3 | G2 | G1 | G0 | B7 | B6 | B5 | B4 | В3 | B2 | B1 | В0 |
| | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(255) | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Basic | Blue(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Color | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | RED(000) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | RED(001) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| RED | | | | | | | | | | | | | | | | | | | | | | | | | |
| | RED(254) | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | RED(255) | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | GREEN(000) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | GREEN(001) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| GREEN | | | | | | | | | | | | | | | | | | | | | | | | | |
| | GREEN(254) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | GREEN(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | BLUE(000) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | BLUE(001) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| BLUE | | | | | | | | | | | | | | | | | | | | | | | | | |
| | BLUE(254) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| | BLUE(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

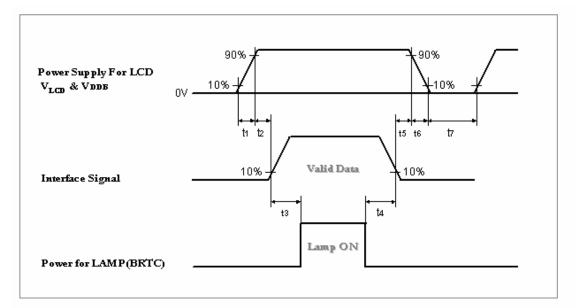
COLOR DATA REFERENCE

©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



3-6 Power Sequence

1. Power sequence of panel



| | | Units | | |
|-----------|---------------|-----------|-------|--------|
| Parameter | Min. | Typ. Max. | | Offics |
| t1 | 400 | - | 20000 | us |
| t2 | 0 | - | 50 | ms |
| t3 | 700 or (200)* | - | - | ms |
| t4 | 200 | - | - | ms |
| t5 | 0 | - | - | ms |
| t6 | 0.47 | - | 30 | ms |
| t7 | 0.3 | - | - | S |

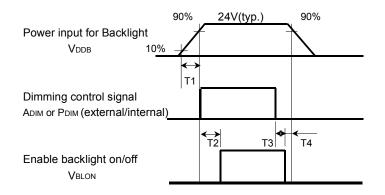
* : If t3=200ms, input black signal till 700ms from system is necessary. In case of t3<200ms, the abnormal display will be happened. But it will not damage timing controller.

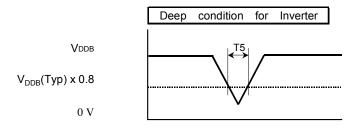
Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution : The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



2. Power sequence of inverter





| Parameter | | Units | | |
|-----------|------|-------|------|----|
| | Min. | Тур. | Max. | |
| T1 | 20 | - | - | ms |
| T2 | 500 | - | - | ms |
| Т3 | 0 | - | - | ms |
| T4 | 1 | - | - | ms |
| T5 | - | - | 10 | ms |



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

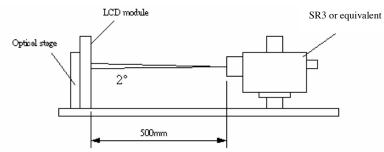


Fig.4-1 Optical measurement equipment and method

| Parameter | | Symbol | | Values | | Units | Notes | | |
|-----------|--------------------------------|-----------------------|----------|--------|-----------|------------|--------------------|--|--|
| | | | Min. Typ | | Max. | | | | |
| Contras | t Ratio | CR | 1000 | 1500 | | | 1 | | |
| Surface | Luminance, white | LWH | 400 | 500 | | ۔ cd/mُ | 2 | | |
| Lumina | nce Variation | δ _{wнiте} 5р | | | 1.25 | | 3 | | |
| Respon | se Time (Average) | Τγ | | 6 | | ms | 4,5 (Gray to Gray) | | |
| | Rise Time | Tr | | 15 | | ms | 4 | | |
| | Decay Time | Tf | - | 5 | | ms | 4 | | |
| Color C | oordinates | | | | 1 | | 1 | | |
| | RED | R _x | | 0.640 | | | 1 | | |
| | | R _Y | | 0.330 | | | 1 | | |
| | GREEN | G _X | | 0.290 | | - | | | |
| | | G _Y | Typ0.03 | 0.600 | Тур.+0.03 | | 1 | | |
| | BLUE | B _X | Typ0.03 | 0.150 | | | Τ | | |
| | | B _Y | | 0.060 | | | 1 | | |
| | WHITE | W _X | | 0.280 | | - | 1 | | |
| | | W _Y | | 0.290 | | | 1 | | |
| Viewing | Angle | | | | | | Contrast Ratio>10 | | |
| | x axis, right(φ =0°) | $	heta_{ m r}$ | | 89 | | Degree | 6 | | |
| | x axis, left(φ =180°) | θ_{\perp} | | 89 | | - | T | | |
| | y axis, up(φ =90°) | θu | | 89 | | | 1 | | |
| | y axis, down (φ =0°) | θ_{d} | | 89 | | | T | | |

©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



Note:

1. Contrast Ratio (CR) is defined mathematically as:

Contrast ratio (CR)= Brightness on the "white" state Brightness on the "black" state

 Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When VDDB = 24V, IDDB = 7.3A.
 L_{WH}=L_{on1}, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

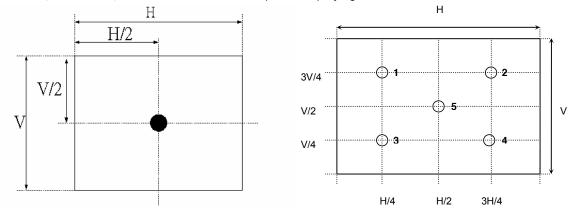
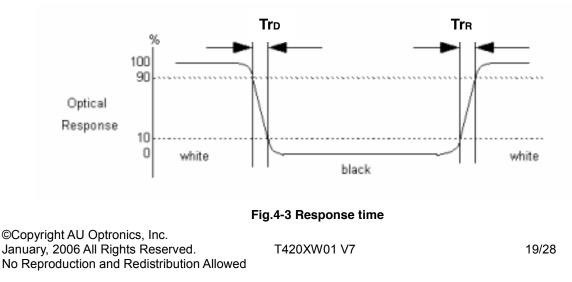


Fig.4-2 Optical measurement point

- 3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as: $\delta_{\text{WHITE(5P)}}$ =Maximum(L_{on1}, L_{on2},...,L_{on5})/Minimum(L_{on1}, L_{on2},...L_{on5})
- 4. Response time is the time required for the display to transition from white(L255) to black(L0) (Decay Time, Tr_D=Tf) and from black(L0) to white(L255) (Rise Time, Tr_R=Tr). For additional information see Fig. 4-3.





5. The response time is defined as the following figure and shall be measured by switching the input signal among 0%, 25%, 50%, 75%, 100% luminance. For additional information see Fig. 4-4.

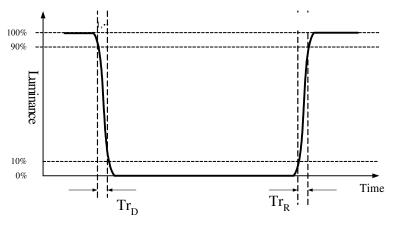


Fig.4-4 Response time

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-5. (Optical measurement by SR3)

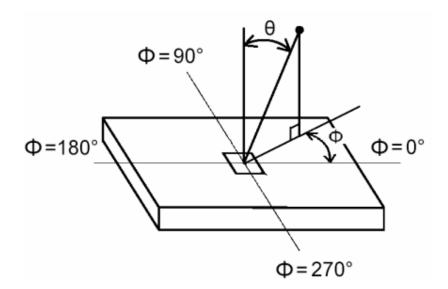


Fig.4-5 Viewing Angle Definition

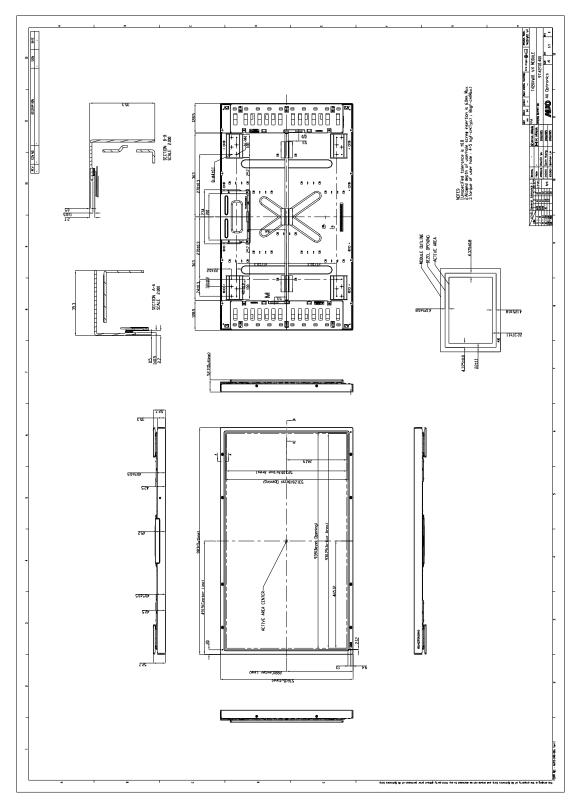


5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T420XW01. In addition the figures in the next page are detailed mechanical drawing of the LCD.

| | Horizontal (typ.) | 983.0mm | | |
|---------------------|-------------------|------------------------|--|--|
| Outline Dimension | Vertical (typ.) | 576.0mm | | |
| | Depth (typ.) | 52.7mm (with inverter) | | |
| Bezel Area | Horizontal (typ.) | 939.0mm | | |
| | Vertical (typ.) | 531.3mm | | |
| Activo Display Area | Horizontal | 930.25mm | | |
| Active Display Area | Vertical | 523.01mm | | |
| Weight | 15000g (Max.) | | | |
| Surface Treatment | AG, 3H | | | |





©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



| No | Test Item | Condition | | | | |
|-----------------|---------------------------------|--|--|--|--|--|
| 1 | High temperature storage test | Ta=60°C, 500hr judge | | | | |
| 2 | Low temperature storage test | Ta=-20 $^\circ\!\mathrm{C}$, 500hr judge (-25 $^\circ\!\mathrm{C}$, 500hr for reference) | | | | |
| | High temperature/High humidity | Ta=50°C, 80%RH, 500hr judge (interval 3min) | | | | |
| 3 | operation test | (40 $^{\circ}$ C, 95%RH, 500hr for reference) | | | | |
| 4 | High temperature operation test | Ta=50℃, 500hr judge | | | | |
| 5 | Low temperature operation test | Ta=-5℃, 500hr judge | | | | |
| | | -20C/1hr ~ 60C/1hr, 100cycle (-25C/1hr ~ 60C/1hr, | | | | |
| 6 Thermal shock | | 100cycle for reference) | | | | |
| | | Wave form: Random | | | | |
| 7 | Vibration test | Vibration level: 1.5G RMS, Bandwidth: 10-500Hz | | | | |
| | (non-operating) | Duration: X, Y, Z (1hr each direction) | | | | |
| | | Shock level: 50G | | | | |
| 8 | Shock test (non-operating) | Waveform: half since wave, 11ms | | | | |
| | | Direction: ±X, ±Y, ±Z (One time each direction) | | | | |
| | | Wave form: Random | | | | |
| 9 | Vibration test | Vibration level: 2.16G RMS, Bandwidth: 10~50Hz | | | | |
| | (with carton) | Duration: X=15min Y=15min, Z=60min (total 90min) | | | | |
| 10 | Drop test | Height: 46cm | | | | |
| 10 | (with carton) | 1 corner, 3 edges, 6 surfaces (ASTMD4169-I) | | | | |
| 11 | ESD | Level: Class C, Contact: ±20KV, Air: ±20KV | | | | |
| | ESP | 150pF, 330ohm, 1sec, 8 points/panel, 25 times/point | | | | |
| | | Test Method: JIS D0207 F3 | | | | |
| 12 | Dust test | Dust density: 100mg/m³, Dust blow time: 7.5min, Stop time: 7.5min | | | | |
| 12 | Dustiest | Test time: 8hrs (32 cycles) | | | | |
| | | Dust spec: 日本関東砂第八種(5~10 μ m), Dust pressure : 0.5kg/cm 2 | | | | |
| | | Test time: power on ~0.5hr, power off ~0.5hr | | | | |
| | | Test equipment: 1m between Microphone and panel | | | | |
| | | Judge criteria: | | | | |
| 13 | Cracking test | 1. (Back ground noise +3)dB $>$ Noise level, disregarded | | | | |
| | orability toot | 2. 23dB $>$ Noise level $>$ (Back ground noise +3)dB , total count 15 | | | | |
| | | 3. 23dB \leq Noise level < 30dB, total count < 5 times | | | | |
| | | 4. One time noise level \geq 30dB during testing should judge fail. | | | | |
| | | *Back ground noise shall be less than 19 dB(A) | | | | |

©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



7. International Standard

7-1. Safety

- UL6500, UL60065, Underwriters Laboratories, Inc. (AUO file number : E204356) Audio and video electronic apparatus, safety requirement.
- (2) CSA E60065, Canadian Standards AssociationAudio, video and similar electronic apparatus, safety requirement.
- IEC 60065 ver. 7th, European Committee for Electro technical Standardization (CENELEC) Audio, video and similar electronic apparatus, safety requirement

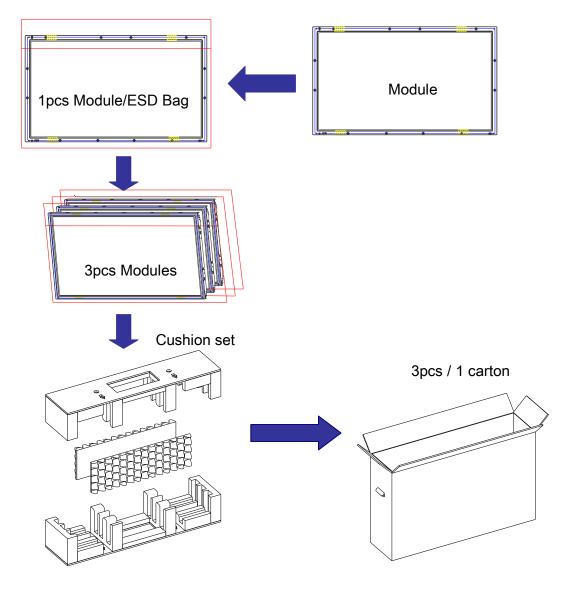
7-2. EMC

- (1) Use CISPR20.
- (2) Use FCC class B part15.



(1) Packing

Packing Instruction



- Package information:
- Carton outside dimension : 1087x285x716mm
- Carton/Package weight : 3kg
- Gross weight(per Box) : 48kg

©Copyright AU Optronics, Inc. January, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



Shipping label

| -7194-64222000 (30-LAAADT Rating : XV _{rm.} ; XA | Marvatectured XXXXXX Model No: T420XW01 All Optonics MADE IN TALMAN(MA) | VX IIZXXX | |
|--|--|--------------|--|
| | | | |

Green Mark Description:

For Pb Free products, AUO will add 🕑 for identification.

For RoHS compatible products, AUO will add **bulk** for identification.

Note: The Green Mark will be present only when the green documents have been ready by AUO

Internal Green Team. (The definition of green design follows the AUO green design checklist.)

Carton label



Pallet information

By air cargo : : (4x1) x2 layers, one pallet put 8 boxes, total 24 pcs module.

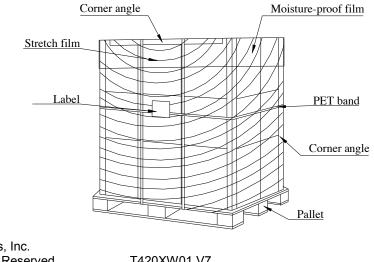
By sea : (4x1) x3 layers, one pallet put 12 boxes, total 36 pcs module.

Pallet dimension : 1150x1100x120mm

Pallet weight : 10kg

By air total weight : 48 kg/box X 8 boxes=384 kg (with pallet weight 394kg)

By sea total weight : 48 kg/box X 12 boxes=576 kg (with pallet weight 586kg)





(2) PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged on back side of panel.
- (2) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (3) You should adopt radiation structure to satisfy the temperature specification.
- (4) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (5) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (6) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference



shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.