

TFT-LCD PRODUCT A 2003.09.24 REVISION HISTORY		h setter		PRODUCT GROUP	REV.	ISSUE DATE
REV. ECN NO. DESCRIPTION OF CHANGES DATE PREPARED 0 Initial Release '03.06.25 D.J.LEE A E309-F008 Change Logic Current & Power (for ASIC Modify) '03.09.24 K.S.Shin		во <mark>еhydis</mark>		TFT-LCD PRODUCT	А	2003.09.24
0 AInitial Release'03.06.25D.J.LEE1) $I_{DD:}$ 33(mA) \rightarrow 80(mA)'03.09.24K.S.Shin				REVISION HISTORY		
AE309-F008Change Logic Current & Power (for ASIC Modify)'03.09.24K.S.Shin1) $I_{DD:} 33(mA) \rightarrow 80(mA)$	REV.	ECN NO.		DESCRIPTION OF CHANGES	DATE	PREPARED
	0		Change 1) I _{DD:} 3	Release Logic Current & Power (for ASIC Modify) 33(mA) → 80(mA)	, 03.06.25	5 D.J.LEE
SPEC. NUMBER SPEC. TITLE PAGE	SPEC.	NUMBER	SPEC.	TITLE		PAGE
						2 OF 20

	41.4	PRODUCT GROUP	REV.	ISSUE DAT	
во <mark>еhydis</mark>		TFT-LCD PRODUCT	А	2003.09.24	
		Contents			
No.		Items		Page	
1.0	General	Description		4	
2.0	Absolute	Maximum Ratings		6	
3.0	Electrica	7			
4.0	Optical S	8			
5.0	Interface Connection			10	
6.0	Signal T	Signal Timing Specifications			
7.0	Signal T	iming Waveform		14	
8.0	Power S	equence		16	
9.0	Reliabilit	ty Test		17	
10.0	Handling	g & Cautions		17	
11.0	Appendi	x		18	

SPEC. NUMBER	SPEC. TITLE		PAG	E
S864-1146	HT07W12-200 Product Specification	3	OF	20

	PRODUCT GROUP	REV.	ISSUE DATE
вое hydis	TFT-LCD PRODUCT	А	2003.09.24
Transistors) as active s measured active area w into RED, GREEN, Bl colors.	TION olor active matrix TFT-LCD unit using an witching devices. This unit has a 7 inch (18 ith resolutions (480 horizontal by 234 vertica LUE dots, which are arranged in vertical s CN1 VR, VG, VB	cm) diagonally (l pixel array). Ea	(Aspect ratio 16:9) ach pixel is divided
	V _{COM} / V _{DL} Vcom		
Mode 1,2,3 H _{SYNC} / V _{SYNC}			
BLK, POLS U/D, L/R, N/P Composite Sync	POLC CPH Hsync/Vsync U/D,L/R,N/P C. Sync	Sourc	e Driver IC
	Timing Controller Gate Driver IC VDD OE VDL VGL	48	D Panel 0 X 234 /L Lamp
	V _{BL} CN2		
 1.2 Features Slim, light weight and Wide view angle and Compatible with NT High brightness Image reversion: Up/ Multi Video display mages 1.3 Application CAR TV & Portable 	high contrast ratio SC and PAL system /Down and Left/Right node		
SPEC. NUMBER	SPEC. TITLE		PAGE
S864-1146	HT07W12-200 Product Specification		4 OF 20

Parameter	Specification	Unit	Remarks
Display Mode	Normally white		
Supply Voltage	$V_{COM}(9V), V_{DD}(5V), V_{GH}(18V),$	Volts	S/B Input
	$V_{GL}(-15V), V_{DL}(-10V)$	+ +	
Drive System	H-Line Inversion		
Number of pixels	480(H)*234(V)	Pixels	
Pixel Pitch	0.321(H)*0.372(V)	mm	
Pixel Arrangement	RGB Vertical stripe	Calara	
Display Colors	Full	Colors	
Effective Viewing Area	154.080*87.048	mm	
S-res Sizes1	7.0 (Diagonal)	Inch	TTL Level
Sync Signal Brightness	Hor./Ver. Sync Output for RGB	Cd / m ²	.Note 1
Outline Dimension	450 Typ. 167.0*102.0 (typ)	Mm	.11010 1
Thickness	12.5 max	mm	
Back-light	1CCFL(Note 2), Side-light type,' ⊏' type		
Weight	210max.	σ	
Surface Treatment	Anti-Glare and Hard Coating	g	
lote: 1. Non-Operating Con lote: 2. CCFL (Cold Catho			

REV.

ISSUE DATE

SPEC. NUMBER	SPEC. TITLE		PAGE	
S864-1146	HT07W12-200 Product Specification	5	OF	20

вое**hydis**

	PRODUCT GROUP	REV.	ISSUE DATE
BOE hydis	TFT-LCD PRODUCT	А	2003.09.24

2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

2.1 Environmental

Parameter	Symbol	Min.	Max.	Unit	Remarks
Operation	T _{OP}	-20	70	C	If humidity is larger than 45RH,
Temperature	RH		45	%	temperature should be kept lower
(Humidity)	T _{OP}		45	C	than 45° C.
	RH		90	%	
Storage Temperature	T _{ST}	-30	85	Ĉ	
(Humidity)	RH		45	%	

 \langle Table 2. Environmental Maximum Specifications \rangle

2.2 Source Board Input

⟨ Table 3. Source Board Input Maximum Specifications ⟩

Parameter	Symbol	Min.	Max.	Unit	Remarks
VCOM Driving Voltage	V _{COM}	-10.0	9.0	V	
Logic Voltage	V _{DD}	-0.3	6.0	V	Ta= 25℃
Source Driver Voltage	V_{EE}	-0.3	7.0	V	$V_{CC1} = +12V$
Logic Signal Voltage	V _{IN}	-0.3	V _{DD} +0.3	V	$V_{CC2} = +12V$
Analog Input Voltage	V _{ANA}	-0.3	V_{EE} +0.3	V	Z=75Ω
Gate High Voltage	V _{GH}	0.3	40.0	V	Ta= 25°C
Gate Low Voltage	V _{GL}	-20.0	+0.3	V	$V_{CC} = +12V$

Note: Source board input is supplied by Video board, so, customer doesn't need to adjust these values. This table is reference if customer uses their own video board design.

SPEC. NUMBER	SPEC. TITLE		PAGE
S864-1146	HT07W12-200 Product Specification	6	OF 20

_		
BO	≡hydis	

TFT-LCD PRODUCT

2002.00

REV.

А

2003.09.24

ISSUE DATE

3.0 ELECTRICAL SPECIFICATIONS

3.1 Driving Condition for TFT-LCD Panel

						$[Ta = 25 \pm 2^{\circ}C]$
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply Voltage for Logic	V _{DD}		5.0		V	
Supply Current for Logic	I _{DD}		80.0		mA	
Supply Voltage for COM	V _{COM}		9.0		V	
Supply Current for COM	I _{COM}		8.5		mA	
Community V 14 and fam	V _{GH}		+18		V	Gate High
Supply Voltage for Gate Driver	V _{GL}		-15		V	Gate Low
Gale Dilver	V _{DL}		-10		V	Gate Ref.
	I _{GH}		0.1		mA	Gate High
Supply Current for Gate Driver	I _{GL}		2.8		mA	Gate Low
	I _{DL}		8.5		mA	Gate Ref.
LCD Panel Power Consumption	P _{LCD}		0.7		W	
Analog R,G,B	R,G,B		5.0		Vpp	
Horizontal Sync	H _{SYNC}		5.0		Vpp	
Vertical Sync	V _{SYNC}		5.0		Vpp	
Composite Sync	C _{SYNC}		5.0		Vpp	
	L/R,U/D,N/P,	+4.7	5.0	-	V	Level "H
Input s/w signal	SSW, VSW, MODE1~3	-	_	+0.2	V	Level "L
Vertical sync frequency	F _V	58.0	59.94	62.00	Hz	@ NTCC
Horizontal sync frequency	F _H	15.2	15.73	16.20	KHz	@ NTSC

SPEC. NUMBER	SPEC. TITLE		PAG	E
S864-1146	HT07W12-200 Product Specification	7	OF	20

	PRODUCT GROUP				REV.	ISSUE DATE
BOEhydis	TFI	T-LCD PRO	DUCT		А	2003.09.24
3.2 Driving Condition for BLU						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
Backlight Lamp Voltage	V _{BL}		970		Vrms	
Backlight Lamp Current	I _{BL}		4.0	4.3	mA rms	
Lamp Operating Frequency	F _L	30		80	KHz	
Lamp Start Voltage				1700	Vrms	3 0
Lamp Start Voltage				1360	Vrms	25°C
Lamp Life		20,000	30,000		Hours	IBL=4.0 mA
BLU Power Consumption	V _{BL}		4.0		W	

4.0 OPTICAL SPECIFICATIONS

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25 ± 2 °C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and ϕ equal to 0°. We refer to $\theta_{\phi=0}$ (= θ_3) as the 3 o' clock direction (the "right"), $\theta_{\phi=90}$ (= θ_{12}) as the 12 o' clock direction ("upward"), $\theta_{\phi=180}$ (= θ_9) as the 9 o' clock direction ("left") and $\theta_{\phi=270}$ (= θ_6) as the 6 o' clock direction ("bottom"). While scanning θ and/or ϕ , the center of the measuring spot on the display surface shall stay fixed. The measurement shall be executed 30 minutes after lighting at rating with the back-light CCFL being run at a 4.0 mA current after 30 minutes warm-up period. Vdd shall be 5.00±0.2V at 25°C. Optimum viewing angle direction is 6 o' clock. The test setup, geometry, and measurement location are shown in FIGURE 1. and FIGURE 2. (shown in Appendix)

SPEC. NUMBER	SPEC. TITLE		PAG	E
S864-1146	HT07W12-200 Product Specification	8	OF	20



TFT-LCD PRODUCT

A

2003.09.24

4.2 Optical Specifications

< Table 4. Optical Specifications >									
[Be measured at Composite NTSC/PAL, Ta = 25 ± 2 °C									
Para	meter	Symbol	Condition	Min	Тур	Max	Unit	Remark	
	Horizontal	Θ_3	CD 10		60	-	Deg		
Viewing	Horizontai	Θ_{9}	CR > 10 IL = 4.0mA		60	-	Deg	Note 1	
Angle	Vertical	Θ_{12}	Operation		40	-	Deg	NOIC I	
	ventical	Θ_{6}			50	-	Deg		
Luminance	Luminance of white		$\Theta = 0^{\circ}$		450		cd/m^2	Note 2	
White Chron	maticity	Wx	IL = 4.0 mA		0.295			Note 3	
white Child	matienty	Wy	NonOperation		0.315			Note 5	
Luminance	contrast ratio	CR		150	250	-		Note 4	
White lumin uniformity	White luminance uniformity		$\Theta = 0^{\circ}$ IL = 4.0mA			1.2		Note 5	
Response tir	me	Tr	Operation	_	10	20	ms	Note 6	
Kespolise til		Td		_	25	50		Note 6	

Note

1. Viewing angle is the angle at which the contrast ratio is greater than 5. The viewing angles are determined for the horizontal or 3, 6 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1 shown in Appendix)

2. Average luminance of white is defined as arithmetic mean of one point across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

3. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue, and white measurements shall be made at the center of the panel.

4. Contrast measurements shall be made at viewing angle of $\theta = 0^{\circ}$ and at the center of the LCD surface. See FIGURE 1 (located in Appendix). Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. Luminance Contrast Ratio (CR) is defined mathematically as : CR = Luminance when displaying a white raster / Luminance when displaying a black raster.

5. The white luminance uniformity on LCD surface is then expressed as : $\Delta Y = Maximum$ luminance of five points / Minimum luminance of five points.

6. The electro-optical response time measurements shall be made as shown in FIGURE 3 (shown in Appendix) by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

SPEC. NUMBER	SPEC. TITLE		PAGI	E
S864-1146	HT07W12-200 Product Specification	9	OF	20

	⊨hydis			PRODUCT G	ROUP	REV	.]	ISSUE DAT	
BC	enyuis		TFT-LCD PRODUCT A					2003.09.24	
INTE	RFACE	CONNI	ECTIO	DN					
	e I/O inte	erface: C	N1:Ir	terface Connector : GF05	53-30S-LSS (LG) of	r equivale	ent		
Pin No.	Sym	bol		Description	SPEC		I/O	Remark	
1	N	С		No Connection	-				
2	Analog l	Red	А	nalog Red Video Input	5Vpp		Ι		
3	Analog (Green	An	alog Green Video Input	5Vpp		Ι		
4	Analog I	Blue	Aı	nalog Blue Video Input	5Vpp		Ι		
5	GN	1D		Ground	0 V		-		
6	N	С		No Connection	_				
7	N	С		No Connection	_				
8	V _C	ОМ	Co	mmon Electrode Voltage	DC 9V		Ι		
9	N	С		No Connection	_				
10	Moo	de 1	Di	splay Mode Selection 1	High(5V) / Low	/(0V)	Ι	TTL Level	
11	Moo	de2	Di	splay Mode Selection 2	High(5V) / Low	/(0V)	Ι	TTL Level	
12	Moo	de3	Di	splay Mode Selection 3	High(5V) / Low	/(0V)	Ι	TTL Level	
13	GN	1D		Ground	0 V		-		
14	BL	K	Z	Coom in Control Signal	High(5V) / Low	/(0V)	0		
15	H _{SY}	NC	Н	lorizontal Sync Output	5Vpp		0	TTL Level	
16	V _{SY}	/NC		Vertical Sync Output	5Vpp		0	TTL Level	
17	N	С		No Connection	_		-		
18	POI	LS	Data	Polarity Alteration Signal	High(5V) / Low	/(0V)	0	TTL Level	
19	POI	LC	V _{COM}	Polarity Alteration Signal	High(5V) / Low	/(0V)	0	TTL Level	
20	N	С		No Connection	-		-		
21	U/	D	Sc	an(Up/Down) Selection	0V: Normal / 5V: 0	Opposite	Ι	TTL Level	
22	L/	R	Sc	an(Left/Right) Selection	0V: Normal / 5V: 0	Opposite	Ι	TTL Level	
23	N/	P		NTSC/PAL Selection	0V: PAL / 5V: N	NTSC	Ι	TTL Level	
24	N	С		No Connection	-		-		
25	Csy			Composite Sync	5Vpp		Ι		
26	GN	1D		Ground	0 V		-		
27	V	D		Logic Supply Voltage	DC 5V		Ι		
28	Ve	н		Gate High Voltage	DC 18V		Ι		
29	VI		Gate	Driver Reference Voltage	DC -10V		Ι		
30	Vo			Gate Low Voltage	DC -15V		Ι		
.2 Lan	np connec	tion(CN2	-	nnector : BHR-03VS-1 (JS r side Connector : 2 pins,	· •	1 TR (IS	T) or (equivalent	
Pin	No	Input		Function				Color	
1 111	110.	HI	Po	wer Supply for BackLigh		a)		Pink	
	∟)	GND		Power Supply for BackLig				/hite	
2			1						
SPI	C. NUN	/IBER		SPEC. TITLE				PAGE	
	S864-11			SPEC. TITLE HT07W12-200 Product Specification				10 OF 2	



TFT-LCD PRODUCT

REV.

А

2003.09.24

ISSUE DATE

5.3 Display mode

Mode		Input		Display (4:3 aspect-ratio	Note	
-	MODE1	MODE2	MODE3	input signal)		
Full	н	н	Н		Input signals are diffull screen(16:9)	splayed on
Zoom1	L	Н	Н		Central 176 lines of are displayed on ful (vertical extension z factor=4/3)	ll screen
Zoom Wide1	Н	L	Н		Central 176 lines of are displayed on ful different horizontal	l screen with
Normal	L	L	Н		Input signals(4:3) a on center 75% scree	
Zoom2	Н	Н	L		Lower 205 lines of i are displayed on ful (vertical extension z factor=8/7)	ll screen
Wide	L	Н	L		Input signals are diffull screen with diff horizontal timing sc	erent
Zoom Wide2	Н	L	L		Lower 205 lines of input signals are displayed on full screen with different horizontal timing scaling	
Zoom3	L	L	L		Center 205 lines of i are displayed on ful (vertical extension z factor=8/7)	ll screen
					PAGE 11 OF 2	

во	≡hydis

TFT-LCD PRODUCT

2002.00.2

REV.

А

ISSUE DATE

6.0 SIGNAL TIMING SPECIFICATION

6.1 Display Time Range

ITEMS	Cor	ndition		Display	y Mode		Unit
TTEMI5	CO	luiuon	Full	Zoom1	Zoom wide	Normal	Om
	NTSC	ODD	24	53	53	24	Line
Vertical	NISC	EVEN	287	316	316	287	Line
Display Start	PAL	ODD	29	62	62	29	Line
	FAL	EVEN	342	375	375	342	Line
Vertical	Ň	ГSC	234	176	176	234	Line
Display Position	PAL		281	210	210	281	Line
Horizontal	Ň	ГSC	12.96	12.96	12.96	8.83	μs
Display Start	Р	AL	13.86	13.86	13.86	9.85	μs

ITEMS	Cor	dition		Displa	y Mode		Unit
TTENIS	COL	luiuon	Zoom2	Wide	Zoom Wide2	Zoom3	Ullit
	NTSC	ODD	50	24	50	38	Line
Vertical	NISC	EVEN	313	287	313	301	Line
Display Start	DAI	ODD	65	29	65	48	Line
	PAL	EVEN	378	342	378	361	Line
Vertical	N	ТSC	205	234	205	205	Line
Display Position	Р	AL	239	281	239	239	Line
Horizontal	N	ТSC	12.96	12.96	12.96	12.96	μs
Display Start	Р	AL	13.86	13.86	13.86	13.86	μs

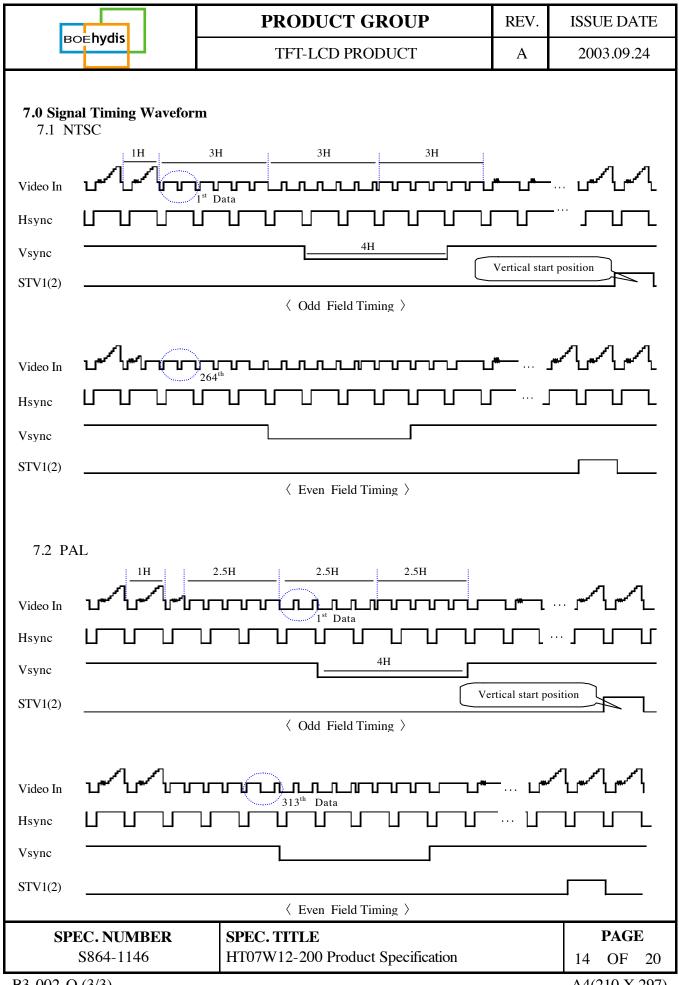
SPEC. NUMBER	SPEC. TITLE	PAGE
S864-1146	HT07W12-200 Product Specification	12 OF 20

	PRODUCT GROUP	REV.	ISSUE DATE
BOE hydis	TFT-LCD PRODUCT	А	2003.09.24

6.2 H / V Timing Specifications

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Vertical sync. Width	tVSYN		4		tH	
Vsync period	tV		16.7		ms	
Csync period	tH	61.5	63.5	65.5	μs	
Csync pulse width	t _{CSYN}	4	4.7	5.4	μs	
Hsync pulse width	tHSYN		4.64		μs	
Csync to Hsync time	t2	1.8	2.5	15.8	μs	

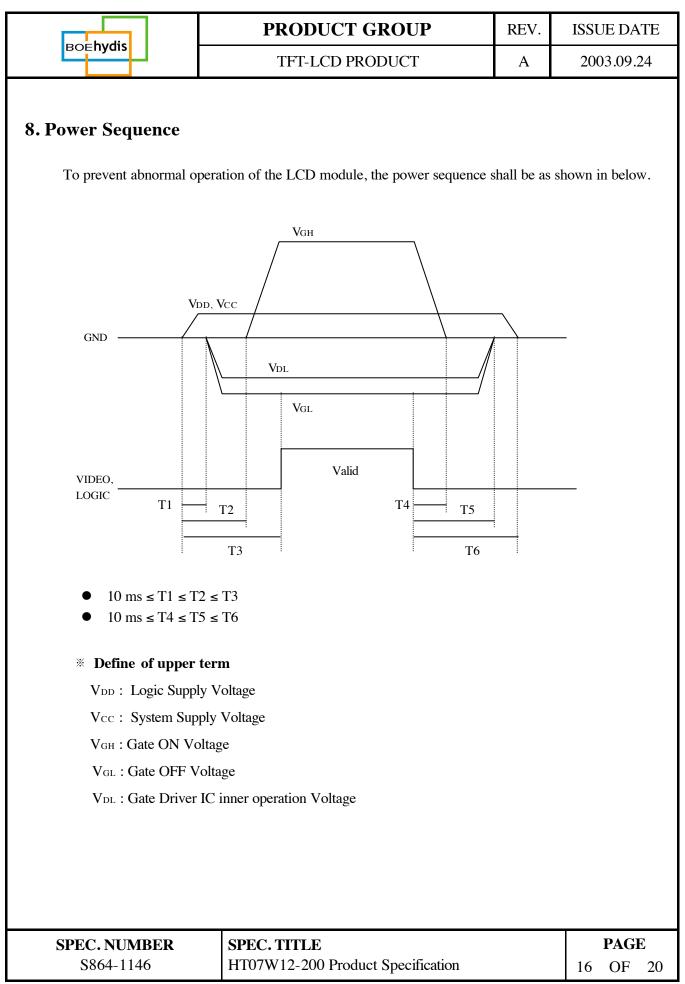
SPEC. NUMBER	SPEC. TITLE	PAGE		
S864-1146	HT07W12-200 Product Specification	13 OF 20		



B3-002-O (3/3)

A4(210 X 297)

	PRODUCT GROUP	REV.	ISSUE DATE		
во <mark>еhydis</mark>	TFT-LCD PRODUCT A		2003.09.24		
7.3 Vertical Timing W					
VSYNC					
	tV				
7.4 Horizontal Timing					
CSYNC	tCSYN				
	tH				
HSYNC	HSYN				
SPEC. NUMBER	SPEC. TITLE		PAGE		
S864-1146	HT07W12-200 Product Specification		15 OF 20		

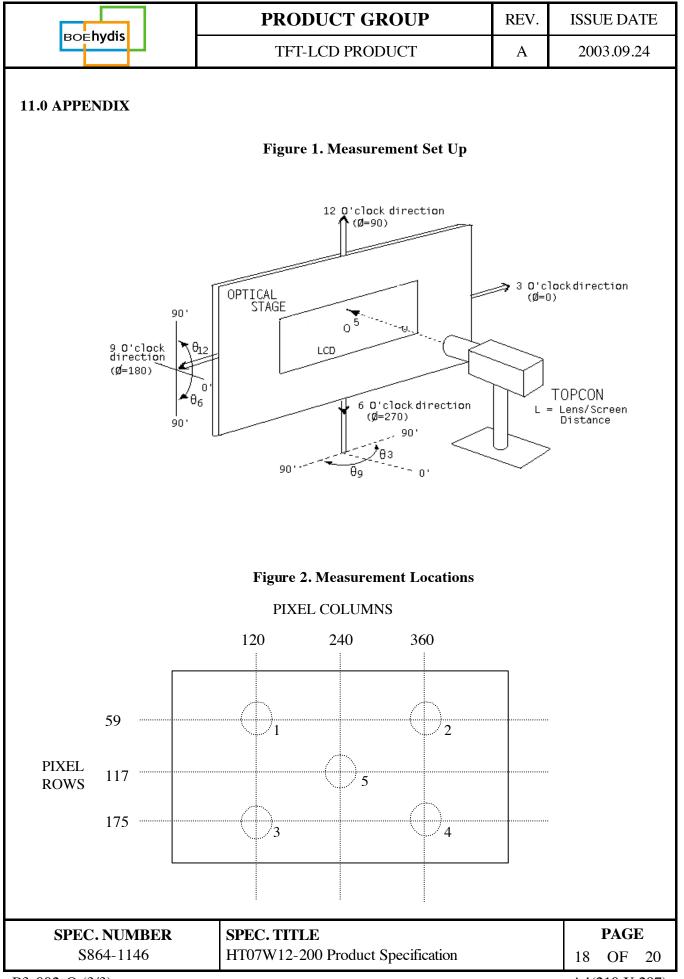


PRODUCT GROUP REV. **ISSUE DATE** во<mark>еhydis</mark> **TFT-LCD PRODUCT** A 2003.09.24 9. RELIABLITY TEST The Reliability test items and its conditions are shown in below. \langle Table 6. Reliability Test Parameter \rangle Conditions No Test item High temperature storage test Ta=85°C, 192hrs 1 2 Low temperature storage test Ta=-30°C, 192hrs High temperature & high humidity 3 Ta=45°C, 90%RH, 192hrs operation test High temperature operation test 4 Ta=70°C, less than 45RH, 192hrs 5 Low temperature operation test Ta=-20°C, 192hrs Thermal shock 6 Ta=-30°C (30m)↔85°C (30m):100 cycles Frequency : $10 \sim 55$ Hz Amplitude : 1.5mm 7 Vibration test Sweep: 11mins 6Cycles for each direction of X, Y, Z 8 Shock Test 100G, 6ms, Direction: ±X, ±Y, ±Z 2Times 9 Endurance of atmospheric pressure 0.5AT,2hrs **10.0 HANDLING & CAUTIONS** (1) Cautions for handling the module As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible. As the LCD panel and back-light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided. As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning. Do not pull the interface connector in or out while the LCD module is operating. Handle connectors and cables with care.

(2) Other cautions

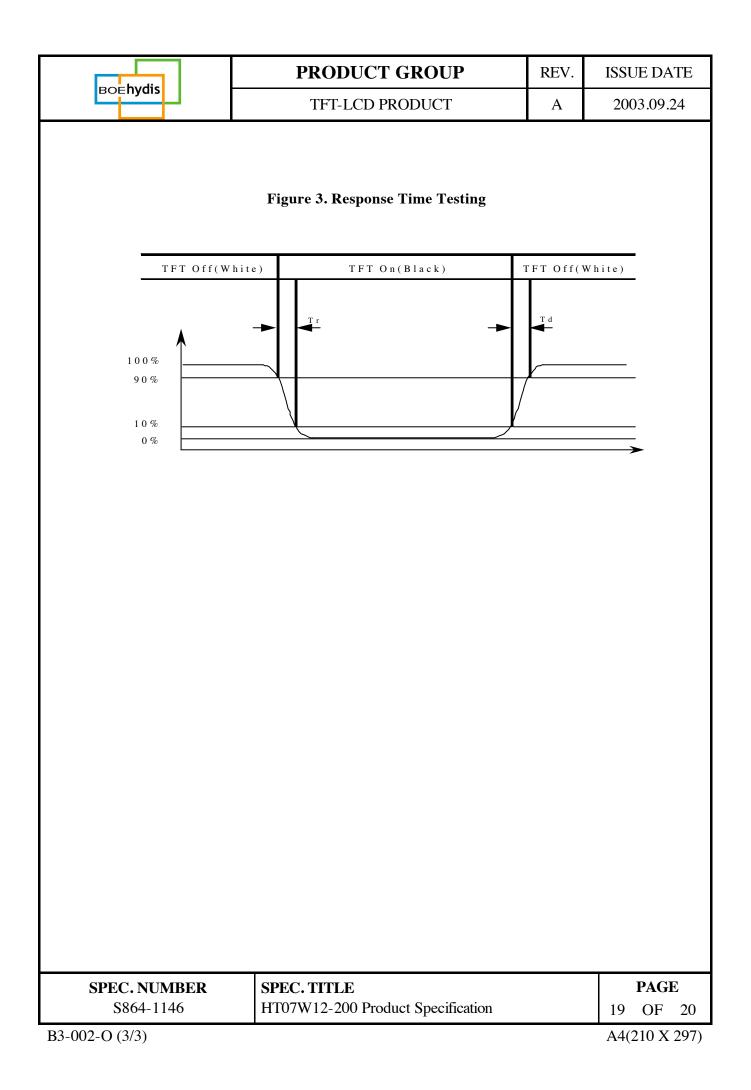
- Do not disassemble and/or re-assemble LCD module.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend using the original shipping packages.

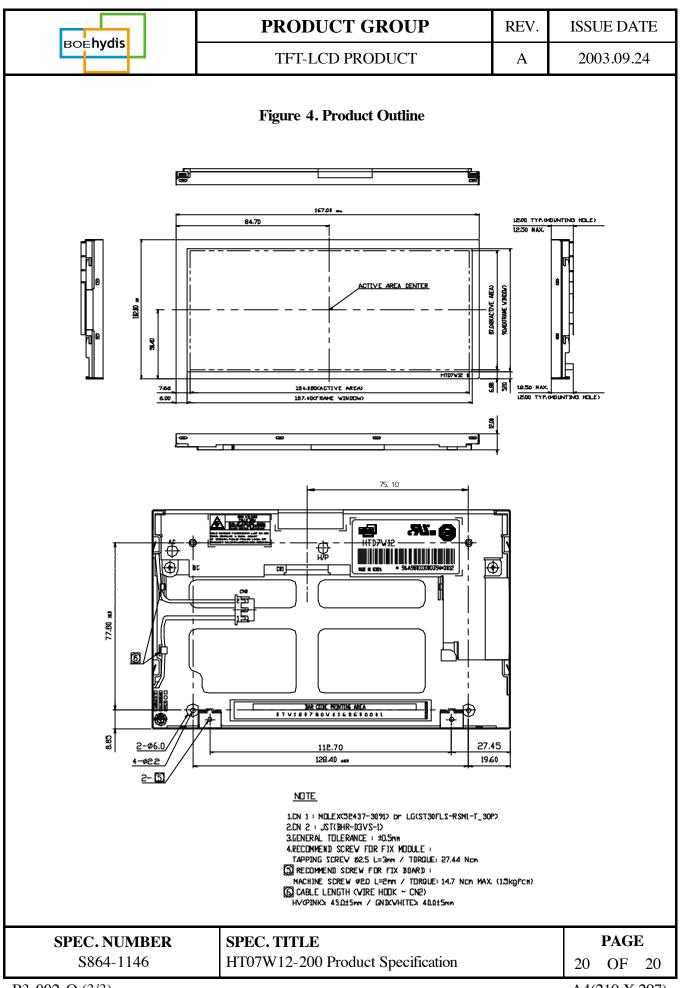
SPEC. NUMBER	SPEC. TITLE		PAGE		
S864-1146	HT07W12-200 Product Specification	17	OF	20	



B3-002-O (3/3)

A4(210 X 297)





A4(210 X 297)