

# \* Records of Revision \*

Rev.	Page	Description of changes	Date	prepared by
0	All	Original Release	10.03.12	Mu.J.F
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☑ 一般事项 特殊事项内容:	□ 特殊事项	

Model	BTL222432-305L	2/26	PRODUCT SPECIFICATION



## \* Contents \*

- 1. Features
- 2. Mechanical Specifications
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- 4. Electrical Characteristics
- 5. Recommended Software Setting Value (LDI: HX8347-G)
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#### 1. Features

The features of BTL222432-305L are as follows

\* Display mode : TFT 260K Colors, Transmissive, Normally White

\* Driving Condition : 240x3Ch-Source / 320Ch-Gate

\* Connection :ZIF Type (39pins,Hirose:FH26-39S-0.3SHW)

\* LCD Driver & Control IC

:HX8347-G(HIMAX)

\* Back Light : White LED Back Light (3 Chips in Parallel)

\* MPU Interface : 80-Series, 16bits/8bits Parallel Data Bus

\* Type of Surface Contion

:Clear Type

## 2. Mechanical Specifications

Item		Specification	Unit	
Resolution	Main	Main 240( x RGB) x 320		
Resolution	Sub	Sub NA		
LCM Outline Deme	nsion	55.10 x40.90x2.05(TYP)	mm	
Active Area (M. v. II)	Main	33.84x 45.12	, ma ma	
Active Area (W × H)	Sub	NA	mm	
Divol Ditab (M v. II)	Main	0.141×0.141	ma ma	
Pixel Pitch (W x H)	Sub	NA	mm	
Viewing Direction	Main	6	Olalask	
(Human Eye)	Sub	NA	O'clock	
Gray Scale Inversion Direction	Main	12	O'clock	
(Contrast Ratio)	Sub	NA	(Rubbing Direction)	
Weight		12	g	

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# 3. Absolute Maximum Ratings

(Ta=25<sup>°</sup>C Note1)

Items	Symbol	Min.	Max.	Unit	Remark
Logic voltage	l <sub>ovcc</sub>	-0.3	4.6	V	
Analog voltage	V <sub>CI</sub>	-0.3	4.6	V	
Input signal voltage	V <sub>IN</sub>	-0.3	IOVCC+0.5	V	
LED forward current	I <sub>LED</sub>	-	20	mA	For each LED
Operation temeprature	T <sub>OPR</sub>	-20	70	°C	
Storage temperature	T <sub>STG</sub>	-30	80	°C	
Humidity (ambient		Ta≤60°C	90% F	RH Max.	

Note1: Device is subject to be damaged permanently,

if stresses beyond those absolute maximum ratings listed above.

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## 4. Electrical Characteristics

Main Ta=25℃

Iten	าร	Symbol	Min.	Тур.	Max.	Unit	Remark
Logic voltage		l <sub>ovcc</sub>	1.65	1.8	3.6	٧	
Analog(Pow	Analog(Power) voltage		2.5	2.8	3.6	٧	
Gate	High level	$V_{GH}$	12	-	18	٧	Note 1
voltage	Low level	$V_{GL}$	-10	-	-7	٧	Note i
Input signal	High level	V <sub>IH</sub>	0.7×IOVcc	-	IOVcc	٧	
voltage	Low level	V <sub>IL</sub>	0	-	0.3×IOVcc	٧	
current con	sumption	Icc	-	6	9	mA	Note 2

Note 1) The value can be adjusted by software to optimize display quality Note 2) Display Black Pattern

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## 5. Recommended Software Setting Value (LDI: HX8347-G)

#### **Initial Code**

iiiitiai ooac					
INDEX	DATA			Into Standby M	<b>l</b> lode
Hardware Reset POV		POWER VOTA	GE SETTING	INDEX	DATA
00EA	0000	001B	001B	0028	0038
00EB	0020	001A	0001	DELA	Y 50ms
00EC	003C	0024	0070	001F	0089
00ED	00C4	0025	0058	DELA	Y 50ms
00E8	0048	0023	005A	0028	0020
00E9	0038	POWE	POWER ON DELAY 5		Y 50ms
00F1	0001	0018	0036	0019	0000
00F2	8000	0019	0001	DELA	Y 10ms
000C	0000	0001	0000	0001	00C0
000D	0000	001F	0088		
000F	0000	DELAY	10ms		
GAMMA (	Correction	001F	0800		
0040	0001	DELAY	10ms	Exit Star	ndby Mode
0041	0007	001F	0090	0001	0000
0042	0016	DELAY	10ms	DELAY 10ms	
0043	001E	001F	00D0	0019	0001
0044	001B	DELAY	10ms	001F	0088
0045	0026	SET P	SET PANEL		Y 10ms
0046	0025	0017	0005	001F	0080
0047	006F	0036	0009	DELA'	Y 10ms
0048	0002	0028	0038	001F	90
0049	001B	DELAY 50ms		DELA'	/ 10ms
004A	001D	0028	003C	001F	D0
004B	001B	SET GRAM AREA		DELA	Y 10ms
004C	0016	0002	0000	0028	0038
0050	0019	0003	0000	DELA'	7 50ms
0051	0024	0004	0000	0028	003C
0052	0021	0005	00EF		
0053	0029	0006	0000		
0054	0038	0007	0000		
0055	003E	0008	0001	-	
0056	0010	0009	003F		
0057	005A			Partial Display	Setting
0058	0009	0022		INDEX	DATA
0059	0004			0002,0003	StartX
005A	0002			0004,0005	EndX
005B	0004			0006,0007	StartY
005C	001D			0008,0009	EndY
0050	0000	1			•

NOTE: BOE requires the customer to follow the above instructions strictly. If customer would like to change the above instructions, the customer should inform BOE and get re-check from BOE, or the customer will be responsible for any unexpected result because of the change.

00CC

005D

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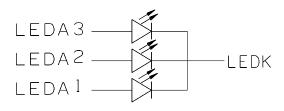
## 6. Back Light System Characteristics

Ta=25°C

Items	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward current	lf	-	15	20	mA	Note1
Forward voltage	Vf	3.0	-	3.4	V	Note1
B/L Power consumption	P <sub>BL</sub>	-	-	204	mW	Note2

Note 1: The Driving conditon is defined for each LED chip.

Note 2: The B/L Power consumption is defined for the backlight module.the schematic drawing of the backlight module as the figure.



LED CIRCUIT

Ref. Total power consumpation(max) depends on LED current/ LED driver efficiency, etc.

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# 7. Optical Characteristics

#### **Transmissive Mode**

Ta=25℃

It	:em	Sy	mbol	Min.	Тур.	Max.	Unit	Condition	Note	
			Ø=0° (X1)	ı	45	-		Cr > 10		
Viewi		Viewing Angle		Ø=180° (X2)	ı	45	-		4	Note2
viewi	ng Angle	θ	Ø=90° (Y1)	ı	50	=	deg.		NOIGZ	
			Ø=270° (Y2)	ı	20	-				
	rast ratio missive)		Cr	170	320	-	1	θ = 0	Note1 Note4	
Respo	nse Time	Tı	- + Tf	-	30	-	ms	θ = 0	Note3	
0.15	R	(	(x,y)	0.57,0.29	0.61,0.33	0.65,0.37				
CIE Coordi	G	(	(x,y)	0.31,0.51	0.35,0.56	0.39,0.60		$\theta = 0$		
- nate	В	(	(x,y)	0.11,0.06	0.15,0.10	0.19,0.14		$\emptyset = 0$		
	W	(	(x,y)	0.26,0.29	0.30,0.33	0.34,0.37				
Brig	htness		L	200	250	-	cd/m2	15mA/LED	Note5	
Unif	ormity			70	-	-		15mA/LED	Note6	

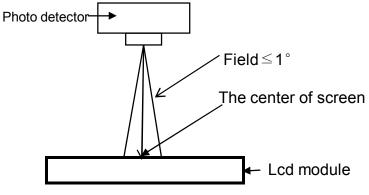
<sup>\*</sup> Ø = 0  $^{\circ}\,$  , Ø = 90  $^{\circ}\,$  ,Ø = 180  $^{\circ}\,$  ,Ø = 270  $^{\circ}\,\,$  means viewing direction.

<sup>\*</sup> B/L is turned on.

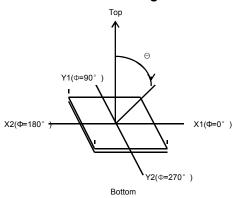


The optical characteristics should be measured in dark room, and after 5 minutes operation, the measurment begin.

Note1. Definition of Measure System



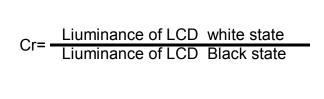
Note2. Definition of Angle O.



Note3. Definition of Response Time

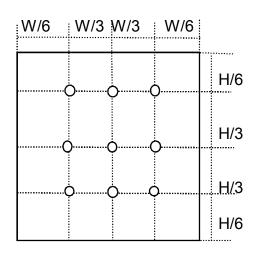
White(TFT off) Black(TFT on) White(TFT off)

Note4.definition of contrast ratio



Note 6. definition of Uniformity

Note 5. Measuring Point(9 Points) (WxH)

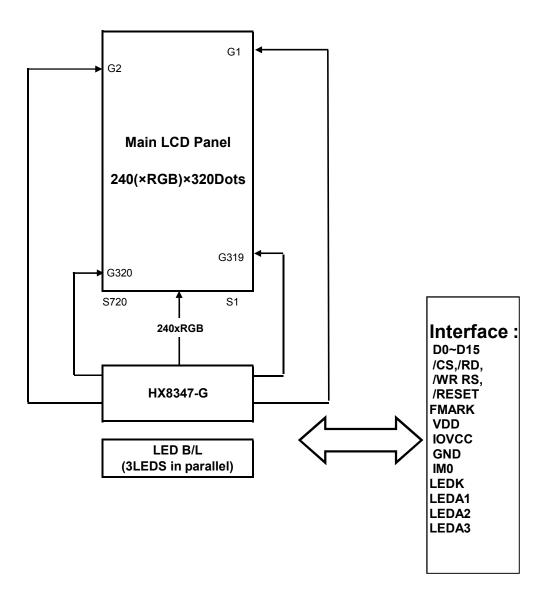


Iniformity= max. Liuminance of measurede point

Rating is defined as the average brightness inside the viewing area



## 8. Block Diagram



IM0=0	80-Series, 16bits Parallel Data Bus (DB0DB15)
IM0=1	80-Series, 8bits Parallel Data Bus (DB8DB15)

Model BTL222432-305L 11/26 PRODUCT SPECIFICATION



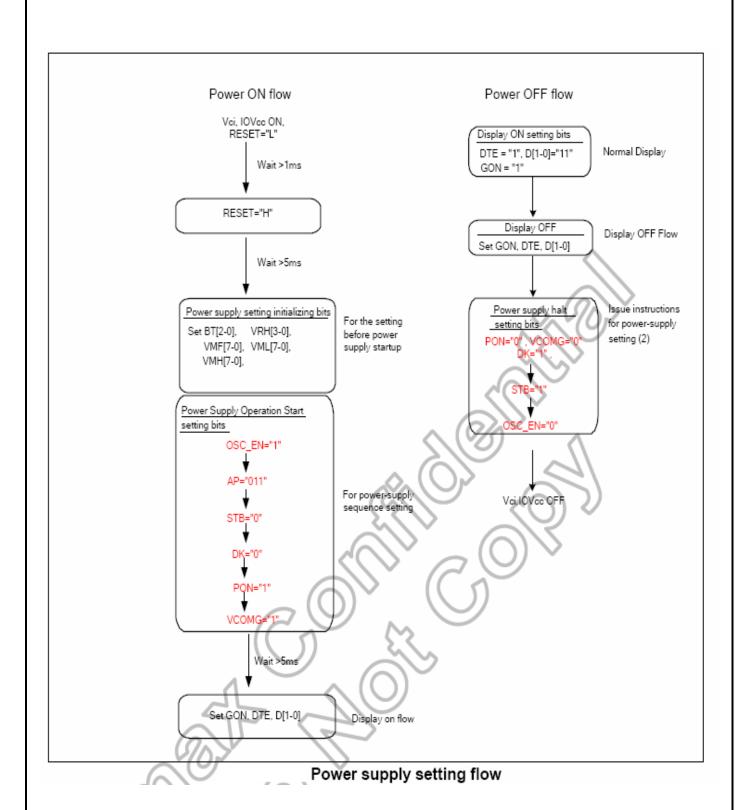
# 9. Interface Pin Assignment

No	Symbol	Description
1	GND	Ground
2	/CS	Chip Select Signal(low active)
3	RS	RS Signal (RS=0:Control, RS=1:data)
4	/WR	Write Signal (low active)
5	/RD	Read Signal(low active)
6	DB0	Ground
7	DB1	Bi-directional (I/O) Data Line
8	DB2	Bi-directional (I/O) Data Line
9	DB3	Bi-directional (I/O) Data Line
10	DB4	Bi-directional (I/O) Data Line
11	DB5	Bi-directional (I/O) Data Line
12	DB6	Bi-directional (I/O) Data Line
13	DB7	Bi-directional (I/O) Data Line
14	DB8	Bi-directional (I/O) Data Line
15	DB9	Bi-directional (I/O) Data Line
16	DB10	Bi-directional (I/O) Data Line
17	DB11	Bi-directional (I/O) Data Line
18	DB12	Bi-directional (I/O) Data Line
19	DB13	Bi-directional (I/O) Data Line
20	DB14	Bi-directional (I/O) Data Line
21	DB15	Bi-directional (I/O) Data Line
22	IM0	Selec the MPU system interface mode
23	FLM	Frame head pulse signal
24	/RESET	Hardware Reset Signal(low active)
25	GND	Ground
26	X+(R)	Touch Panel
27	Y+(U)	Touch Panel
28	X-(L)	Touch Panel
29	Y-(D)	Touch Panel
30	GND	Frame head pulse signal
31	IOVCC	Power Supply(1.8V/2.8V)
32	VDD	Power Supply(2.8V)
33	LED1+	LED Anode(+)
34		LED Anode(+)
35		LED Anode(+)
36	NC	NC
37	NC	NC
38	NC	NC
39	LED-	LED Cathode(-)

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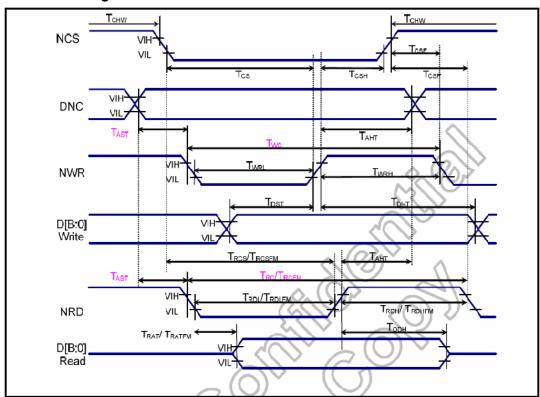
### 10. Power Supply Sequence





## 11. Read/Write Timing characteristics (80 series MPU)

### 1) Read/Write Timing



 $(VSSA=0V, IOVCC=1.65V \text{ to } 3.3V, VCI=2.3V \text{ to } 3.3V, T_A = -30 \text{ to } 70^{\circ} \text{ C})$ 

		(VSSA=0V, IOVCC=1.	1		,1-2.3V t	0 3.3 4, 1	A = -30 to 70 °C)
Signal	Symbol	Parameter		Spec. Typ	Max.	Unit	Description
	tAST	Address setup time	<b>Min.</b> 10	- Typ	iviax.		
DNC_SCL	tAHT	Address hold time (Write/Read)	10	-	-	ns	-
NCS	tCHW tCS tRCS tRCSFM tCSF tCSH	Chip select "H" pulse width Chip select setup time (Write) Chip select setup time (Read ID) Chip select setup time (Read FM) Chip select wait time (Write/Read) Chip select hold time	0 15 45 355 10 10			ns	
NWR_SCL	tWC tWC tWRH tWRL	Write cycle( 1pixel for one write) Write cycle (1 pixel for 2 or 3 write) Control pulse "H" duration Control pulse "L" duration	100 50 15 15			ě	-
NRD(ID)	tRC tRDH tRDL	Read cycle (ID) Control pulse "H" duration (ID) Control pulse "L" duration (ID)	160 90 45			ns	When read ID data
NRD(FM)	tRCFM tRDHFM tRDLFM	Read cycle (FM) Control pulse "H" duration (FM) Control pulse "L" duration (FM)	450 90 355		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	ns	When read from frame memory
DB17 to DB0	tDST tDHT tRAT tRATFM tODH	Data setup time Data hold time Read access time (ID) Read access time (FM) Output disable time	10 10 - 20		100 340 80	ns	For maximum CL=30pF For minimum CL=8pF

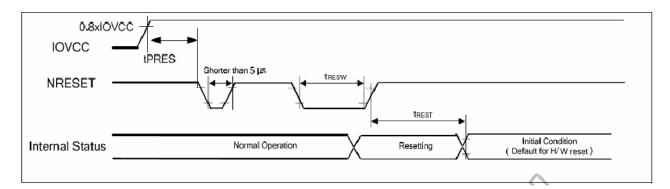
Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.

Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

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#### 2) Reset Timing characteristics

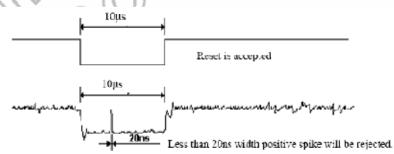


		Related		Spec.			
Symbol	Parameter	Pins	Min.	Тур.	Max.	Note	Unit
tRESW	Reset low pulse width <sup>(1)</sup>	NRESET	10	-	-	~~ ·	μs
+DEST	Reset complete time <sup>(2)</sup>	-	5	-	9	When reset applied during STB OUT mode	ms
tREST	Reset complete time	-	120	-2		When reset applied during STB mode	ms
tPRES	Reset goes high level after Power on time	NRESET & IOVCC	10	V.C	<u> </u>	Reset goes high level after Power on	ms

**Note:** (1) Spike due to an electrostatic discharge on NRESET line does not cause irregular system reset according to the table below.

NRESET Pulse	Action
Shorter than 5 µs	Reset Rejected
Longer than 10 µs	Reset
Between 5 µs and 10 µs	Reset Start

- (2) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in STB Out mode. The display remains the blank state in STB –mode) and then return to Default condition for H/W reset.
- (3) During Reset Complete Time VMF value in OTP will be latched to internal register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of NRESET.
- (4) Spike Rejection also applies during a valid reset pulse as shown below.



(5) It is necessary to wait 5msec after releasing !RES before sending commands. Also STB Out

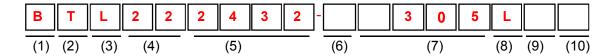
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## 12. External Dimension 1.GENERAL TOLERANCE:±O.≥ 2.BACKLIGHT LED USED 3CHIP 3.FPC CONNECTOR:HIROSE:FH26-39S-0.3SHW 4.IC:HX8347G <24.86> SCALE 2:1 FPC PAD PLATING HIRDSE: FH26-395-035HW GENERAL ROUND : R=0.1 240(RGB) 1 320 DOTS VIEWING ANGLE OVGA 350.95 D 30 WHITE SCREEN PRINTING SINGLE LAYER STIFFENER PRE! LEDA3-LEDA1-LED CIRCUIT [MINARY 2.05 to 1 WIHTOUT D/A TAPE 3-0.6 ĘĘŖ T.T.ZHAD DESC PART NO ZONE <u>9.70</u>±03 12.00:0:08 LTR ECN NO D/A TAPE O.141 RGB RGB O.047 O.047 MODEL BTL222432-305L 屬 REVISION WIGHT 374 A3 Ē III II DWG EXTERNAL -8 9 DB 10 DB 11 DB 11 DB 12 DB 13 DB 14 DB 15 DB 16 DB 17 DB 18 DB 1 BME-10-012 DRAVING J. ddW SHEET RO REV



#### 13. COLOR LCD MODULE NUMBERING SYSTEM



(1) B: BHL

(2) Drive System

C: CSTN T: TFT E: OLED M: MONO

(3) Product Status

L: LCD Model F: FOG Model G: COG Model P: PANEL Model C: CELL Model

(4) Display size(精确到小数点后1位,四舍五入)

(5) Resolution

Number of Row Dots \* Number of column Dots(前两位有效)

(6) Viewing Direction

Nil: 6 H U: 12 H L: 9 H R: 3 H W: Wide view E: 其他

- (7) Serial Number (\*001-9999: 按照产品状态,各类产品序列号实行大排行处理,\*为0时省略不写)
- (8) Back Light

Nil:Without backlight + Reflective H:CCFL + Translective

T:Without backlight + Transflective E:LED Frontlight + Reflective

F:CCFL Frontlight + Reflective D:LED + Transflective

L:LED + Transmissive

(9)DUAL LCD

Nil: Single LCD M:MONO C:CSTN T:TFT O:OLED

(10)TOUCH PANEL

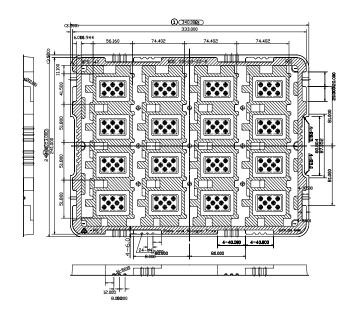
Nil:Without TP P:with TP

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## 14. Package Terms

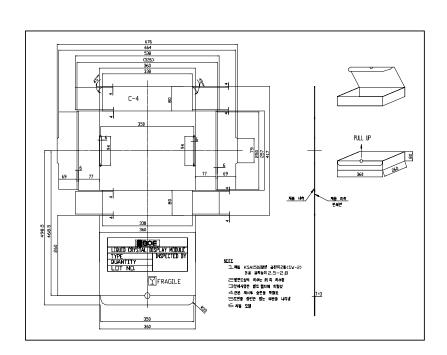
1、Tray Size L:340mm W:248mm (16pcs LCM/Tray)



**Tray Drawing** 

2、Inner BOX Size L:360mm W:260mm H:80mm

(7pcs Tray) / Inner Box)



**Inner Box Drawing** 

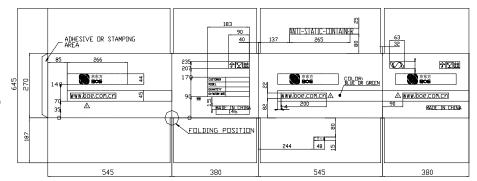
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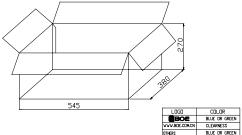




L: 545mm W: 380mm H: 270mm

(6pcs Inner / Out)

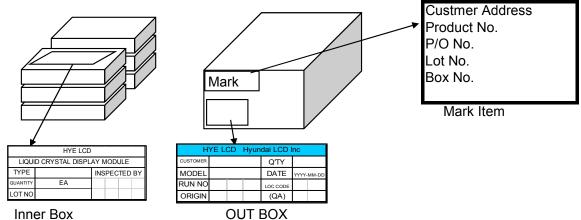




NOTE

1.MATERIAL: KSA 1531,DW2(T=8mm)
2.DRAWING DIMESIONS ARE EQUAL TO OUTSIDE DIMENSION.
3.INNER BOX(C-4) ARRANGEMENT: 3STEPS X?2RDWS
4.MARKS ARE REFER TO SEPERATE CONSULTATION.

#### 4. Packing label content



#### 5. Packing notice

- [1]Sub LCD should be placed upwardly while in the tray.
- [2] Every seven full trays with a blank one while twining twice on both sides by adhesive tape.
- [3]. Every tray should be put crossedly.

#### 6、Product label

[1] There should be Logo and product modle of BOE on FPC ASS'Y.

### 7、Packing Q'ty list

1						
				INNER BOX	TRAY	MODULE
	OUT BOX		6	42	576	
		INNER E	BOX	1	7	96
			TRAY	_	1	16

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## 1.LCD Module Out-Going Quality Level

### (1.0) Purpose

The LCD specification provides outgoing provision and its expected quality level based on our outgoing inspection of LCD.

### (2.0) Applicable Scope

The LCD specification is applicable to the arrangement in regard to outgoing Inspection and quality assurance after it.

### (3.0) Quality Specification

### (3.1) Quality Level

The quality level of BHL&BMDT are based on GB/T2828.1-2003, Apply Level II, normal inspection by single sampling.

Rank	Item	AQL	Note
Major(MA)	Parts Short, Parts Mounting Back Light, Open Solder Bridging Outside Dimension Solder Ball, Abnormal/No Display		
Minor (MI)	Stains on LCD Panel Surface Stains, Scratchs, Foreign Substance, Spots, Air Bubbles	1.0	

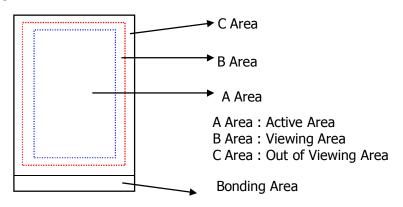
#### (3.2) Appearance Standards

#### 1) Inspection Conditions

The inspection shall be applied under 20W white fluorescent lamp light at a distance between 400-500mm, with the eyes 300mm away from products and the angle of view within 30° to perpendicular line.

The mobile lens should be fixed on when doing inspection in case the mobile with len

### 2) Definition of the Area



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## (3.3) Apperance Spec

No	Item	Criteria	Rank	Remark
1	Parts Short	Not allowed	MA	
2	Solder Bridging	Any bridging between components, except common circuit, is not allowed	MA	
3	Outside Dimension	Drawing & specification must be within permitable tolerance	MA	
4	Open	Not allowed	MA	
5	Cold Solder	Not allowed	MA	
6	Stains On LCD Panel Surface	Stains which can be wiped off with soft cloth are counted as defect  Stains which can't be removed even with soft cloth are not allowed	MI	Diameter $(X + Y)/2$
7	Back Light	No light and short of light and abnormal lighting are not allowed	МА	
8	Air Bubles Between Glass & Polarizer (Polarizer Defects)	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	MI	

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No	Item	Criteria	Rank	Remar
9	Parts Mounting	Parts mounting failure is not allowed Wrong parts mounted is not allowed	MA	
10	Stains Foreign Substance	$ \begin{array}{c cccc} (1) \ \text{Round shape} \\ \hline & \text{Area} & \text{Acceptable Q'ty} \\ \hline & \text{Dimension} & \text{A Area} & \text{B Area} \\ \hline & \Phi \leqslant 0.10 & \text{Ignore} \\ \hline & 0.10 < \Phi \leqslant 0.20 & 2 & \text{Ignore} \\ \hline & 0.20 < \Phi \leqslant 0.30 & 1 & \text{Ignore} \\ \hline & 0.30 < \Phi & 0 & \text{Ignore} \\ \hline \end{array} $		
10	Scratches Spots			
11	Abnormal Display	Not allowed	MA	
12	No Display	Not allowed	МА	

Note: A limitation sample is given top priority

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# (4.0) Specification for Touch Panel Inspection

# (4.1) Inspection Criterion and Level

Rank	Item	AQL	Note
Major (MA)	Function Failure	0.65	
Minor (MI)	Bubble, Scratch, Foreign Particle	1.0	

# (4.2) Inspection Criterion

No	Item	Criteria	Rank	Note
1	Function	Fail to adjust, hard to adjust (can't be adjusted with times): Reject Stroke Drift, Stroke Suspension: Reject	in 3 MA	
2	Air Bubble Scratch Foreign Particle	$ \begin{array}{ c c c c }\hline 1) \ Round \ shape \\ \hline Dimension & Acceptable Q'ty \\ \hline A \ area & B \ area \\ \hline \hline 0.10 < \Phi \leqslant 0.10 & Ignore \\ \hline 0.10 < \Phi \leqslant 0.20 & 2 & Ignore \\ \hline 0.20 < \Phi \leqslant 0.30 & 1 & Ignore \\ \hline 0.30 < \Phi & 0 & Ignore \\ \hline \hline 2) \ Line \ shape & \hline \hline Dimension & Acceptable Q'ty \\ \hline length & Width & A \ area & B \ area \\ \hline - & W \leqslant 0.025 & Ignore \\ \hline & L \leqslant 3.0 & Ignore \\ \hline & 3.0 < L \leqslant 5.0 & 2 & Ignore \\ \hline \leqslant 7 & W \leqslant 0.1 & 1 & \\ \hline & - & W > 0.1 & Follow \ Round \ shape \\ \hline \end{array} \right] \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	MI	Y **: 平均 直 径 (X + Y)/2

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## (5.0) Reliability Test - Module Middle Reliability

No.	Item	Condition	Duration	Sample Quantity	Creteria (Acc/Rej)	Note
1	High Temp Operation	70 ± 2℃	48 hrs	3	0/1	
2	Low Temp Operation	-20 ± 2℃	48 hrs	3	0/1	
3	High Temp and High Humidity Storage	60℃,90% RH 90%rh	48 hrs	3	0/1	
4	Thermal Shock	30min Stage $-20$ °C $\leftrightarrow$ 70 °C	10cycles/	3	0/1	

## (5.1) Criteria

- a. No changes for indication and appearance.
- b. Leave the all samples under room temperature 4 hours after reliability test ends.



### 2. BHL&BMDT Customer Quality Service Process

In order to provide better service for Customer, BHL&BMDT shall apply the after-sales product quality service process as below:

- 1> According to the P/O from Customer, BHL&BMDT should deliver required product to the right place appointed by Customer.
- 2> Customer shall apply inspection to the incoming procuct.
- 3> Inspection standard should be provided by BHL&BMDT, and it will become effective after confirmed by Customer. Inspection and Defects determination should be carried out according to the standard agreed by both Parties.
- 4> In order to guarantee in-time communication of product quality situation and effective service, QA staff on Customer side should send Weekly Quality Report to the appointed CS staff in BHL&BMDT.
- 5> After BHL&BMDT get related information, both sides should arrange time and place to determin the non-performing products found by Customer.
- 6> BHL&BMDT should cooperate with Customer in case of special quality requirement.
- 7> After confirmed by both side, BHL&BMDT should be responsible for the defect part caused by our quality problem. BHL&BMDT take back the confirmed defect products and return normal goods to customer.
- 8> BHL&BMDT agree to provide related training of LCD product technology and usage.
- 9> Customer should use the LCD product according to the instruction. BHL&BMDT will not be responsible for the defect product caused by violation of Users' Instruction.
- 10> Both parties should deal with the quality problem under the principle of mutual consultation and cooperation. And both parties should negotiate to handle the defect products of which the cause isn't clear.



#### 3.LCD Module Operation Instruction

#### 3.1 Cautions for storage

- 1> Avoid hitting the LCD Panel in any way because the LCD is made of glass.
- 2> Physical status of liquid crystal will change under extrem temperature, and it can not be resumed when the temperature returns to normal. So LCD module should be stored in required temperature. Same reason, LCD module should be stored in required humidity. Low hymidity may add static, while high humidity may corrode the ITO circuit of LCD product. The suitable storage environment is: temperature: 22±5 °C, humidity: 55%±10%.
- 3> Avoid exposing LCD module under sunshine, strong fluorescence or ultraviolet radiation for a long time. It should be stored in dark area.
- 4> Chemical liquid is forbidden to clean the LCD, such as alcohol, acetone and IPA. Because all of these can do damage to the LCD. Water on the LCD surface must be cleaned as soon as possible, or it will cause POL color change or other defect.
- 5> LCD products should be stored in static-protective polythene bag under certain requirement.

#### 3.2 Cautions for installing and assembling

- 1> Please make sure that operators wear static-protective bands correctly and working tables are effectively grounded during operation.
- 2> Please place LCD module on the tray provided by BHL&BMDT while moving it, in order to avoid mechanical damage. Hold the module's side frames to avoide damage during moving.
  Please move and assemble LCD very carefully during assembly, and avoid pushing or twisting it.
- 3> Avoid disassembling LCD module or damage the FPC or permanent defect may happen.
- 4> Avoid cleaning the LCD surface with hard materials. Please clean LCD with Air-gun or very soft cloth when necessary. The protective film on the POL is prohibited to be removed until assembly, otherwise, dust, spit or other foreign matter may fall on the LCD surface. After the protective film is removed, only air-gun can be applied to remove any dust or foreign matter. Fingure or cloth MUST NOT be used in such cases.
- 5> Avoid twisting, disassembling, squeezing or hitting the PCB. It will damage the circuit or component on PCB and cause functional defect.
- 6> Please use the connector according to the instruction provided by BHL&BMDT.
- 7> Please place dual module with the sub-panel upward. Trays should be placed in contrary direction. An empty tray should be placed on the top.
- 8> Sealing operation on PCB must be very careful to avoid short or cutting the original circuit on PCB. Otherwise, permenant damage to the LCD may happen.
- 9> Please take great care to use connector. Defect caused by wrong or careless operation on Customer side are not within the compensation range.

#### 3.3 Cautions for operation

- 1> Avoid adding direct DC or high voltage to LCD panel. It will cause functional damage to the LCD or shorten the life of LCD product.
- 2> LCD may respond slowly or display abnormally in extrem temperature (lower than -20  $^{\circ}$ C or higher than 50 $^{\circ}$ C). But this doesn't mean LCD functional defect. LCD will display normally in regular temperature. Therefore, avoid using LCD product in extrem temperature.
- 3> Avoid pushing the display area of LCD panel which may cause abnormal display. This doesn't mean LCD functional defect, neither. LCD will display normally in regular temperature.
- 4> The black tape on IC on LCD product is used to protect the IC from light. Please do NOT remove it.
- 5> Electrical inspection for LCD product is carried out by using mobile phone provided by Customer. Special test equipment could be applied under mutual consent.

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