

Product Specifications

Customer	
Description	7" TFT LCD Module
Model Name	LW700AT9003
Date	2008/2/21
Doc. No.	
Revision	00

Customer Approval	
Date	
The above signature represents that the product specifications, testing regulation, and warranty in the specifications are accepted	

Engineering			
Check	Date	Prepared	Date

Brief Spec

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RECORD OF REVISIONS

Revision	Date	Page	Description
00	2008/2/5	all	Brief Spec New Creation

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1. SUMMARY

This technical specification applies to 7" TFT-LCD module with a LED Backlight unit and a 40-pin TTL interface. This module supports 800*R.G.B x 480 WVGA mode and can display 262,144 colors.

2. FEATURES

- Thin and Light Weight.
- WVGA(800x480 pixels) resolution.
- 3.3 V TTL interface

3. GENERAL SPECIFICATIONS

Parameter	Specifications	Unit
Screen size	7"(Diagonal)	inch
Display Format	800 RGB x 480	Dot
Active area	152.4x91.44	mm
Pixel size	190.5 x 190.5	um
Surface treatment	Anti-glare	
Pixel Configuration	RGB Vertical Stripe	
Outline dimension	165(W) x 104.44(H) x 5.2 (D)	mm
Weight	(TBD)	g
View Angle direction	6 o'clock	
Temperature Range	Storage	-30~80
		°C

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4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power Voltage	V _{CC}	GND=0	-0.3	6	V	-
	AVDD	GND=0	6.5	13.5	V	-
	V _{GH}	GND=0	7	V _{GL} +40	V	-
	V _{GL}	GND=0	-20	-5	V	-
	V _{GH} -V _{GL}	GND=0	12	40	V	-
Input Signal Voltage	V _i	GND=0	-0.3	VDDA+0.3	V	-
	V ₁ -V ₅	GND=0	0.4AVDD	AVDD-0.1	V	-
	V ₆ -V ₁₀	GND=0	0.1	0.6AVDD	V	-
	V _{COM}	GND=0	-	-	V	-

5. ELECTRICAL CHARACTERISTICS

5.1 Recommended Operation condition (GND=AVSS=0V)

Ta= 25 °C

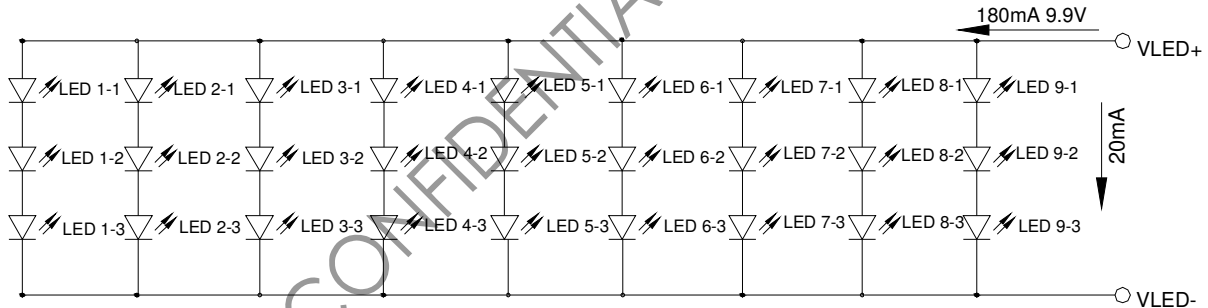
Parameter	Symbol	Rating			Unit	Condition
		Min.	Typ.	Max.		
Power Supply Voltage	V _{CC}	3.0	3.3	3.6	V	
	AVDD	11.5	12	12.5	V	
	V _{GH}	17	18	19	V	
	V _{GL}	-8	-7	-6	V	
Input Signal Voltage	V _{1~V5}	0.4AVDD	-	AVDD-0.1	V	
	V _{6~V10}	0.1	-	0.6AVDD	V	
	V _{COM}	-	3.6	-	V	
Digital Input Voltage	High Level	V _{IH}	0.7V _{CC}	-	V _{CC}	V
	Low Level	V _{IL}	0	-	0.3V _{CC}	V

5.2 LED Driving Conditions

Ta = 25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	I _{LED}	-	180	250	mA	Note 1
LED voltage	V _{LED}	-	9.9	-	V	
LED Life Time	-	(10,000)	-	-	Hr	Note 2

Note 1 : There are 9 Groups LED shown as below , V_{LED}=9.9V (min.).



Note 2 : Brightness to be decreased to 50% of the initial value.

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6. DC CHARACTERISTICS

6.1 For the digital circuit

Parameter	Symbol	Rating			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V _{CC}	2.7	3.3	3.6	V	Ditigal power
Low Level Input Voltage	V _{IL}	0	-	0.3V _{CC}	V	For the ditigal circuit
High Level Input Voltage	V _{IH}	0.7V _{CC}	-	V _{CC}	V	For the ditigal circuit
High Level Output Voltage	V _{OH}	0.8V _{CC}	-	V _{CC}	V	-
Low Level Output Voltage	V _{OL}	0	-	0.2V _{CC}	V	-
Input Leakage Current	I _{IN}	-1	-	+1	V	No pull up or pull down.
Digital Stand-by Current	I _{VCC}	-		TBD	μA	All inputs are stopped and outputs are High-Z.
Digital Operating Current	I _{CC}	-	TBD		mA	F _{CPH} =33MHz, F _{HS} =40.1KHz, black pattern, V _{CC} =3.3V
Pull high resistor	R _H	600	900	1200	KΩ	-
Pull low resistor	R _L	600	900	1200	KΩ	-

6.2 For the analog circuit

Parameter	Symbol	Rating			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	AVDD	11.5	12	12.5	V	For the analog circuit power
Input level of V _{r1} ~ V _{r7}	V _{REF}	0.4AVDD	-	AVDD-0.1	V	Gamma correction voltage
Input level of V _{r8} ~ V _{r14}	V _{REF}	0.1	-	0.6AVDD	V	Gamma correction voltage
Output Voltage deviation	V _{VD}	-	±20	-	mV	SO1~SO1200
DC offset	V _{OS}	-	-	±20	mV	SO1~SO1200, V _{IN} =0.1~13.4V,
Output Current	I _{OL}	40	60	-	μA	SO1~SO1200, V _O =0.1V vs. 1.0V, V _{DDA} =10V
Output Current	I _{OH}	40	60	-	μA	SO1~SO1200, V _O =9.9V vs. 9V, V _{DDA} =10V
Analog standby current	I _{VDDA}			TBD	μA	All LCD outputs are High-Z.
Analog Operating Current	I _{DD}	-	TBD		mA	F _{CPH} =33MHz, F _{HS} =40.1KHz, black pattern, V _{DDA} =8.4V

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7. AC CHARACTERISTICS

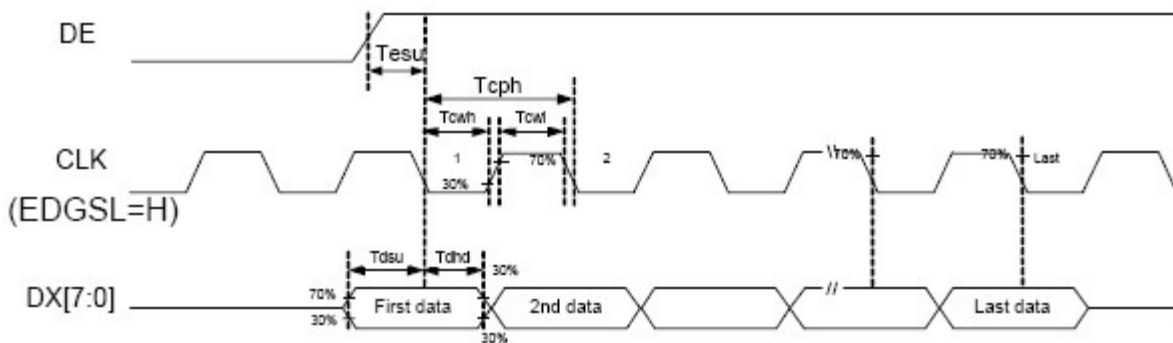
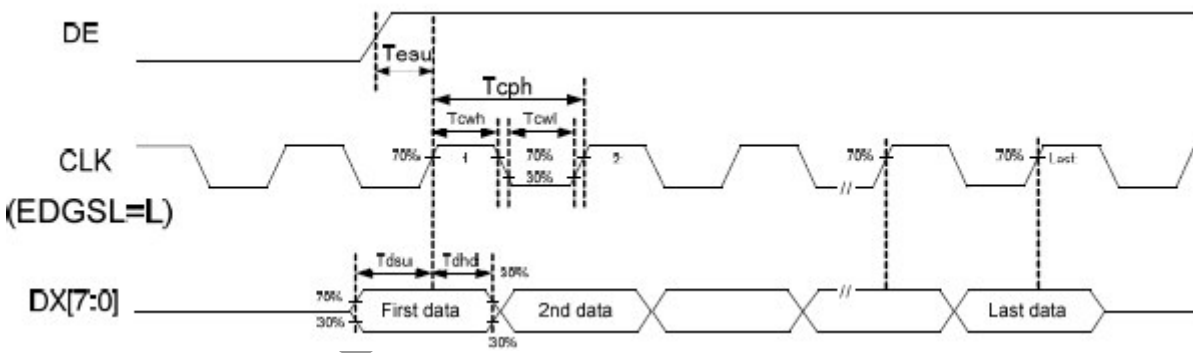
7.1 AC Electrical CHARACTERISTICS

Parameter	Symbol	Rating			Unit
		Min.	Typ.	Max.	
Data setup time	Tdsu	6	-	-	ns
Data hold time	Tdhd	6	-	-	Tcph
DE setup time	Tesu	6	-	-	Tcph
CLK frequency	F _{CPH}		33.26		MHz
CLK period	T _{CPH}		30.06		ns
CLK pulse duty	T _{CWH}	40	50	60	%
DE period	T _{DEH} +T _{DEL}	1000	1056	1200	T _{CPH}
DE pulse width	T _{DEH}	-	800	-	T _{CPH}
DE frame blanking	T _{DEB}	10	45	110	T _{DEH} +T _{DEL}
DE frame width	T _{DE}	-	480	-	T _{DEH} +T _{DEL}

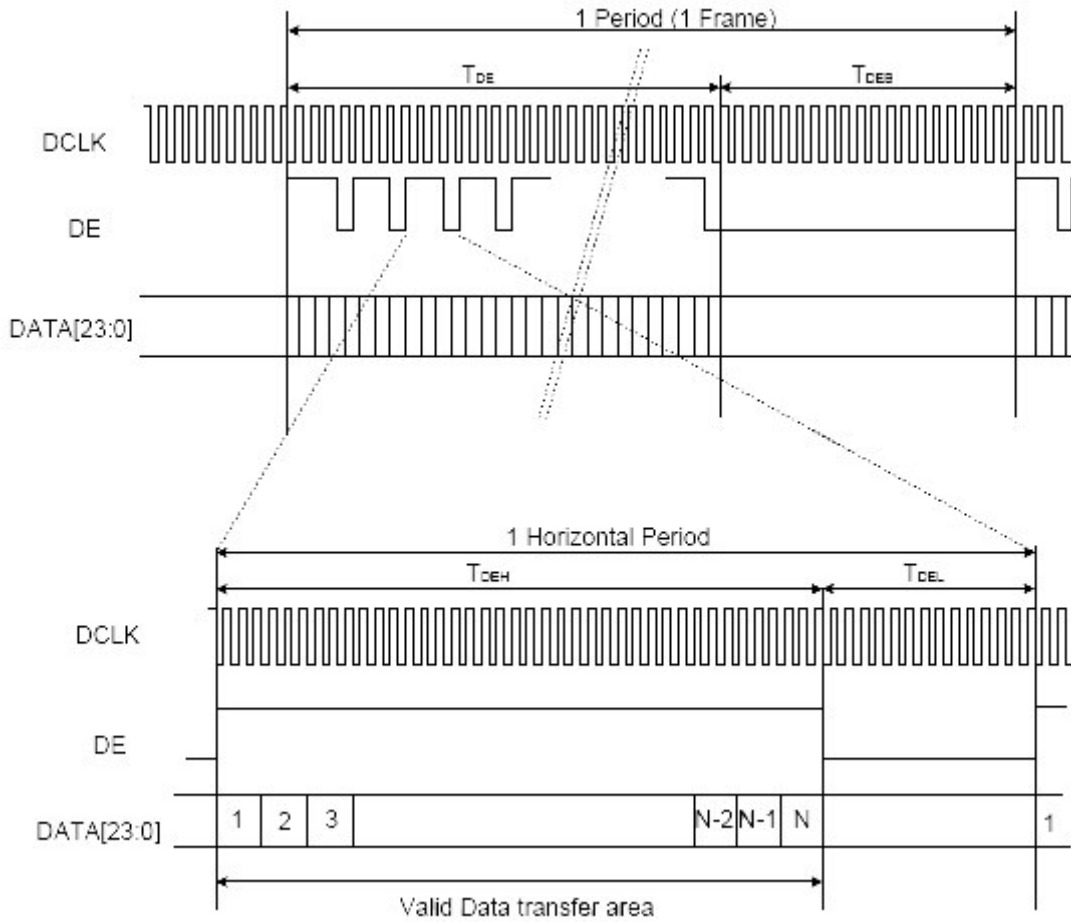
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7.2 Timing Controller Timing Chart

Clock and Data input waveforms



7.3 Data input format



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8. OPTICAL CHARACTERISTIC

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	
Response time	Tr	$\theta = 0^\circ \cdot \Phi = 0^\circ$	-	(5)	(10)	ms	Note 3,5	
	Tf		-	(11)	(16)	ms		
Contrast ratio	CR	At optimized viewing angle	(250)	(400)	-	-	Note 4,5	
Color Chromaticity	White	Wx	$\theta = 0^\circ \cdot \Phi = 0^\circ$	Typ-0.05	(0.313)	Typ+0.05	-	Note 2,6,7
		Wy			(0.329)			
	Red	Rx			(0.597)			
		Ry			(0.338)			
	Green	Gx			(0.315)			
		Gy			(0.535)			
	Blue	Bx			(0.138)			
		By			(0.154)			
Viewing angle	$\psi = 180^\circ$	θ_l	(65)	(70)	-	Deg.	Note 1	
	$\psi = 0^\circ$	θ_r	(65)	(70)	-			
	$\psi = 90^\circ$	θ_u	(55)	(60)	-			
	$\psi = 270^\circ$	θ_d	(55)	(60)	-			
Brightness	-	-	(300)	(350)	-	cd/m ²	Center of display	

Note 1: Definition of viewing angle range

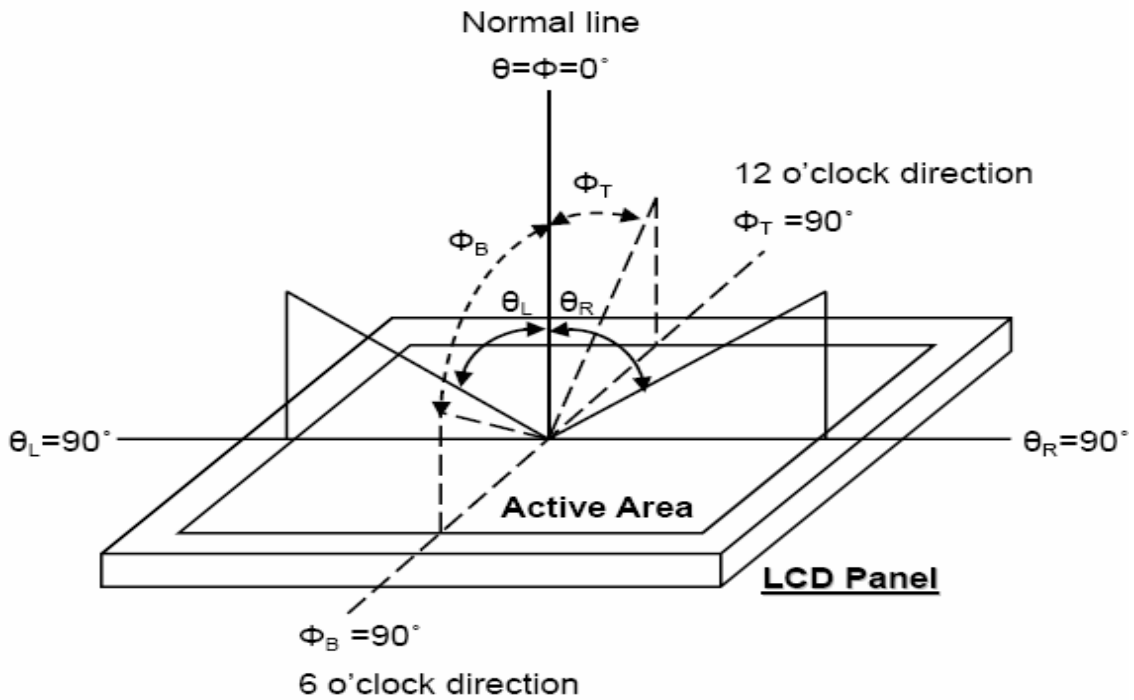


Fig. 8-1 Definition of viewing angle

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Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

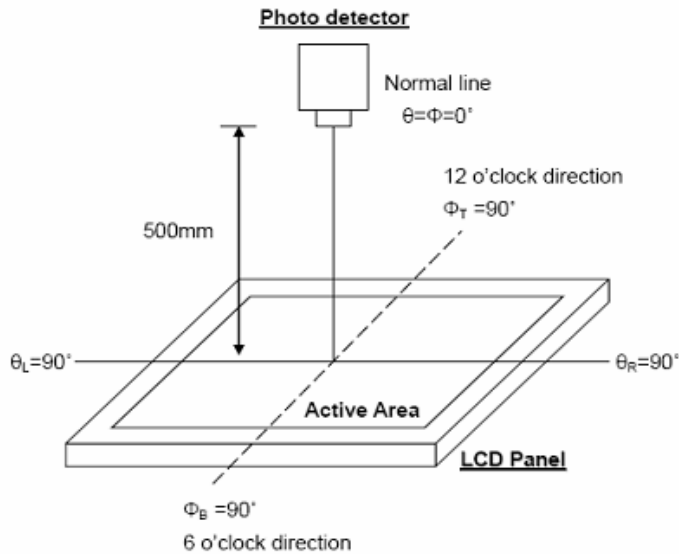


Fig. 8-2 Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White state and “Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10% . And fall time, T_f , is the time between photo detector output Intensity changed from 10% to 90% .

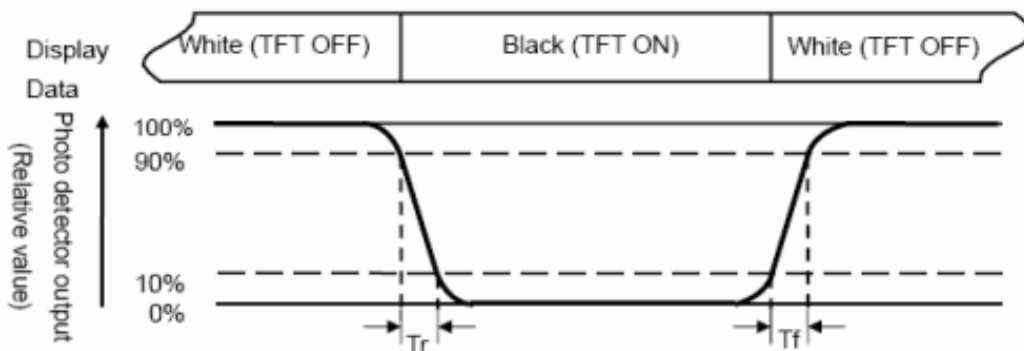


Fig. 3-3 Definition of response time

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Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White $V_i = V_{i50} \pm 1.5V$

Black $V_i = V_{i50} \pm 2.0V$

“±” means that the analog input signal swings in phase with VCOM signal.

“±” means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

$$\text{Note 8 : Uniformity (U)} = \frac{\text{Brightness (min)}}{\text{Brightness (max)}} \times 100\%$$

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9. INTERFACE

9.1 LCM PIN Definition

Pin No.	Symbol	Description	Remark
1	Vss	Power Ground	
2	Vss	Power Ground	
3	N/A	-	
4	N/A	-	
5	N/A	-	
6	N/A	-	
7	Vcc	Power Supply for Digital Circuit	
8	Vcc	Power Supply for Digital Circuit	
9	DE	Data Enable	
10	Vss	Power Ground	
11	Vss	Power Ground	
12	Vss	Power Ground	
13	B5	Blue Data 5 (MSB)	
14	B4	Blue Data 4	
15	B3	Blue Data 3	
16	Vss	Power Ground	
17	B2	Blue Data 2	
18	B1	Blue Data 1	
19	B0	Blue Data 0 (LSB)	
20	Vss	Power Ground	
21	G5	Green Data 5 (MSB)	
22	G4	Green Data 4	
23	G3	Green Data 3	
24	Vss	Power Ground	
25	G2	Green Data 2	
26	G1	Green Data 1	
27	G0	Green Data 0 (LSB)	
28	Vss	Power Ground	
29	R5	Red Data 5 (MSB)	
30	R4	Red Data 4	
31	R3	Red Data 3	
32	Vss	Power Ground	
33	R2	Red Data 2	
34	R1	Red Data 1	
35	R0	Red Data 0 (LSB)	
36	Vss	Power Ground	
37	Vss	Power Ground	
38	DCLK	Clock Signals ; Latch Data at the Falling Edge	
39	Vss	Power Ground	
40	Vss	Power Ground	

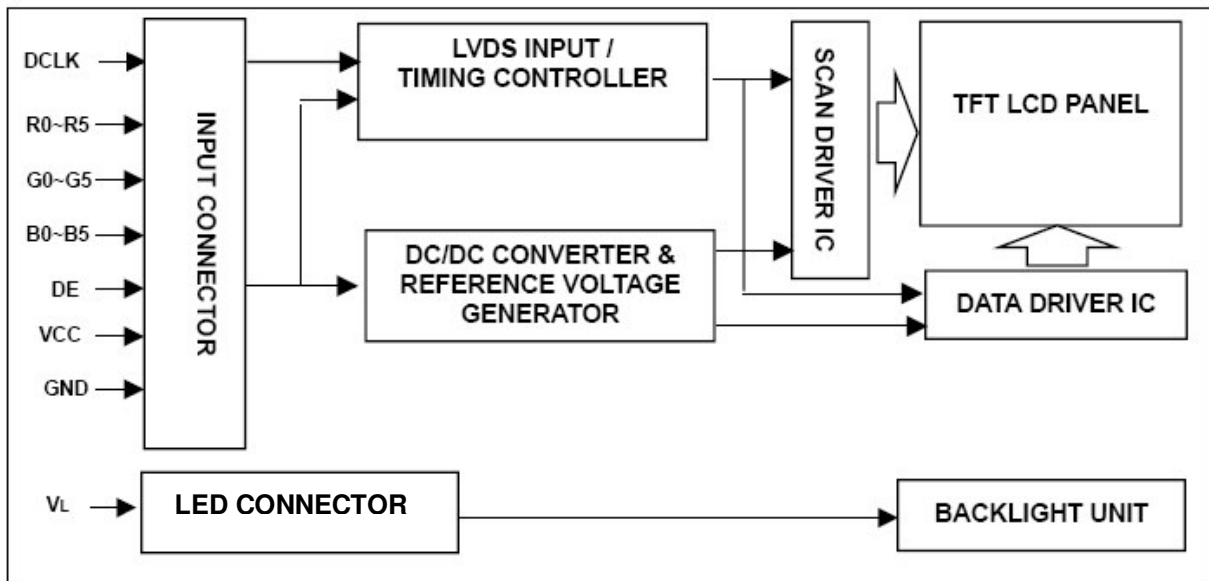
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9.2 Backlight Driving Part

Pin No.	Symbol	Description
1	VLED+	Red, LED_ Anode
2	VLED-	White, LED_ Cathode

Note: The backlight interface connector is a model **SM02B-BHSS-1-TB** manufactured by JST or equivalent. The matching connector part number is **BHSR-20VS-1** manufactured by JST or equivalent.

10. BLOCK DIAGRAM



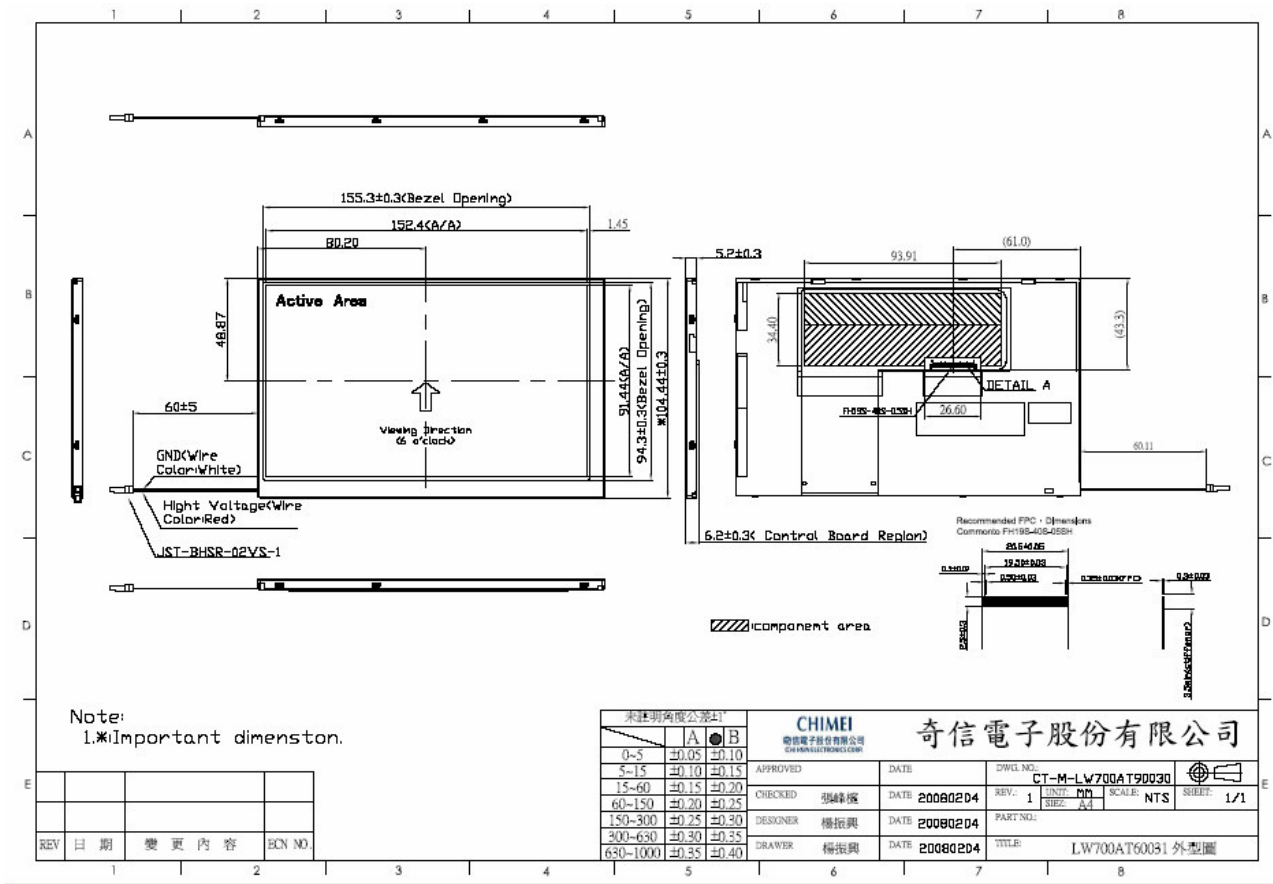
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11. QUALITY ASSURANCE

No.	Test Items	Test Condition	REMARK
1	High Temperature Storage Test	Ta=80°C Dry 240h	
2	Low Temperature Storage Test	Ta=-30°C Dry 240h	
3	High Temperature Operation Test	Ta=70°C Dry 240h	
4	Low Temperature Operation Test	Ta=-20°C Dry 240h	
5	High Temperature and High Humidity Operation Test	Ta=60°C 90%RH 240h	
6	Electro Static Discharge Test	150pF, 330Ω, ±8KV(Contact)/±15KV(Air), 5 points/panel, 5 times/point	
7	Shock Test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces (I.e. run 180G 2ms for all six faces)	
8	Vibration Test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis	
9	Thermal Shock Test	-20°C (0.5h) ~ 70°C (0.5h) / 100 cycles(Dry)	

***** Ta= Ambient Temperature

12. OUTLINE DRAWING



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13. PACKAGE INFORMATION
TBD

14. PRECAUTIONS

Please pay attention to the following when you use this TFT LCD module.

14.1 MOUNTING PRECAUTIONS

- (1) You must mount a module using arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module.
And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach a transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not describe because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are determined to the polarizer)

- (7) When the surface becomes dusty, please wipe gently with adsorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

14.2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

14.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

14.4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

14.5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

14.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. Is apt to remain on the polarizer. Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.