



Chunghwa Picture Tubes, Ltd.

Technical Specification

To : YIH HSING ENTERPRISE CO.,LTD.
Date : 2006/08/31

CPT TFT-LCD

CLAA150XP07F Y

ACCEPTED BY :

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1. OVERVIEW

CLAA150XP01 is 15" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight. By applying 8 bit digital data(6 bits+FRC), 1024×768, 16.2M-color images are displayed on the 15" diagonal screen. General specification are summarized in the following table:

ITEM	SPECIFICATION
Display Area (mm)	304.1(H)x228.1(V) (15.0-inch diagonal)
Number of Pixels	1024 (H) × 768(V)
Pixel Pitch (mm)	0.297(H) × 0.297(V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white, TN
Number of Colors	16.2M(6 bits+FRC)
Brightness (cd/m ²)	450cd/m ² (Typ.)(center, 6.5mA)
Viewing Angle	140/125(Typ.)
Wide Viewing Angle Technology	Optical Compensation Film
Surface Treatment	Anti-glare
Response Time	16 ms(Typ.)
Color Saturation	65%
Total Module Power (W)	16.64+1.8(Typ.)
Optimum Viewing Angle	6 o'clock
Module Size (mm)	326.5(W) × 253.5(H) × 14.0(D) (Typ.)
Module Weight (g)	1300(Typ.)
Backlight Unit	CCFL, 4 tables, edge-light (top/bottom)

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cables, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for above application or not listed in "Standard" as follows, please contact our sales people in advance.

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

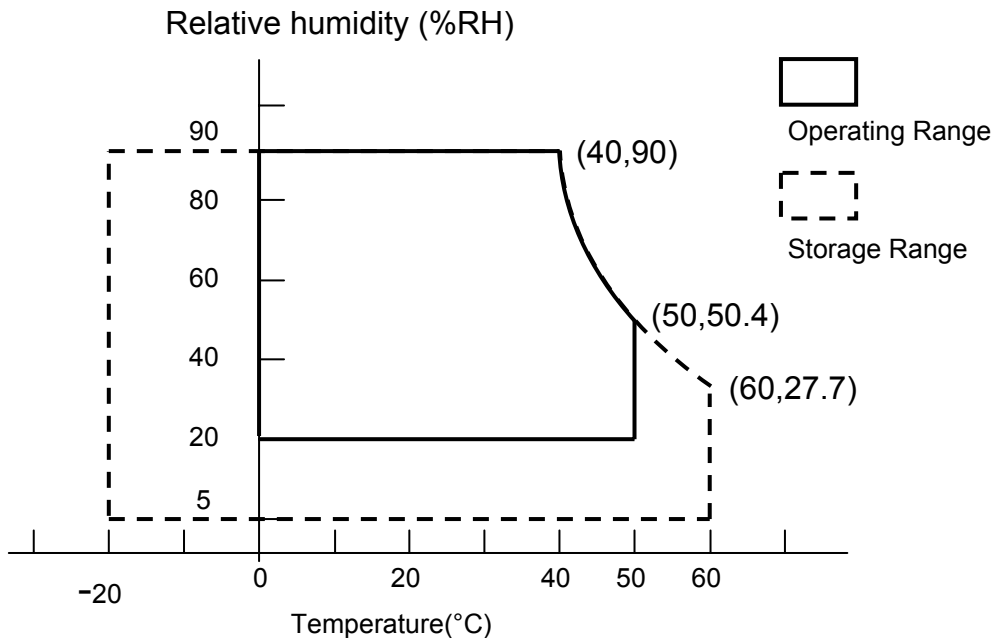
2. ABSOLUTE MAXIMUM RATINGS

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN.	MAX.	UNIT	Remark
Power Supply Voltage For LCD	VDDD	--	4.0	V	--
IDDD Rush Current	IRUSHd	--	4.0	A	*1)
Lamp Voltage	VL	495	605	Vrms	--
Lamp Current	IL	3.5	8.0	mArms	--
Lamp Frequency	FL	40	80	kHz	--
Operation Temperature (Surrounding) *1)	T _{op}	0	50	°C	*2), 3), 4), 5)
Storage Temperature *1)	T _{stg}	-20	60	°C	*2), 3), 4)

[Note]

- *1) The rising time of VCC is 550 μ sec (measured conditions are described below), If VCC rise time increase then I_{RUSH} decrease.
- *2) The relative temperature and humidity range are as below sketch, 90%RHMax. (Ta \leq 40°C)
- *3) The maximum wet bulb temperature \leq 39°C (Ta > 40°C) and without dewing.
- *4) If you use the product in a environment which over the definition of temperature and humidity too long to effect the result of eye-atching.
- *5) The lamp life time relates to lamp current, when you use our products please see the statement about backlight lists at page 7.
- *6) If you operate the product in normal temperature range, the center surface of panel should be under 60°C.
 Humidity :
 Humidity \leq 85%RH without condensation.
 Relative Humidity \leq 90% (Ta \leq 40°C)
 Wet Bulb Temperature \leq 39°C (Ta \geq 40°C)



3. ELECTRICAL CHARACTERISTICS

3.1 TFT-LCD

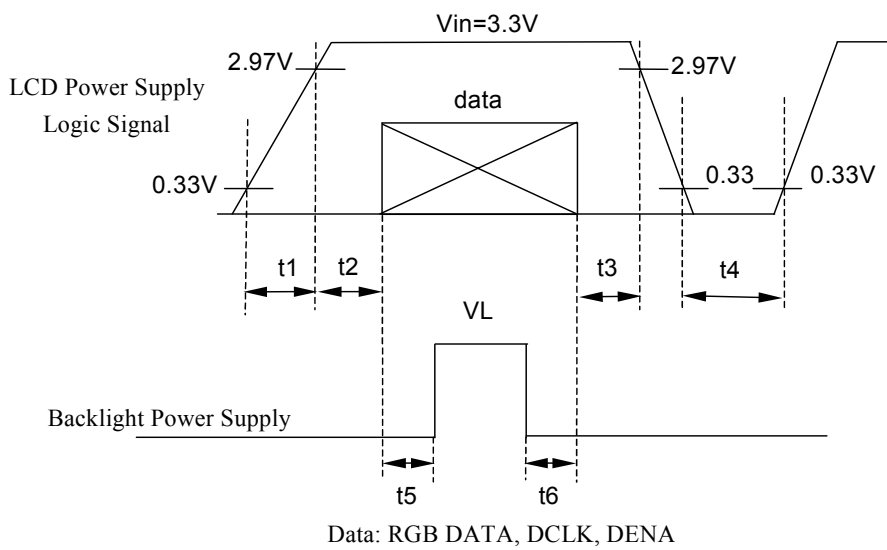
Ta=25°C

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Power Supply Voltage for Logic	VDDD	3.0	3.3	3.6	V	*1)
Power Supply Current for Logic	IDDD	--	450	600	mA	*2)
Permissible Ripple Voltage for Logic	VRPd	--	--	100	mVp-p	Vin=+3.3V
Differential impedance	Zm	90	100	110	Ω	*3)

[Note]

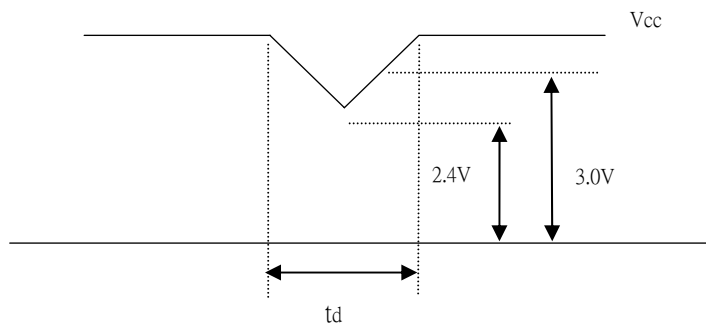
*1)Power 、 data sequence

- 0.5ms ≤ t1 ≤ 10ms 500ms ≤ t4
- 0 ≤ t2 ≤ 50ms 200ms ≤ t5
- 0 ≤ t3 ≤ 50ms 200ms ≤ t6



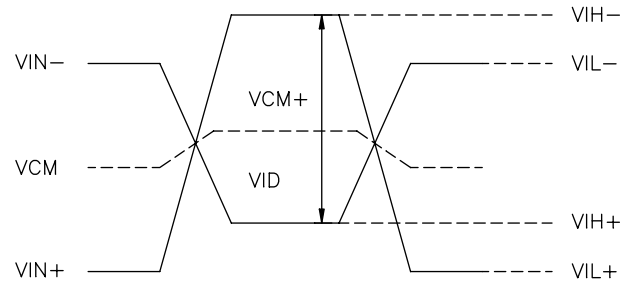
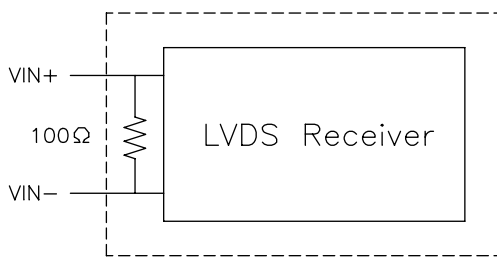
VCC-dip state :

- 1) When 2.7 V ≤ VCC < 3.0V, td ≤ 10 ms.
- 2) VCC-dip condition should also follow the VCC-turn-off condition.



*2) Typical current situation: 0~255-gray-bar pattern, 768 line mode, VCC=+3.3V, fCLK=65 MHz.

*3) Definition of the LVDS data



$$VID = VIN_+ - VIN_-$$

$$\Delta VCM = |VIN_+ - VCM_-|$$

$$\Delta VID = |VIN_+ - VID_-|$$

$$VID_+ = |VIN_+ - VIH_-|$$

$$VID_- = |VIN_+ - VIL_-|$$

$$VCM = (VIN_+ + VIN_-)/2$$

$$VCM_+ = (VIN_+ + VIH_-)/2$$

$$VCM_- = (VIN_+ + VIL_-)/2$$

VIN+ : Positive differential DATA & CLK input
 VIN- : Negative differential DATA & CLK input

3.2 Backlight

(a) Electrical Characteristics

Ta=25°C

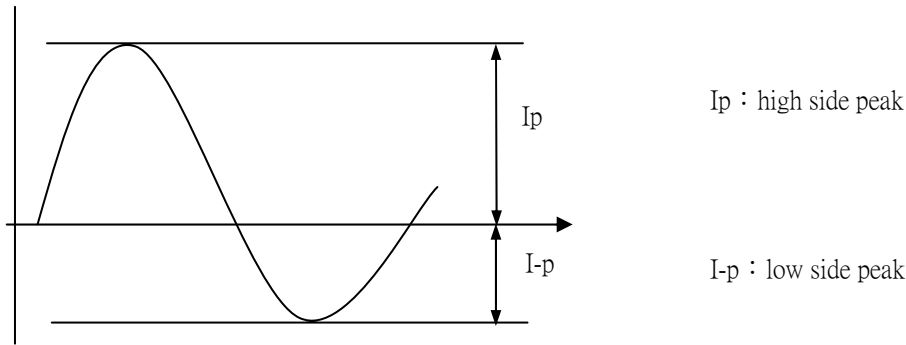
ITEM	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Lamp Voltage	VL	495	550	605	Vrms	IL=8.0mA
Lamp Current	IL	3.5	6.5	8	mArms	*1)
Inverter Frequency	FI	40	--	80	kHz	*2)
Starting Lamp Voltage	VS	950	--	--	Vrms	Ta=25°C *3) *4)
		1300	--	--	Vrms	Ta=0°C *3) *5)

(b) Lamp Life Time

ITEM	IL @3.0 mA	IL @6.5 mA	IL @8.0 mA	UNIT	REMARK
Lamp Life Time	Min. 50,000	Min. 50,000	Min. 30,000	hr	Continuous Operation*6)
Turn-on and Turn-off Operation	--	Min. 100,000	--	time	Continuous Operation*7)

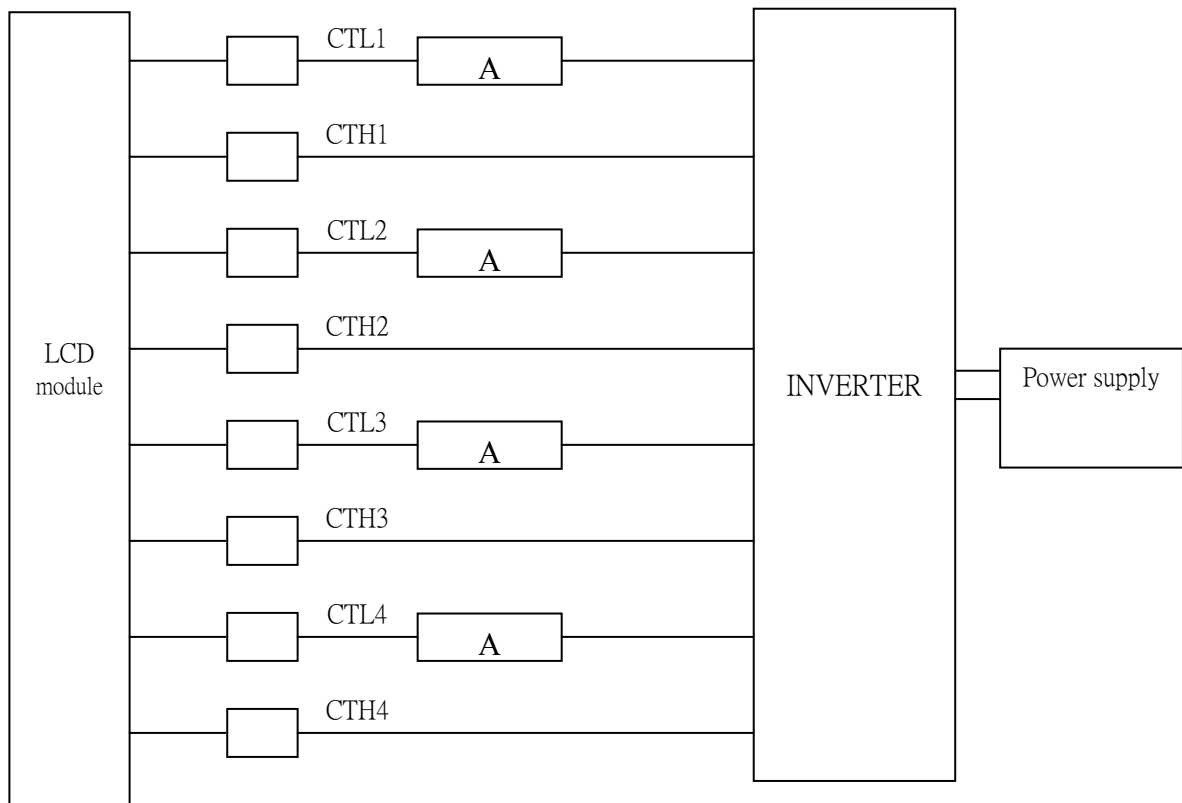
[Note] If the waveform of light up-driving is asymmetric, the distribution of mercury inside the lamp tube will become unequally or will deplete the Ar gas in it. Then it may cause the abnormal phenomenon of lighting-up. Therefore, designers have to try their best to fulfill the conditions under the inverter designing-stage as below:

- The degrees of unbalance : $< 10\%$
- The ratio of wave height : $< \sqrt{2} \pm 10\%$



A : The degrees of unbalance = $| I_p - I-p | / I_{rms} \times 100 (\%)$
 B : The ratio of wave height = I_p (or $I-p$) / I_{rms}

*1) Lamp Current measurement method (The current meter is inserted in cold line)



*2) Frequency in this range can mala the characterisitics of electric and optics maintain in +/- 10% except hue.

Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, please adjust lamp frequency, and keep inverter as far from module as possible or use electronic shielding between inverter and module to avoid the interference.

- *3) The design starting lamp voltage for inverter must probably be equal to or higher than maximum starting lamp voltage.
- *4) While the inverter lights up a whole lamp at once, it should provide the lowest value of lamp' s starting voltage.
- *5) The lamp assembled in the LCD module and connected with the inverter would not be lit up besides the rising rate of starting voltage inferior to 3 kV/s.

*6) Definition of the lamp life time :

- a. Luminance (L) under 50% of specification starting lamp voltage
- b. Starting Lamp Voltage: $V_S > 950V, T_a=25^{\circ}C$
 $V_S > 1300V, T_a=0^{\circ}C$

When lamp current over 8.0mA, lamp life time will drop rapidly. If it lower than 3.0mA, the lamp will be damaged.

*7) The condition of Turn-on and Turn-off operation is as below:

- a. Lamp current is 6.5mA
- b. Frequency is 10 sec.(on)/10 sec.(off)
- c. Repeat it for 10 thousand times
- d. The lamp hue variation must smaller than 0.015
- e. It should not have motion fail when starting lamp voltage is lower than 130%of the initial value.

4. INTERFACE PIN CONNECTION

4.1 CN1

Outlet connector: DF14H-20P-1.25H

Plug connector: DF14-20S-1.25C

PIN #	SYMBOL	FUNCTION
1	VDDD	+3.3V Power Supply
2	VDDD	+3.3V Power Supply
3	GND	GND
4	GND	GND
5	RXIN0-	Negative LVDS Differential Data Input
6	RXIN0+	Positive LVDS Differential Data Input
7	GND	GND
8	RXIN1-	Negative LVDS Differential Data Input
9	RXIN1+	Positive LVDS Differential Data Input
10	GND	GND
11	RXIN2-	Negative LVDS Differential Data Input
12	RXIN2+	Positive LVDS Differential Data Input
13	GND	GND
14	RXCLK	Negative LVDS Differential Clock Input
15	RXCLK	Positive LVDS Differential Clock Input
16	GND	GND
17	RXIN3-	Negative LVDS Differential Data Input
18	RXIN3+	Positive LVDS Differential Data Input
19	GND	GND
20	NC	Reserved

- 1) Keep the NC Pin and don't connect it to GND or other signals.
- 2) GND Pin must connect to the ground, don't let it be a vacant pin.

4.2 CN2,3,4,5 (BACKLIGHT)

Backlight-side connector: BHsR-02VS-1 (JST)

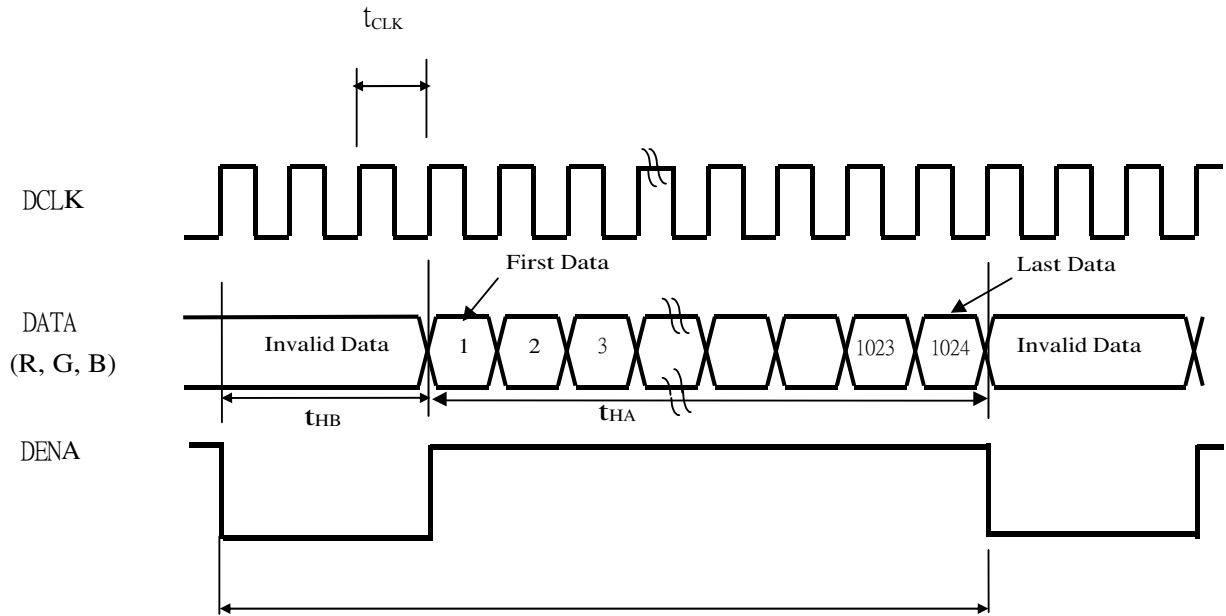
Inverter-side connector: SM02B-BHsS-1 (JST)

CN2

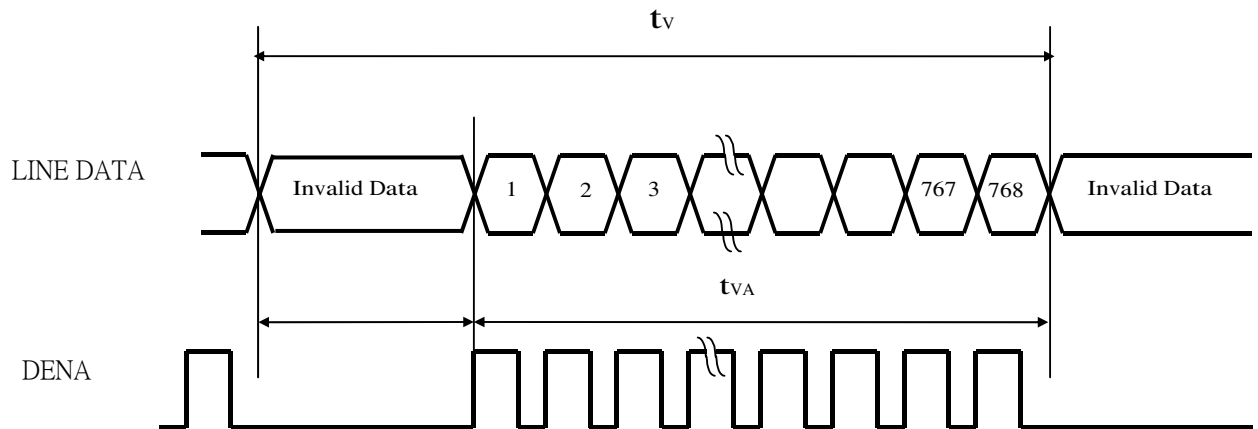
PIN #	SYMBOL	FUNCTION
1	CTH1~4	High Voltage
2	CTL1~4	Low Voltage

5. INTERFACE TIMING

5.1 Horizontal signal:



5.2 Vertical signal:



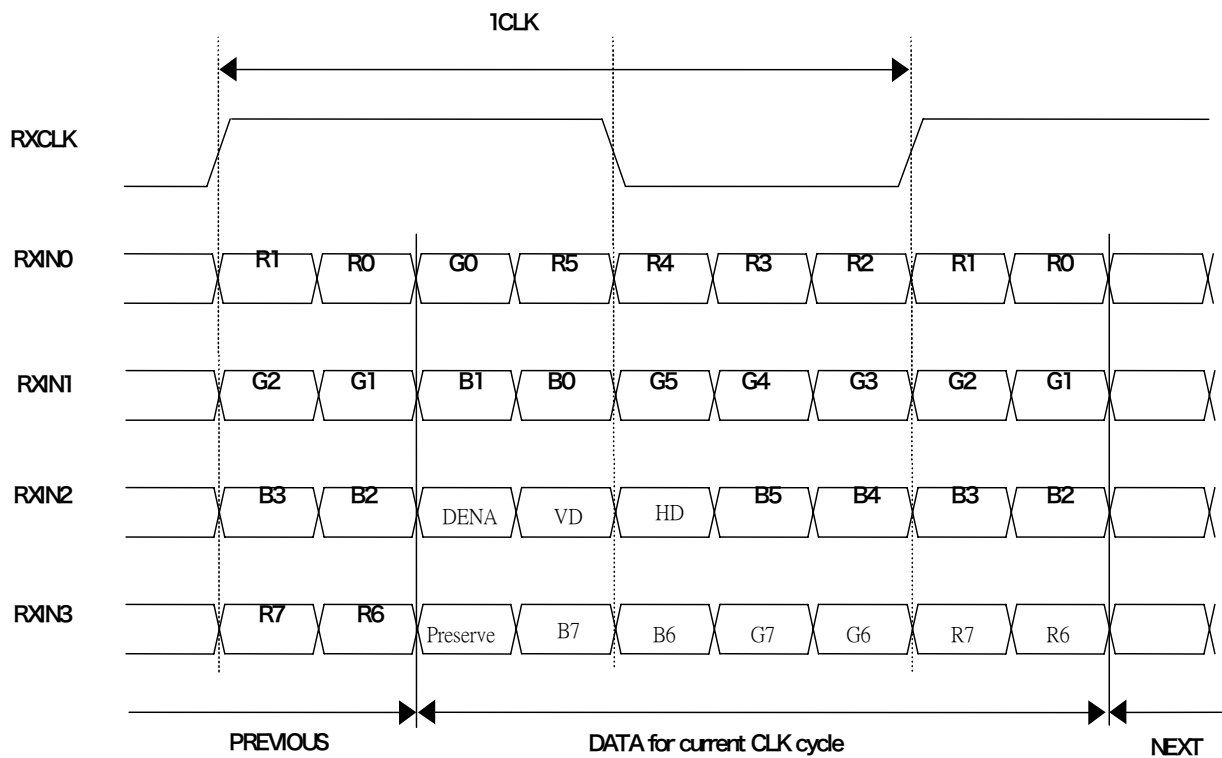
5.3 Timing Chart

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT		
LCD Timing	DCLK	Freq.	f_{CLK}	40	65	80	MHz	
		Cycle	t_{CLK}	12.5	15.3	20	ns	
	DENA	Horizontal	Vertical line Rate	f_H	35.73	48.36	58.39	kHz
			Horizontal total time	t_H	1150	1344	1370	t_{CLK}
			Horizontal effective time	t_{HA}	--	1024	--	t_{CLK}
			Horizontal blank time	t_{HB}	126	320	346	t_{CLK}
	DENA	Vertical	Vertical frame Rate*1)*2)	Fr	45	60	75	Hz
			Vertical total time	t_V	794	806	860	t_H
			Vertical effective time	t_{VA}	--	768	--	t_H
			Vertical blank time	t_{VB}	26	38	92	t_H

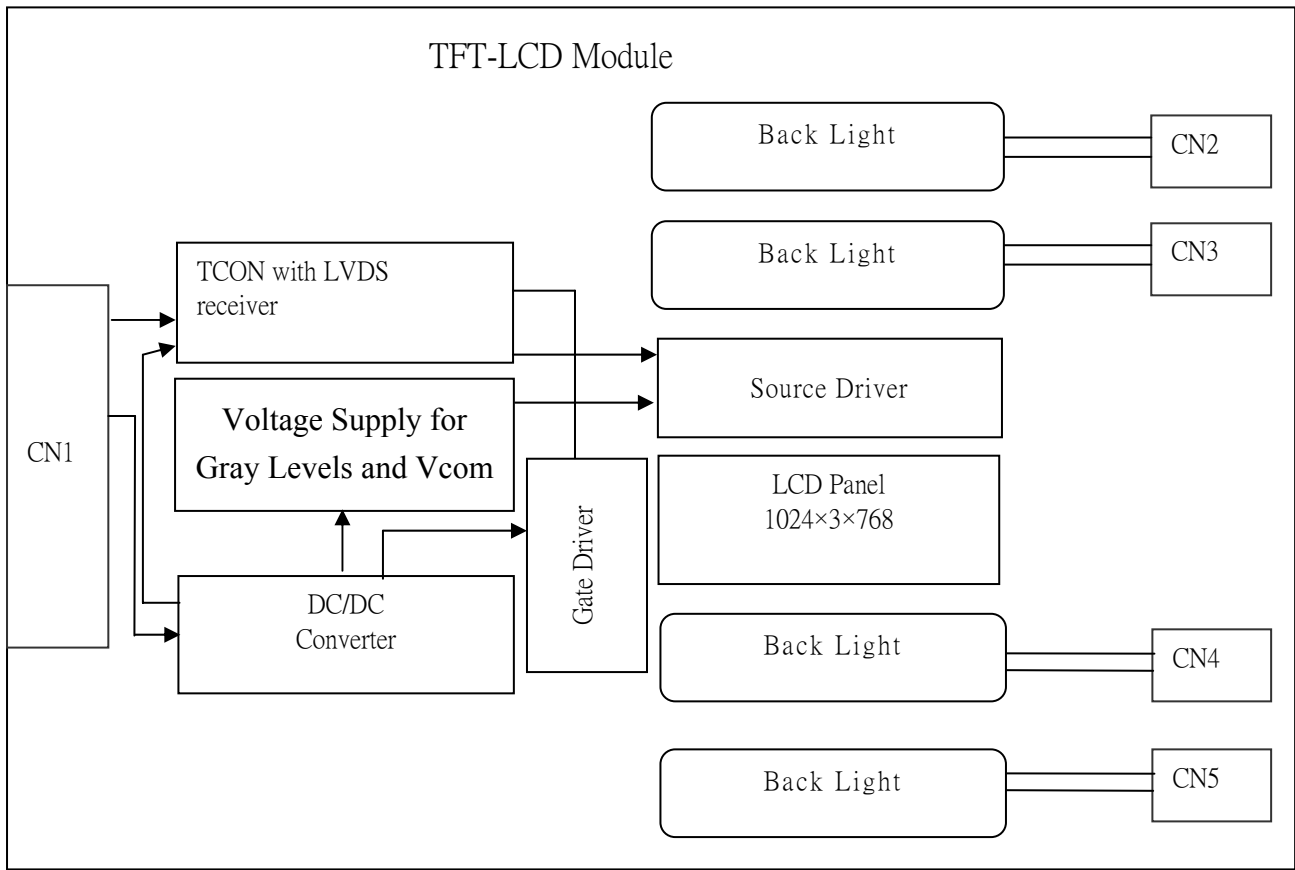
*1) NTSC specification : vertical frame rate 57~63 Hz.

*2) PAL specification : vertical frame rate (47)~(53) Hz.

6.DATA MAPPING



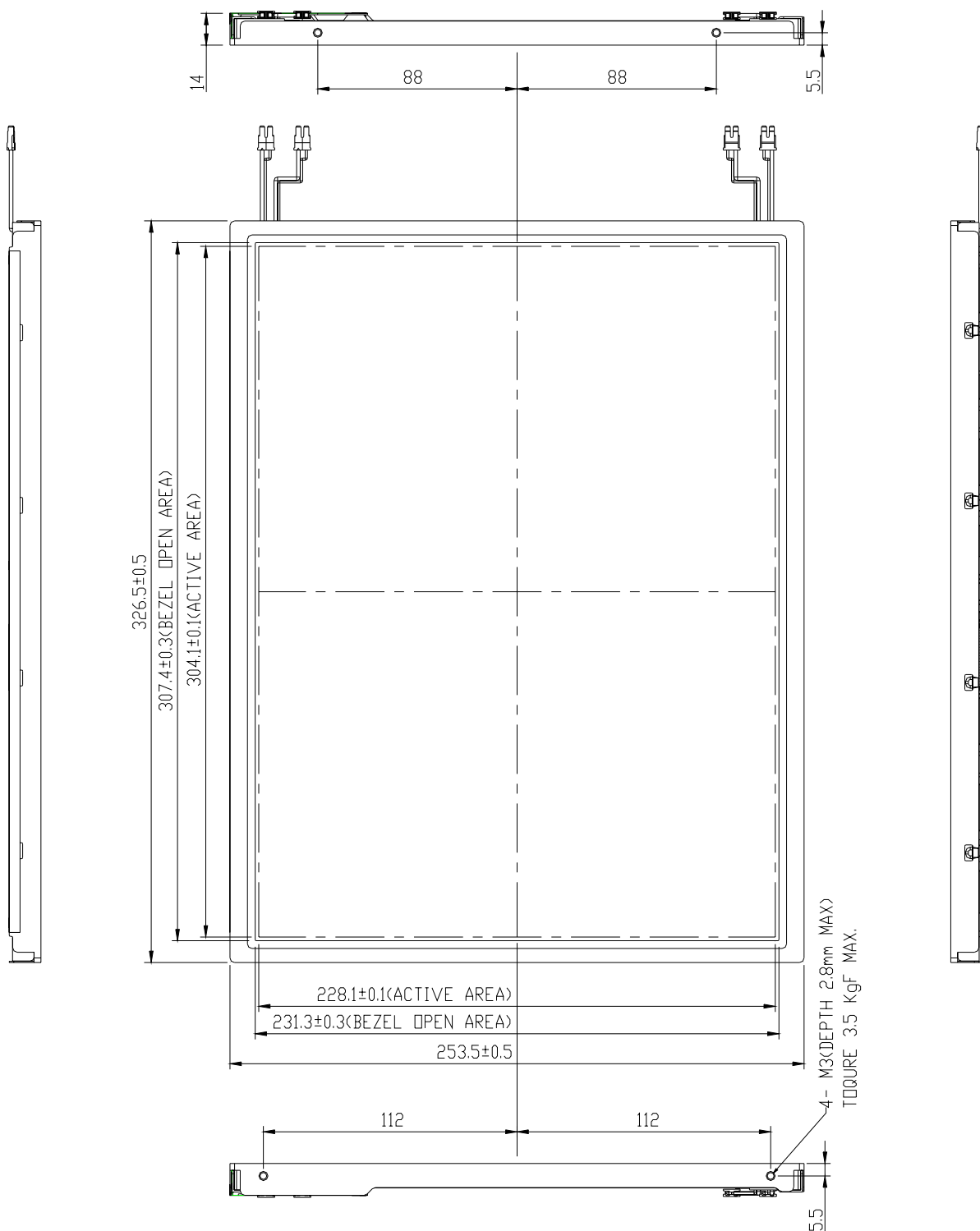
8. BLOCK DIAGRAM



9. MECHANICAL SPECIFICATION

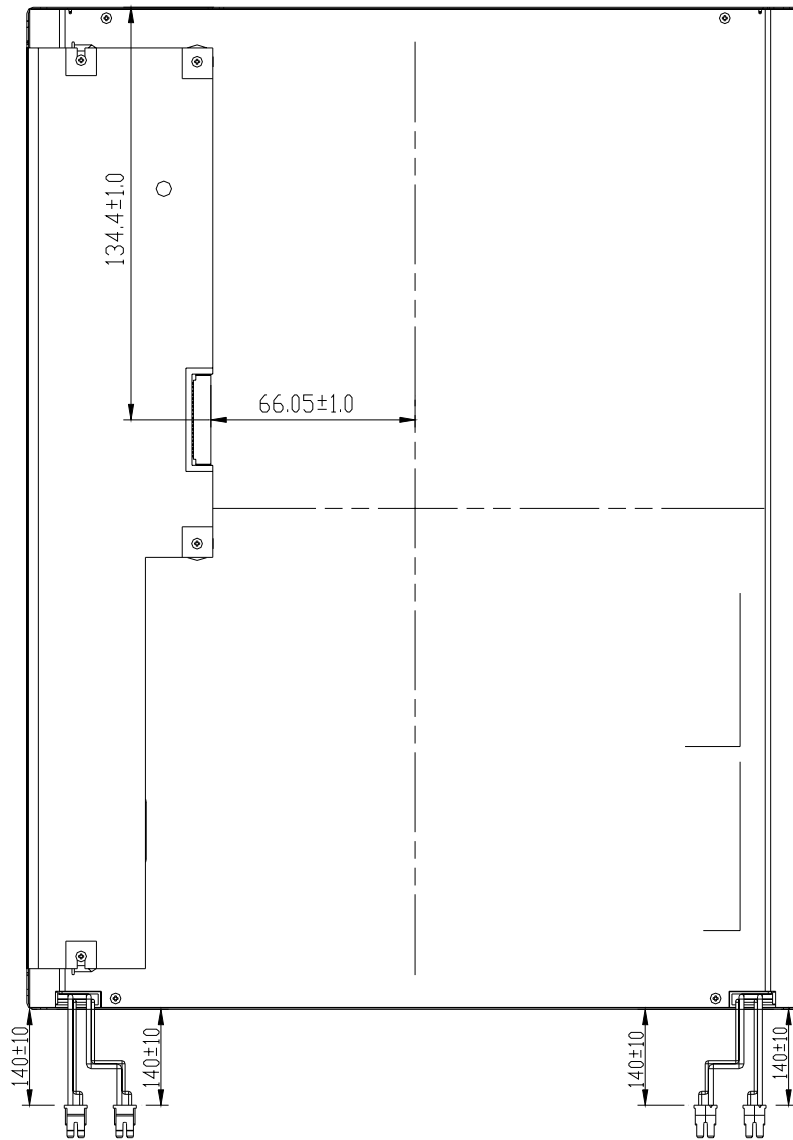
9.1 Front side (Tolerance is ± 0.5mm unless noted)

[Unit: mm]



Rear side (Tolerance is $\pm 0.5\text{mm}$ unless noted)

[Unit: mm]



10.OPTICAL CHARACTERISTICS

Ta = 25°C, VCC=3.3V

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	
Contrast (CEN)	CR	$\theta = \phi = 0^\circ$	450	500	550	--	*1)	
Luminance (CEN)	L	$\theta = \phi = 0^\circ$	350	450	--	cd/m ²	*2)	
9P Luminance (AVG)	L	$\theta = \phi = 0^\circ$	300	400	--	cd/m ²	*2)	
9P Uniformity	ΔL	$\theta = \phi = 0^\circ$	75	80	--	%	*2)	
Response Time	Tr	$\theta = \phi = 0^\circ$	--	5	6	ms	*4)	
	Tf	$\theta = \phi = 0^\circ$	--	11	14	ms		
Cross talk (crosstalk)	CMR	$\theta = \phi = 0^\circ$	--	--	1	%	*7)	
Image sticking	Tis	2 hours	0	--	6	s	*5)	
View angle	Horizontal	ϕ	$CR \geq 5$	-75~75	-85~85	--	Deg.	*3)
	Vertical	θ		-75~75	-85~85	--	Deg.	
View angle	Horizontal	ϕ	$CR \geq 10$	-60~60	-70~70	--	Deg.	*3)
	Vertical	θ		-50~55 (TN Type)	-60~65 (TN Type)	--	Deg.	
Color Coordinates	White	X	$\theta = \phi = 0^\circ$	0.252	0.282	0.312	--	--
		Y		0.263	0.293	0.323		
	Red	X		0.613	0.643	0.673		
		Y		0.305	0.335	0.365		
	Green	X		0.267	0.297	0.327		
		Y		0.558	0.588	0.618		
	Blue	X		0.112	0.142	0.172		
		Y		0.048	0.078	0.108		
Gamma	r	--	(2.0)	(2.2)	(2.4)	--	*6)	

[Note]

These items are measured using BM-5A (TOPCON) under the dark room condition (no ambient light).

Measurement Condition: IL=6.5±0.1mA

Inverter: MPT-M034, Frequency=50kHz.

Definition of these measurement items is as follows:

*1) Definition of Contrast Ratio

CR=ON (White) Luminance/OFF (Black) Luminance

*2) Definition of Luminance and Luminance uniformity

Central luminance: The white luminance is measured at the center position “5” on the screen, see Fig.1 below.

9P Luminance (AVG): The white luminance is measured at measuring points 1 to 9, see Fig.1 below.

9P Uniformity: $\Delta L = (L_{MIN} / L_{MAX}) \times 100\%$

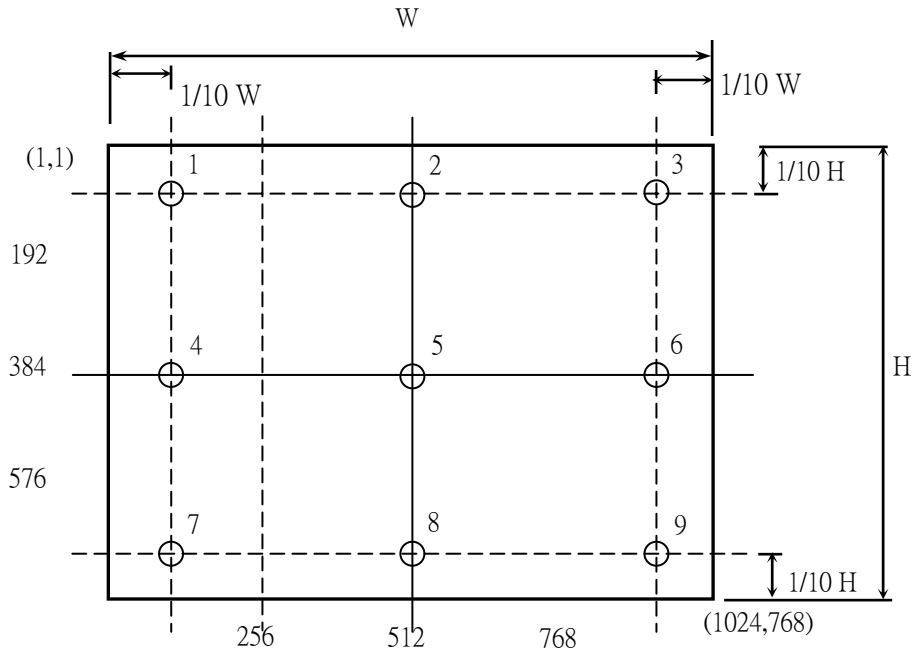
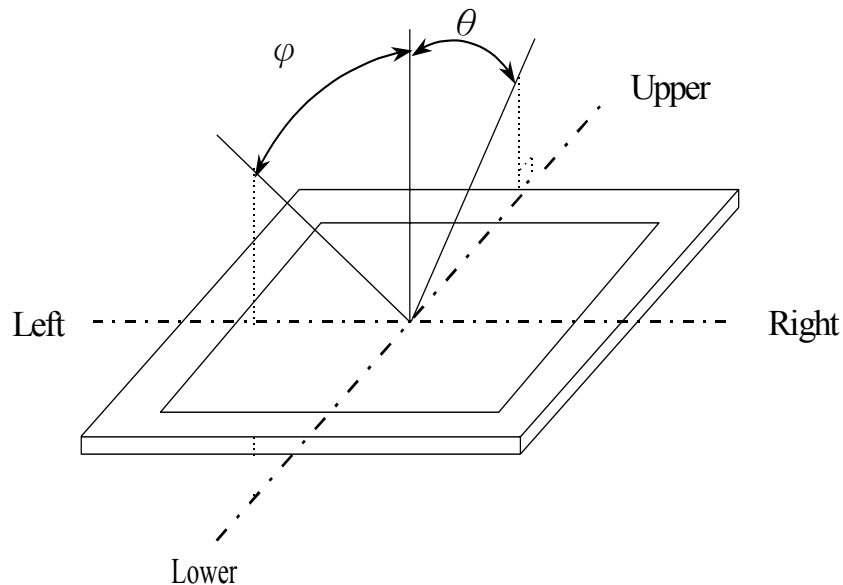


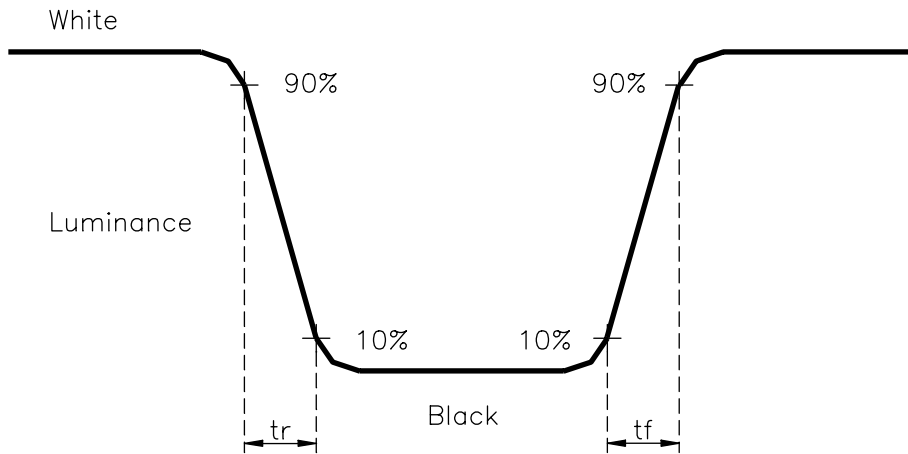
Figure 1. Measurement positions

*3) Definition of Viewing Angle(θ, ϕ)



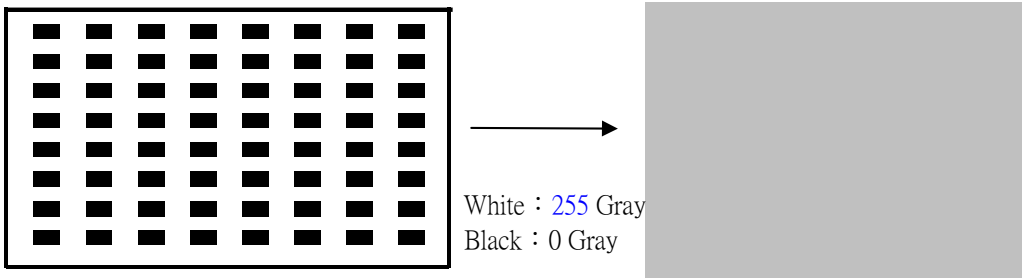
***4) Definition of Response Time**

Change the module frame to Black/white pattern and use Westar TDR-100 to measure tr and tf under 25°C room temperature.



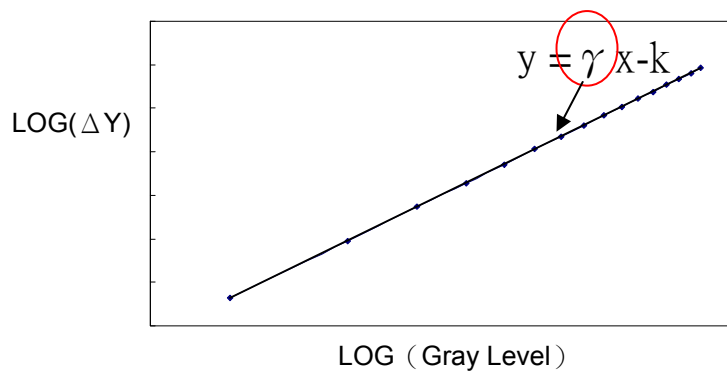
***5) Image sticking:**

Continuously display the test pattern showing in the below figure for 2hrs at 25°C. Then switch to gray pattern (the 120nd gray level pattern), and the previous image should not persist more than 6 sec.



***6) Defination Gamma(VESA)**

Based on Customer Sample, take the average value as a standard center value and the variation range of Gamma value caused by loop voltage error should be between +/- 0.2. the bellow figure shows how to obtain the gamma curve and γ (from gray level: 0、16、32----224、240、255).



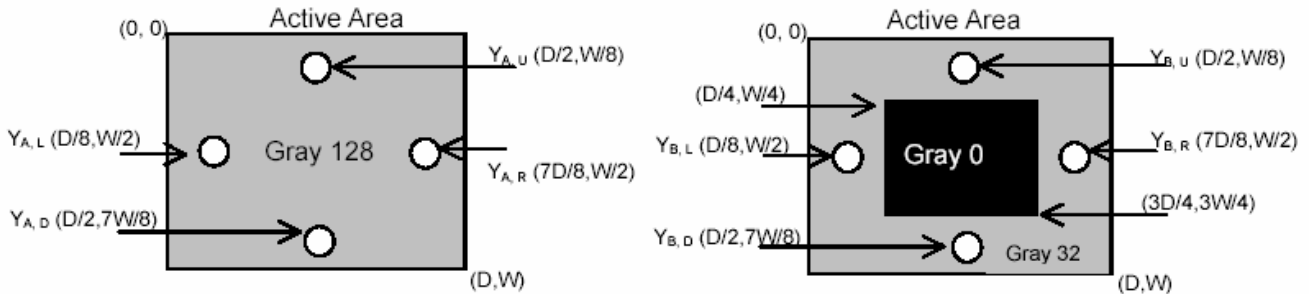
*7) Crosstalk Modulation Ratio:

$$CT = |Y_B - Y_A| / Y_A \times 100\%$$

Y_A 、 Y_B measure position and definition

Y_A means luminance at gray level 128(exclude gray level 0 pattern)

Y_B means luminance at gray level 128(include gray level 0 pattern)



11.RELIABILITY TEST CONDITIONS

(1)Temperature and Humidity

TEST ITEMS	CONDITIONS
High Temperature High Humidity Operation	50°C ; 90%RH; 240hrs (No condensation)
High Temperature High Humidity Storage	60°C ; 90%RH; 48hrs (No condensation)
High Temperature Operation	50°C ; 240hrs
High Temperature Storage	60°C ; 240hrs
Low Temperature Operation	0°C ; 240hrs
Low Temperature Storage	-20°C ; 240hrs

(2) Shock & Vibration

ITEMS	CONDITIONS
Shock (Non-Operation)	Shock level: 1470m/s ² (150G) Waveform: half sinusoidal wave, 2ms Number of shocks: one shock input in each direction of three mutually perpendicular axes for a total of six shock inputs
Vibration (Non-Operation)	Vibration level: 9.8m/s ² (1.0G) zero to peak Waveform: sinusoidal Frequency range: 5 to 500 Hz Frequency sweep rate: 0.5 octave/min Duration: one sweep from 5 to 500Hz in each of three mutually perpendicular axis(each x,y,z axis: 1 hour, total 3 hours)

(3)ESD test

Test Item	Test statements
Connector	200 pF · 0 Ω · ±250 V By using contact-mode to discharge each pin one time and then check the module frame.

module	<ol style="list-style-type: none">1. Under test conditions, by using air-mode to discharge each test point 25 times continuously and then check the module frame.2. Under test conditions, by using contact-mode to discharge each test point of panel frame 25 times continuously and then check the module frame.
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(4) Judgment standard

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

12. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products;

12.1 ASSEMBLY PRECAUTION

- (1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- (2) Please design display housing in accordance with the following guidelines.
 - Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
 - Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
 - When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
 - Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
 - Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.
- (3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- (4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- (5) Please wipe out LCD panel surface with absorbent cotton or soft clothe in case of it being soiled.
- (6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.
- (7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- (8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
- (9) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.

12.2 OPERATING PRECAUTIONS

- (1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- (2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.
- (3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- (4) A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.
- (5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.
- (6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

12.3 PRECAUTIONS WITH ELECTROSTATICS

- (1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.
- (2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

12.4 STORAGE PRECAUTIONS

- (1) When you store LCDs for a long time, it is recommended to keep the temperature between 0°C ~40°C without the exposure of sunlight and to keep the humidity less than 90%RH.
- (2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH.
- (3) Please do not leave the LCDs in the environment of low temperature; below -20°C.

12.5 SAFETY PRECAUTIONS

- (1) When you waste LCDs, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

12.6 OTHERS

- (1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight and strong UV rays.
- (2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

(3) For the packaging box, please pay attention to the followings:

- Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- Please do not pile them up more than 3 boxes. (They are not designed so.) And please do not turn over.
- Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)