

Chunghwa Picture Tubes, Ltd. Technical Specification

To : **YIH HSING ENTERPRISE CO.,LTD.** Date : 2004/05/05

CPT TFT-LCD

CLAA150XP 01

ACCEPTED BY :

APPROVED BY	CHECKED BY	PREPARED BY
		TFT-LCD Product Planning Management General Division

CHUNGHWA PICTUER TUBES, LTD.

1127 Hopin Rd., Padeh, Taoyuan, Taiwan 334, R.O.C. TEL: +886-3-3675151 FAX: +886-3-377-3001

Doc.No:	CLAA150XP01-Yih Hsing-VER1-2004/05/05	Issue Date:	2004/05/05
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1. OVERVIEW

CLAA150XP01 is 15" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight. By applying 8 bit digital data, 1024 ×768, 162M-color images are displayed on the 15" diagonal screen. Interface of data and control signals is Typ. 65 MHz digital. Inverter for backlight is not included in this module. General specification are summarized in the following table:

ITEM	SPECIFICATION
Display Area (mm)	304.1(H)x228.1(V) (15.0-inch diagonal)
Number of Pixels	1024 (H) ×768(V)
Pixel Pitch (mm)	0.297(H) ×0.297(V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white, TN
Number of Colors	16.2M(6bits+FRC)
Brightness (cd/m^2)	250 cd/m ² (Typ.)(center, 8.0mA)
Viewing Angle	140/125(Typ.)
Wide Viewing Angle Technology	Optical Compensation Film
Surface Treatment	Anti-glare
Response Time	16ms
Color Saturation	65%
Total Module Power (W)	10.4+2.1(Typ.)
Optimum Viewing Angle	6 o'clock
Module Size (mm)	326.5(W) ×253.5(H) ×11.0(D) (Typ.)
Module Weight (g)	1060(Typ.)
Backlight Unit	CCFL, 2 tables, edge-light (top/bottom)

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cables, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for above application or not listed in "Standard" as follows, please contact our sales people in advance.

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

2. ABSOLUTE MAXIMUM RATINGS

The following are maximun values which, if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN.	MAX.	UNIT	Remark
Power Supply Voltage For LCD	VDDD	-	4.0	V	
IDDD Rush Current	IRUSHd	-	4.0	А	*1)
Lamp Voltage	VL	522	700	Vrms	
Lamp Current	IL	3	8.5	mArms	
Lamp Frequency	FL	40	80	kHz	
Operation Temperature (Surrounding) *1)	Top	0	50	Ĵ	*2), 3), 4), 5)
Storage Temperature *1)	Tstg	-20	60	°C	*2), 3), 4)

[Note]

*1) In 100 μsec ' but in the situation of longer VDD rising time, the RUSH value will get smaller.

*2) The relative temperature and humidity range are as below sketch, 90%RHMax. (Ta $\leq 40^{\circ}$ C)

*3) The maximum wet bulb temperature $\leq 39^{\circ}C(Ta > 40^{\circ}C)$ and without dewing.

*4) If you use the product in a environment which over the definition of temperature and humidity too long to effect the result of eye-atching.

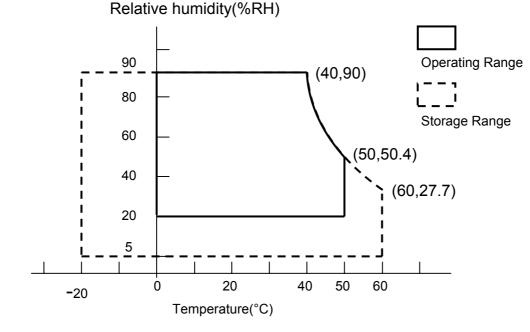
*5) If you operate the product in normal temperature range, the center surface of panel should be under 60°°C.

Humidity :

Humidity $\leq 85\%$ RH without condensation.

Relative Humidity $\leq 90\%$ (Ta ≤ 40 °C)

Wet Bulb Temperature $\leq 39^{\circ}C(Ta \geq 40^{\circ}C)$



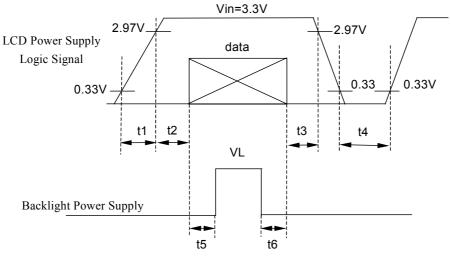
3. ELECTRICAL CHARACTERISTICS

3.1 TFT-LCD Ta=25°C												
ITEM	SYMBOL	MIN	ТҮР	MAX	UNIT	REMARK						
Power Supply Voltage for Logic	VDDD	3.0	3.3	3.6	V	*1)						
Power Supply Current for Logic	IDDD		600	700	mA	*2)						
Permissive Ripple Voltage for Logic	VRPd			100	mVp-p	Vin=+3.3V						
Differential impendence	Zm	90	100	110	Ω	*3)						

[Note]

*1)Power
` data sequence

 $t1 \leq 10ms$ $1 \sec \leq t4$ $0 < t2 \leq 50ms$ $200ms \leq t5$ $0 < t3 \leq 50ms$ $200ms \leq t6$

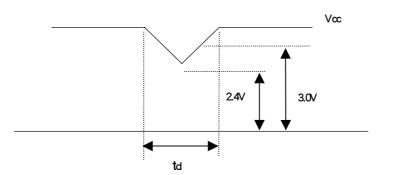


Data: RGB DATA, DCLK, DENA

VCC-dip state :

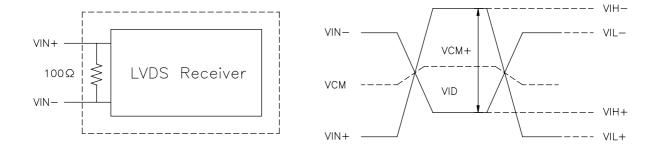
1) When 2.4 V \leq VCC < 3.0V, td \leq 10 ms.

2) VCC-dip condition should also follow the VCC-turn-off condition.



*2) Typical current situation: 0~255-gray-bar pattern, 768 line mode, VCC=+3.3V, f_{CLK}=65 MHz.

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*3) Definition of the LVDS data
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VIN+ : Positive differential DATA & CLK inputVIN- : Negative differential DATA & CLK input

 $VID = VIN_{+} - VIN_{-}$ $\Delta VCM = | VIN_{+} - VCM_{-} |$ $\Delta VID = | VIN_{+} - VID_{-} |$ $VID_{+} = | VIN_{+} - VIH_{-} |$ $VID_{-} = | VIN_{+} - VIL_{-} |$ $VCM = (VIN_{+} + VIN_{-})/2$ $VCM_{+} = (VIN_{+} + VIH_{-})/2$ $VCM_{-} = (VIN_{+} + VIL_{-})/2$

3.2 Backlight

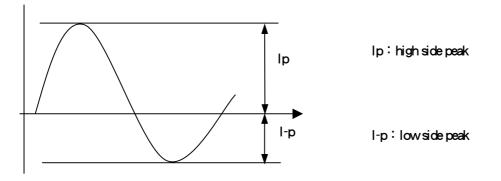
(a) Electrical Charac	(a) Electrical Characteristics Ta=25°C											
ITEM	SYMBOL	MIN	ТҮР	MAX	UNIT	REMARK						
Lamp Voltage	VL	522	580	638	Vrms	IL=8.0mA						
Lamp Current	IL	6	8.0	8.5	mArms	*1)						
Inverter Frequency	FI	40	50	60	kHz	*2)						
Starting Lamp	VS	1280			Vrms	Ta=0°℃						
Voltage	V 3	985			Vrms	Ta=25℃						

(b) Lamp Life Time

ITEM	IL @3.0 mA	IL @8.0 mA	IL @8.5 mA	UNIT	REMARK
Lamp Life Time	Min. 50,000	Min. 30,000	Min. 20,000	hr	Continuous Operation*3)
Turn-on and Turn-off Operation		Min. 100,000		time	Continuous Operation*4)

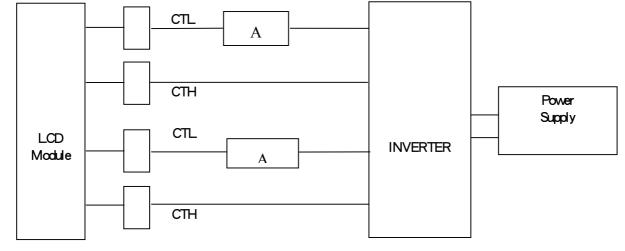
[Note] If the waveform of light up-driving is asymmetric, the distribution of mercury inside the lamp tube will become unequally or will deplete the Ar gas in it. Then it may cause the abnormal phenomenon of lighting-up. Therefore, designers have to try their best to forfill the conditions under the inverter designing-stage as below:

- The degrees of unbalance : <10%
- The ratio of wave height : $<\sqrt{2} \pm 0\%$



A: The degrees of unbalance = $|lp - l-p| / Irms \times 100(\%)$ B: The ratio of wave height = Ip(or I-p)/Irms

*1) Lamp Current measurement method (The current meter is inserted in cold line)



*2) Frequency in this range can mala the characterisitics of electric and optics maintain in +/- 10% except hue.

Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, please adjust lamp frequency, and keep inverter as far from module as possible or use electronic shielding between inverter and module to avoid the interference.

- *3) Definition of the lamp life time :
 - a. Luminance (L) under 50% of specification starting lamp voltage
 - b. Starting Lamp Voltage: VS < 985V, Ta=25°C

VS < 1280V, Ta=0°C

When lamp current over 8.0mA, lamp life time will drop rapidly. If over 8.5mA, it will come up safety issue. But if it lower than 3.0mA, the lamp will be damaged.

- *4) The condition of Turn-on and Turn-off operation is as below:
 - a. Lamp current is 8.0mA
 - b. Frequency is 10 sec.(on)/10 sec.(off)
 - c. Repeat it for 10 thousand times
 - d. The lamp hue variation must smaller than 0.015
 - e. It should not have motion fail when starting lamp voltage is lower than 130% of the initial value.

4. INTERFACE PIN CONNECTION

4.1 CN1

Outlet connector: DF14H-20P-1. 25H Plug connector: DF14 –20S-1.25C

PIN #	SYMBO	FUNCTION
1	VDDD	+3.3V Power Supply
2	VDDD	+3.3V Power Supply
3	GND	GND
4	GND	GND
5	RXIN0-	Negative LVDS Differential Data Input
6	RXIN0+	Positive LVDS Differential Data Input
7	GND	GND
8	RXIN1-	Negative LVDS Differential Data Input
9	RXIN1+	Positive LVDS Differential Data Input
10	GND	GND
11	RXIN2-	Negative LVDS Differential Data Input
12	RXIN2+	Positive LVDS Differential Data Input
13	GND	GND
14	RXCLK	Negative LVDS Differential Clock Input
15	RXCLK	Positive LVDS Differential Clock Input
16	GND	GND
17	RXIN3-	Negative LVDS Differential Data Input
18	RXIN3+	Positive LVDS Differential Data Input
19	GND	GND
20	NC	Reserved

1) Keep the NC Pin and don't connect it to GND or other signals.

2) GND Pin must connect to the ground, don't let it be a vacant pin.

4.2 CN2, 3 (BACKLIGHT)

Backlight-side connector: BHR-03VS-1 (JST) Inverter-side connector: SM03B-BHS-1-TB (JST) CN2

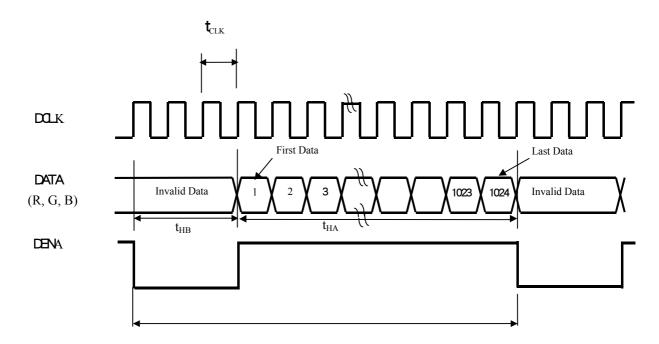
PIN #	SYMBOL	FUNCTION
1	CTH1	High Voltage
2		Empty
3	CTL1	Low Voltage

CN3

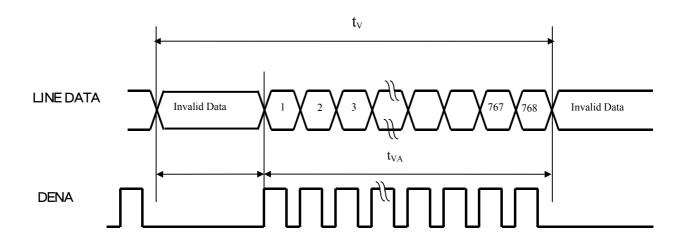
PIN #	SYMBOL	FUNCTION
1	CTH2	High Voltage
2		Empty
3	CTL2	Low Voltage

5. INTERFACE TIMING

5.1 Horizontal signal:

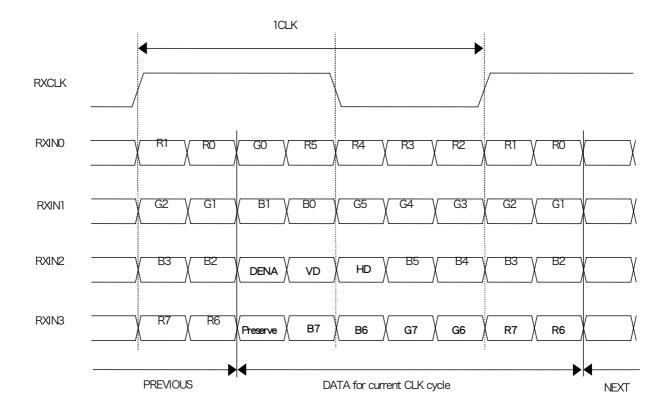


5.2 Vertical signal:



		ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT
	Г	OCLK	Freq.	f _{CLK}	50	65	80	MHz
	DCL		Cycle	t _{CLK}	12.5	15.3	20	ns
			Vertical line Rate	f_H	43.48	48.36	58.39	kHz
			Horizontal total time	$t_{\rm H}$	1150	1344	1370	$t_{\rm CLK}$
		Horizontal	Horiaontal effective time	t _{HA}		1024		t _{clk}
LCD Timing			Horizontal blank time	t _{HB}	126	320	346	t _{clk}
Tinning	DENA		Vertical frame Rate	Fr	55	60	75	Hz
		Vortical	Vertical total time	$t_{\rm V}$	794	806	860	$t_{\rm H}$
		Vertical	Vertical effective time	$t_{\rm VA}$		768		$t_{\rm H}$
			Vertical blank time	$t_{\rm VB}$	26	38	92	$t_{\rm H}$

X CPT accepts Philips request that Frame Rate can be 50 Hz also, but CPT doesn't prosmise any performance and quality issue.



7.COLOR DATA ASSIGNMENT

7.1 Color Data Assignment

Color	Input Data	MS	В		L	SB				MS	В		L	SB				MS	В		L	SB			
Color	Input Data	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red																									
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green																									<u> </u>
			-					-																	
	Green (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\frac{\text{Blue}\left(1\right)}{\text{Blue}\left(2\right)}$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
DI	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue																									
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Dide (233)	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	1	1	1	1	1	1	1	1

[Note]

1) Definition of gray scale:

Color (n): n indicates gray scale level; higher n means brighter level.

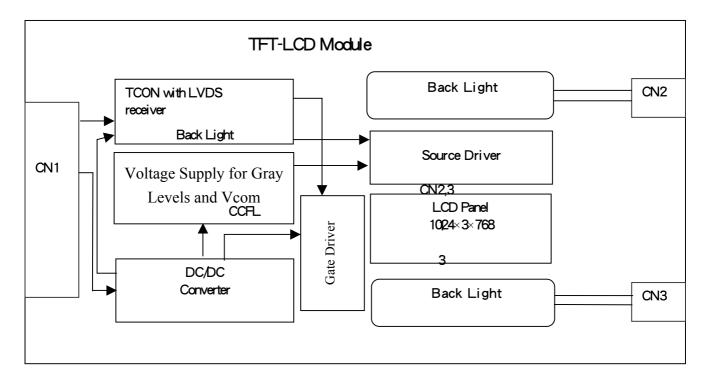
2) Data: 1-High, 0-Low.

7.2 Pixel Mapping

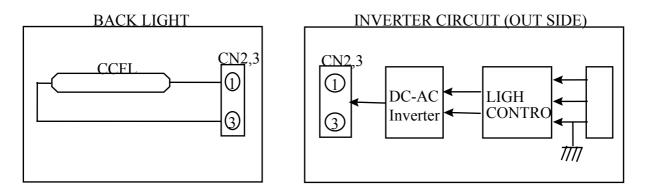
	D(1,1)	D(2,1)		D(X, 1)		D(1023, 1)	D(1024, 1)
	D(1,2)	D(2,2)		D(X, 2)		D(1023, 2)	D(1024, 2)
			+		+		
	D(1, Y)	D(2, Y)		D(X, Y)		D(1023, Y)	D(1024, Y)
			+		+		
	D(1,767)	D(2,767)		D(X,767)			D(1024,767))
	D(1,768)	D(2,768)		D(X,768)		D(1023,768)	D(1024,768)
8. BL	8. BLOCK DIAGRAM						

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8.1 TFT-LCD Module



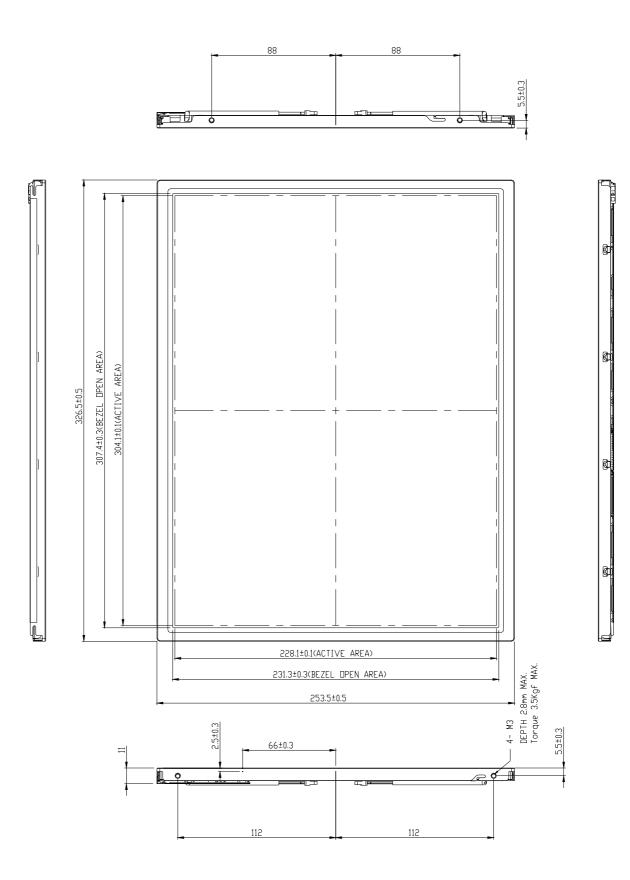
8.2 B/L Unit



9. MECHANICAL SPECIFICATION

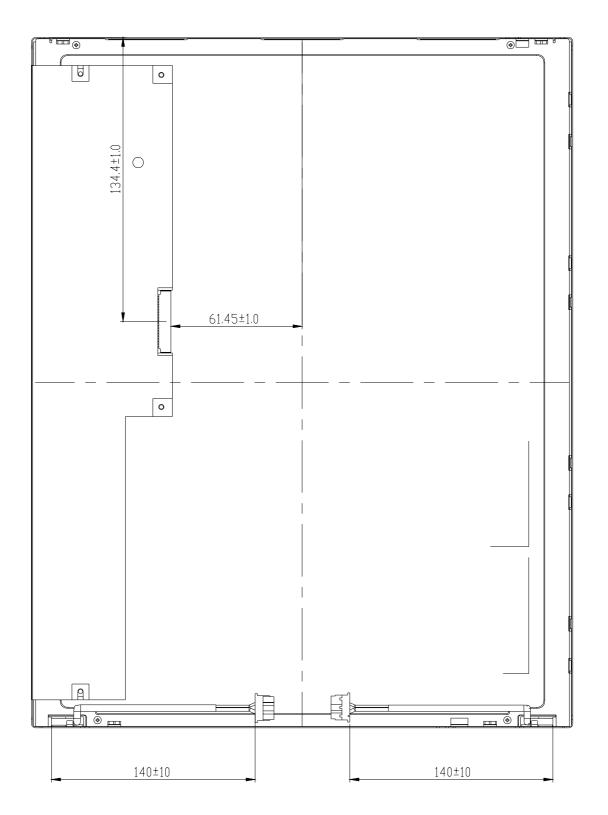
9.1 Front side (Tolerance is \pm 0.5mm unless noted)

[Unit: mm]



9.2 Rear side (Tolerance is ± 0.5 mm unless noted)

[Unit: mm]



								/CC=3.3V
ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Contrast (CEN)		CR	$\theta = \psi = 0^{\circ}$	400	500			*1)
Luminance (CEN)		L	$\theta = \psi = 0^{\circ}$	200	250		cd/m^2	*2)
9P Luminance (AVG)		L	$\theta = \psi = 0^{\circ}$	180	225		cd/m^2	*2)
9P Unif	9P Uniformity		$\theta = \psi = 0^{\circ}$	75	80		%	*2)
Descrete	Response Time		$\theta = \psi = 0^{\circ}$		5	10	ms	*4)
Respons			$\theta = \psi = 0^{\circ}$		11	20	ms	
Cross talk (crosstalk)		CMR	$\theta = \psi = 0^{\circ}$			1	%	*6)
Image sticking		Tis	2 hours	0		2	S	*5)
X7: 1	Horizontal	ψ		-70~70	-85~85		Deg.	*3)
View angle	Vertical	θ	CR≧5	-70~70	-85~85		Deg.	
	Horizontal	ψ		-55~55	-70~70		Deg.	*3)
View angle	Vertical	θ	CR≧10	-45~50	-60~65 (TN Type)		Deg.	
	White	X Y		0.283 0.299	0.313 0.329	0.343 0.359		
Color	Red	X Y	$\theta = \psi = 0^{\circ}$	0.613 0.305	0.643 0.335	0.673 0.365		
Coordinates	Green	X Y	$\theta - \psi - \theta$	0.270 0.555	0.300 0.585	0.330 0.615		
	Blue	X Y		0.113 0.049	0.143 0.079	0.173 0.109		

10.OPTICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$, VCC=3.3V

[Note]

These items are measured using BM-5A (TOPCON) under the dark room condition (no ambient light). Measurement Condition: IL=8.0±0.1mA

Inverter: ConverTek CVT010330BI31402, Frequency=50kHz.

Definition of these measurement items is as follows:

*1) Definition of Contrast Ratio

CR=ON (White) Luminance/OFF (Black) Luminance Fulfil TCO99 1.5.2 Luminance uniformity spec.

*2) Definition of Luminance and Luminance uniformity

Central luminance: The white luminance is measured at the center position "5" on the screen, see Fig.1 below.

9P Luminance (AVG): The white luminance is measured at measuring points 1 to 9, see Fig.1 below.

9P Uniformity: $\Delta L = (L_{MIN} / L_{MAX}) \rtimes 00\%$

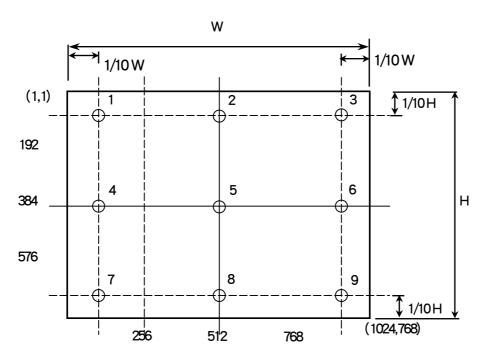
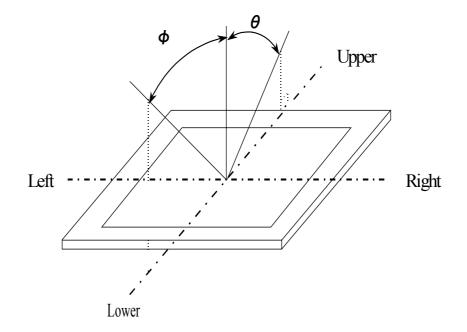


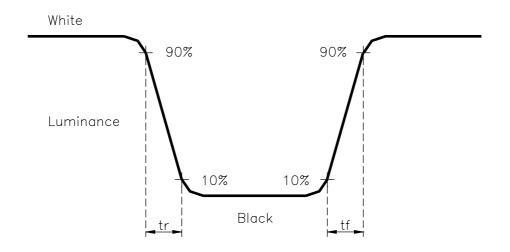
Figure 1. Measurement positions

*3) Definition of Viewing Angle($\boldsymbol{\theta}, \boldsymbol{\psi}$)



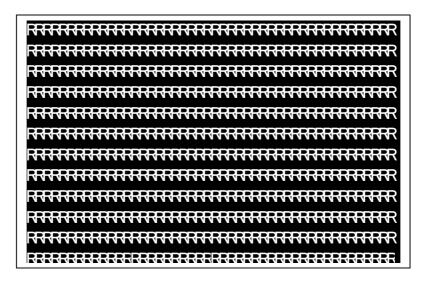
*4) Definition of Response Time

Change the module frame to Black/white pattern and use Westar TDR-100 to measure tr and tf under 25°C room temperature.



*5) Image sticking:

Continuously display the test pattern shown in the figure below for two-hours. Then display a completely white screen. The previous image shall not persist more than two seconds at 25° C.



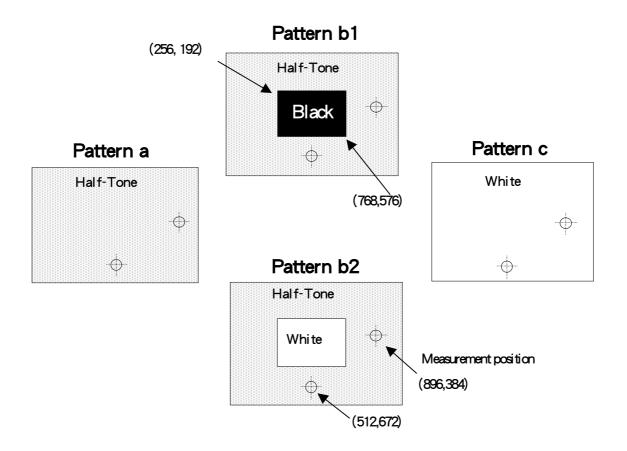
("BLACK" back ground with "WHITE" R's)

 $CRM=MAX((|(LB1-LA)/LC|) \rtimes 00), (|(LB2-LA)/LC|) \rtimes 00)$

LA: Pattern A (Half-Tone pattern) measurement point luninance

LB1 \ LB2 : Pattern B1 \ B2 measurement point luninance

LC: Pattern C (white pattern) measurement point luninance



11.RELIABILITY TEST CONDITIONS

TEST ITEMS	CONDITIONS
High Temperature	40°C; 95%RH; 240hrs
High Humidity Operation	(No condensation)
High Temperature	60°C; 90%RH; 240hrs
High Humidity Storage	(No condensation)
High Temperature Operation	50°C; 240hrs
High Temperature Storage	60°C; 240hrs
Low Temperature Operation	0°C; 240hrs
Low Temperature Storage	-20°C; 240hrs
Thermal Sheelr	Between $-20^{\circ}C(1hr)$ and $60^{\circ}C(1hr)$;
Thermal Shock	100 Cycles

(1)Temperature and Humidity

(2) Shock & Vibration

ITEMS	CONDITIONS
Shock	Shock level: 1470m/s^2(150G)
(Non-Operation)	Waveform: half sinusoidal wave, 2ms
	Number of shocks: one shock input in each direction of three
	mutually perpendicular axes for a total of six shock inputs
Vibration	Vibration level: 9.8m/s ² (1.0G) zero to peak
(Non-Operation)	Waveform: sinusoidal
	Frequency range: 5 to 500 Hz
	Frequency sweep rate: 0.5 octave/min
	Duration: one sweep from 5 to 500Hz in each of three
	mutually perpendicular axis(each x,y,z axis: 1 hour, total 3
	hours)

(3)ESD test

Test Item	Test statements
	200 pF,0 Ω, ±250 V
Connector	By using contact-mode to discharge each pin one time and then check the module frame.
module	 Under test conditions, by using air-mode to discharge each test point 25 times continueously and then check the module frame. Under test conditions, by using contact-mode to discharge each test point of panel frame 25 times continueously and then check the module frame.

(4) Judgment standard

The judgment of the above test should be made as follow:

- Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.
- Fail: No display image, obvious non-uniformity, or line defects.

12. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products;

12.1 ASSEMBLY PRECAUTION

- (1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- (2) Please design display housing in accordance with the following guide lines.
 - Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
 - Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
 - When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
 - Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
 - Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.
- (3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- (4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- (5) Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- (6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.
- (7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- (8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
- (9) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.

12.2 OPERATING PRECAUTIONS

- (1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- (2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.
- (3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- (4) A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.
- (5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.
- (6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

12.3 PRECAUTIONS WITH ELECTROSTATICS

- (1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.
- (2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

12.4 STORAGE PRECAUTIONS

- (1) When you store LCDs for a long time, it is recommended to keep the temperature between 0° C ~40°C without the exposure of sunlight and to keep the humidity less than 90%RH.
- (2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH.
- (3) Please do not leave the LCDs in the environment of low temperature; below $-20^{\circ}C$.

12.5 SAFETY PRECAUTIONS

- (1) When you waste LCDs, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands, wash off throughly with soap and water.

12.6 OTHERS

- (1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays.
- (2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- (3) For the packaging box, please pay attention to the followings:
 - Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
 - Please do not pile them up more than 3 boxes. (They are not designed so.) And please do not turn over.
 - Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)