

Version: 1.0

TECHNICAL SPECIFICATION

MODEL NO: ED060SCG

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Customer's Confirmation
Customer
Date
Ву
E Ink's Confirmation

Approve By

Confirmed By

Prepared By

Prepared By





ED060SCG

Revision History

Rev.	Issued Date	Revised	Contents
1.0	2014-10-14	New	



TECHNICAL SPECIFICATION

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1. General Description

ED060SCG is a reflective electrophoretic E Ink® technology display module based on active matrix TFT substrate. It has 6" active area with 600 x 800 pixels, the display is capable to display images at 2-16 gray levels (1-4 bits) depending on the display controller and the associated waveform file it used.

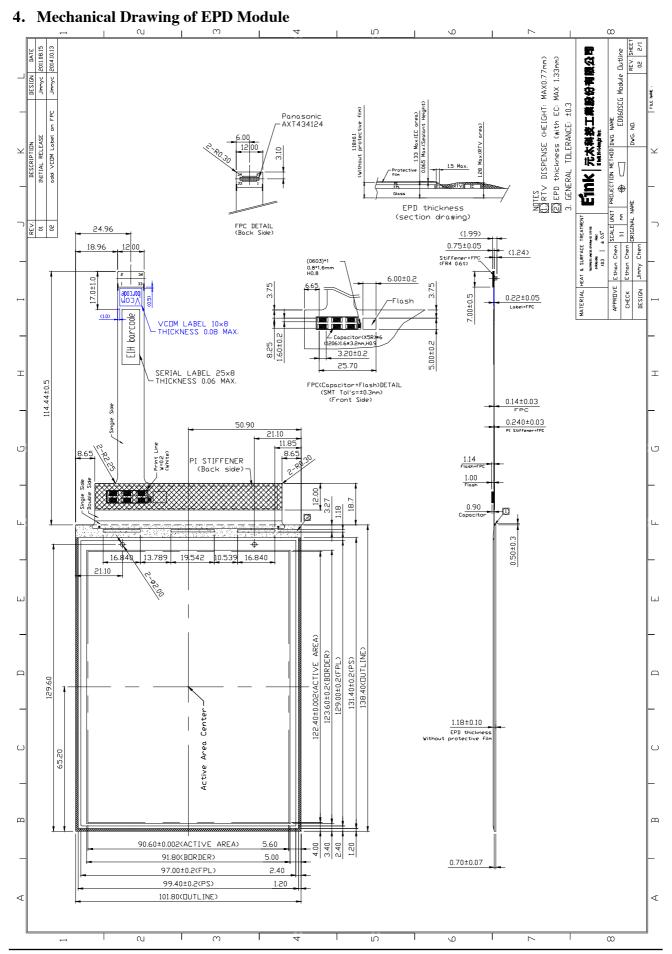
2. Features

- ➤ High contrast reflective/electrophoretic technology
- ➤ 600 x 800 dots resolution
- ➤ High reflectance
- ➤ Ultra wide viewing angle
- > Ultra low power consumption
- > Pure reflective mode
- ➤ Bi-stable
- ➤ Commercial temperature range
- Landscape, portrait mode
- ➤ Antiglare hard-coated front-surface

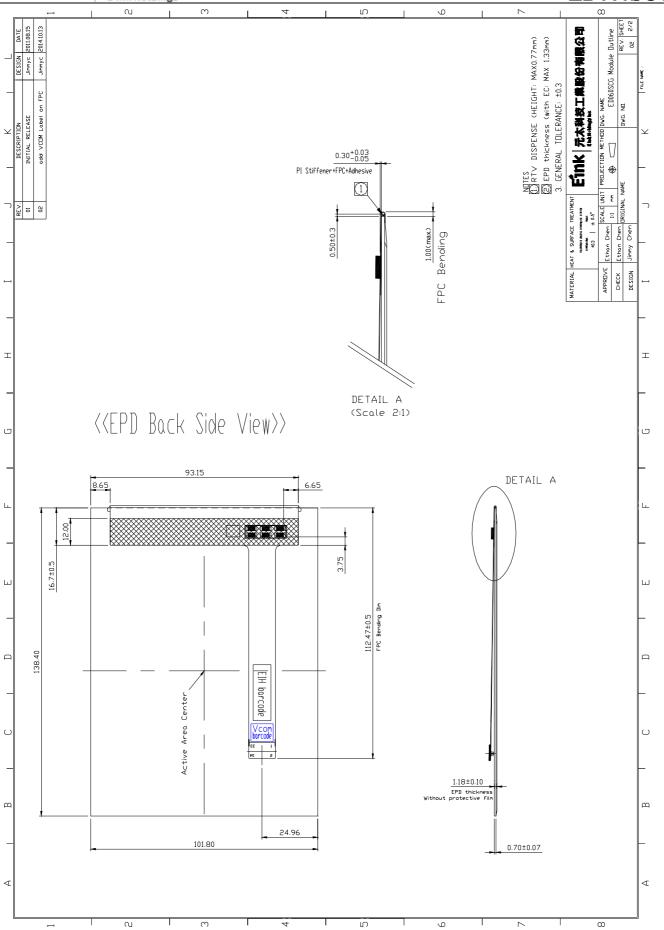
3. Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	6.0 (3:4 diagonal)	Inch	
Display Resolution	600 (H)×800(V)	Pixel	
Active Area	90.6 (H)×122.4 (V)	mm	
Pixel Pitch	0.151 (H)×0.153 (V)	mm	
Pixel Configuration	Rectangle		
Outline Dimension	101.8(W)×138.4(H)×1.18(D) (panel area height)	mm	
Module Weight	34±3.4	g	
Number of Gray	16 Gray Level (monochrome)		
Display operating mode	Reflective mode		
Surface treatment	Anti-glare treatment for protective sheet		





ED060SCG







5. Input/Output Interface

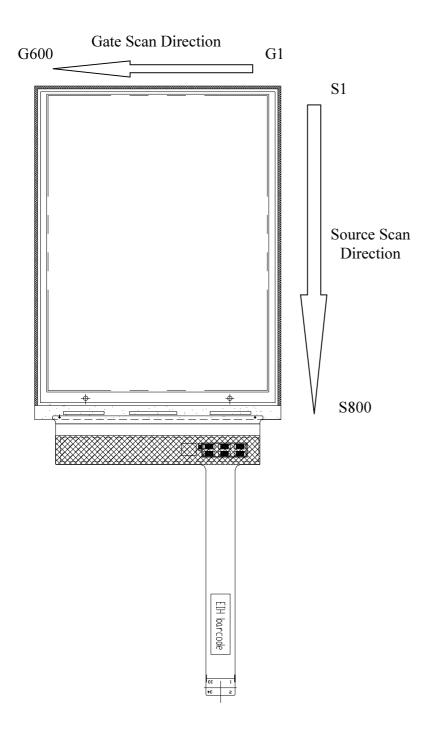
5-1) Connector type: AXT434124

Pin Assignment

Pin #	Signal	Description	Remark
1	VNEG	Negative power supply source driver	
2	VPOS	Positive power supply source driver	
3	VNEG	Negative power supply source driver	
4	VPOS	Positive power supply source driver	
5	VDD	Digital power supply drivers	
6	VSS	Ground	
7	VDD	Digital power supply drivers	
8	VSS	Ground	
9	XCL	Clock source driver	
10	XLE	Latch enable source driver	
11	XOE	Output enable source driver	
12	XSTL	Start pulse source driver	
13	D0	Data signal source driver	
14	D1	Data signal source driver	
15	D2	Data signal source driver	
16	D3	Data signal source driver	
17	D4	Data signal source driver	
18	D5	Data signal source driver	
19	D6	Data signal source driver	
20	D7	Data signal source driver	
21	VCOM	Common connection	
22	NC	No Connection	
23	VCOM	Common connection	
24	NC	No Connection	
25	VGH	Positive power supply gate driver	
26	MODE1	Output mode selection gate driver	
27	VGL	Negative power supply gate driver	
28	CKV	Clock gate driver	
29	VGL	Negative power supply gate driver	
30	SPV	Start pulse gate driver	
31	NC	No Connection	
32	BORDER	Border connection	
33	NC	No Connection	
34	NC	No Connection	



5-2) Panel Scan direction







6.Electrical Characteristics

6-1) Absolute maximum rating

Parameter	Symbol	Rating	Unit	Remark
Logic Supply Voltage	VDD	-0.3 to +7	V	
Positive Supply Voltage	V_{POS}	-0.3 to +18	V	
Negative Supply Voltage	$V_{ m NEG}$	+0.3 to -18	V	
Max .Drive Voltage Range	V _{POS} - V _{NEG}	36	V	
Supply Voltage	VGH	-0.3 to +45	V	
Supply Voltage	VGL	-25.0 to +0.3	V	
Supply Range	VGH-VGL	-0.3 to +45	V	
Operating Temp. Range	TOTR	0 to +50	$^{\circ}\!\mathbb{C}$	
Storage Temperature	TSTG	-25 to +70	$^{\circ}\!\mathbb{C}$	

6-2) Panel DC characteristics

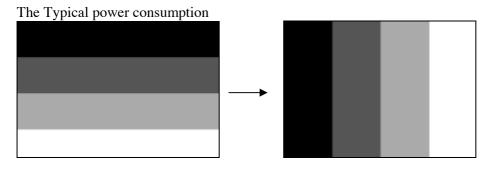
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Signal ground	V_{SS}		-	0	-	V
T ' 37 1, 1	$V_{ m DD}$		3.0	3.3	3.6	V
Logic Voltage supply	I_{VDD}	$V_{DD}=3.3V$	-	1.05	3.15	mA
Cata Nagativa avanla	$ m V_{GL}$		-21	-20	-19	V
Gate Negative supply	${ m I}_{ m GL}$	$V_{\rm GL}$ =-20 V	-	0.8	2.4	mA
Cata Danitiva avanta	$ m V_{GH}$		21	22	23	V
Gate Positive supply	$ m I_{GH}$	$V_{\mathrm{GH}} = 22 \mathrm{V}$	-	0.8	2.4	mA
C N .: 1	$V_{ m NEG}$		-15.4	-15	-14.6	V
Source Negative supply	I_{NEG}	$V_{NEG} = -15V$	-	18	36	mA
Carres Desitive arrests	V_{POS}		14.6	15	15.4	V
Source Positive supply	I_{POS}	$V_{POS} = 15V$	-	16	32	mA
Border supply	V_{com}		-2.5	Adjusted	-0.3	V
Asymmetry source	V_{Asym}	V_{POS} + V_{NEG}	-800	0	800	mV
Common voltage	V_{COM}		-2.5	Adjusted	-0.3	V
Common voltage	I _{COM}		-	0.25	-	mA
Panel Power	P		-	547	1131	mW
Standby power panel	P_{STBY}		-	-	0.4	mW





- The Typical power consumption is measured using 85Hz waveform with following pattern transition: from horizontal 4 gray scale pattern to vertical 4 gray scale pattern. (Note 6-1)
- The standby power is the consumed power when the panel controller is in standby mode.
- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by E Ink
- Vcom is recommended to be set in the range of assigned value \pm 0.1V
- The maximum I_{COM} inrush current is about 800 mA

Note 6-1



6-3) Refresh Rate

The module ED060SCG is applied at a maximum screen refresh rate of 85Hz.

	Min	Max
Refresh Rate	-	85Hz



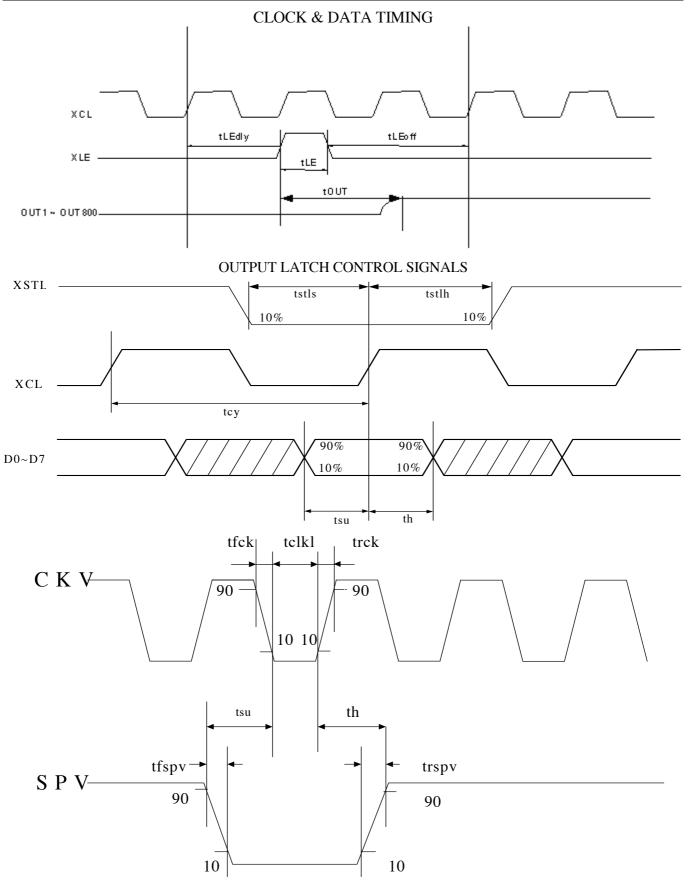


6-4) Source IC & Gate IC Timing Chart

VDD=3.0V to 3.6V, unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max.	Unit	App Pin
Clock frequency	fckv	-	-	200	kHz	
Minimum "L" clock pulse width	twL	0.5	-	-	us	CVV
Clock rise time	trckv	-	-	100	ns	CKV
Clock fall time	tfckv	-	-	100	ns	
Data setup time	tSU	100	-	-	ns	CIVIL CDV
Data hold time	tH	100	-	-	ns	CKV, SPV
Pulse rise time	trspv	-	-	100	ns	CDV
Pulse fall time	tfspv	-	-	100	ns	SPV
Clock XCL cycle time	tcy	50	-	DC	ns	
D0D7 setup time	tsu	8	-	-	ns	
D0 D7 hold time	th	1	-	-	ns	
XLE on delay time	tLEdly	40	-	-	ns	Below
XLE high-level pulse width	tLEw	40	-	-	ns	table
XLE off delay time	tLEoff	200	-	-	ns	
Output setting time to +/- 30mV(C _{load} =200pF)	tout	-	-	12	us	





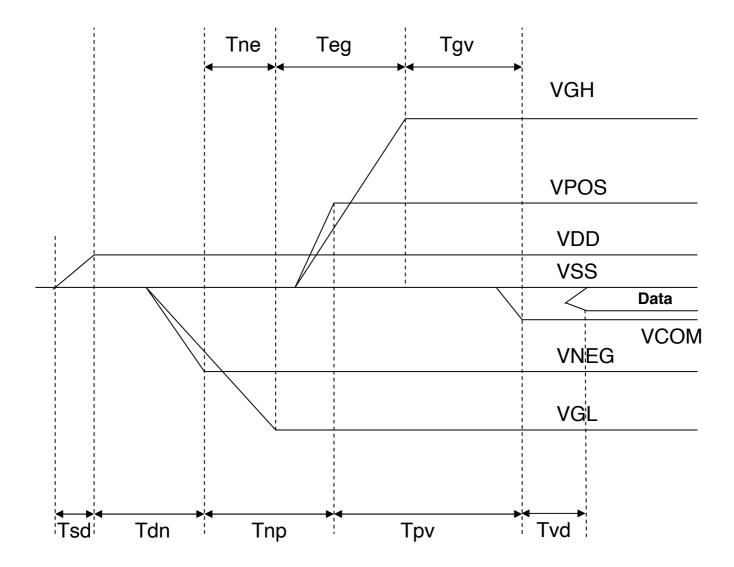


7. Power on Sequence

Power Rails must be sequenced in the following order:

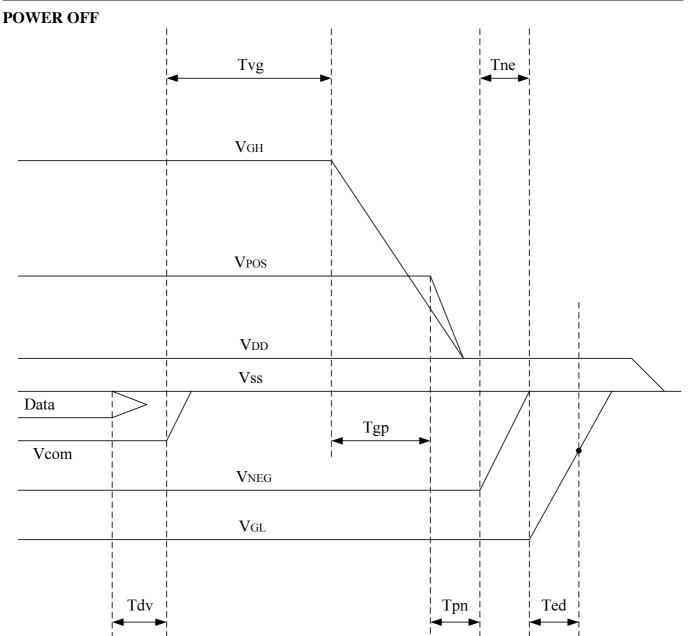
- 1. VSS → VDD → VNEG → VPOS (Source driver) → VCOM
- 2. VSS \rightarrow VDD \rightarrow VGL \rightarrow VGH (Gate driver)

POWER ON



	Min	Max
Tsd	30us	-
Tdn	100us	-
Tnp	1000us	-
Tpv	100us	-
Tvd	100us	
Tne	Ous	
Teg	1000us	-
Tgv	100us	-





	Min	Max	
Tdv	$100\mu\mathrm{s}$	-	
Tvg	0 μ s	-	
Tgp	0 μ s	-	
Tpn	0 μ s	-	
Tne	0 μ s	-	
Ted	0.5s	-	Discharged point @ -7.4 Volt

Note1: Supply voltages decay through pull-down resistors.

Note2: Begin to turn off VGL power after VNEG and VPOS are completely or almost discharged to GND state.

Note3: VGL must remain negative of Vcom during decay period



8. Optical characteristics

8-1) Specifications

Measurements are made with that the illumination is under an angle of 45 degrees, the detection is perpendicular unless otherwise specified.

 $T = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Тур.	Max	Unit	Note
R	Reflectance	White	30	40	-	%	Note 8-1
Gn	N _{th} Grey Level	-	-	DS+(WS-DS) ×n/(m-1)	-	L*	-
CR	Contrast Ratio	-	10	12	-		-

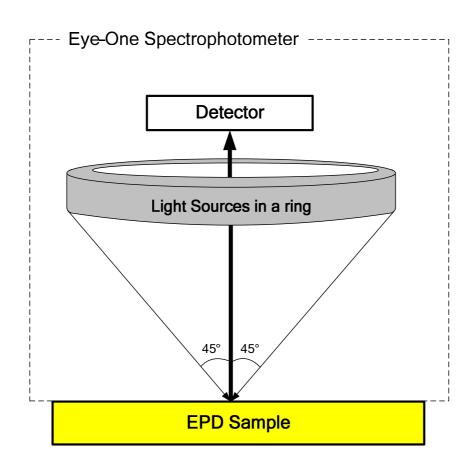
WS: White state, DS: Dark state, Gray state from Dark to White: DS \ G1 \ G2... \ Gn... \ Gm-2 \ WS m:4 \ 8 \ 16 \ when 2 \ 3 \ 4 bits mode

Note 8-1: Luminance meter: Eye – One Pro Spectrophotometer

8-2) Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (Rl) and the reflectance in a dark area (Rd):

CR = RI/Rd







8-3) Reflection Ratio

The reflection ratio is expressed as:

 $R = Reflectance Factor_{white board} \quad x \quad (L_{center} / L_{white board})$

 L_{center} is the luminance measured at center in a white area (R=G=B=1). $L_{white\ board}$ is the luminance of a standard white board. Both are measured with equivalent illumination source. The viewing angle shall be no more than 2 degrees.



9.HANDLING, SAFETY AND ENVIROMENTAL REQUIREMENTS AND REMARK

WARNING

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

CAUTION

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronic components.

Disassembling the display module can cause permanent damage and invalidate the warranty agreements.

IPA solvent can only be applied on active area and the back of a glass. For the rest part, it is not allowed.

Mounting Precautions

- (1) It's recommended that you consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module.
- (2) It's recommended that you attach a transparent protective plate to the surface in order to protect the EPD. Transparent protective plate should have sufficient strength in order to resist external force.
- (3) You should adopt radiation structure to satisfy the temperature specification.
- (4) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the PS at high temperature and the latter causes circuit break by electro-chemical reaction.
- (5) Do not touch, push or rub the exposed PS with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of PS for bare hand or greasy cloth. (Some cosmetics deteriorate the PS)
- (6) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach the PS. Do not use acetone, toluene and alcohol because they cause chemical damage to the PS.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with PS causes deformations and color fading.





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Product specification | This data sheet contains formal product specifications.

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

Remark

All the specifications listed in this document are guaranteed for module only. Post-assembled operation or component(s) may impact module performance or cause unexpected effect or damage and therefore listed specifications is not warranted after any post-assembly operation.



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10. Reliability test

	TEST	CONDITION	METHOD		
1	High-Temperature Operation	T = +50℃, RH = 30% fo r 240 hrs	IEC 60 068-2-2Bp		
2	Low-Temperature Operation	T = 0℃ for 240 hrs	IEC 60 068-2-2Ab		
3	High-Temperature Storage	T = +70℃, RH=23% for 240 hrs Test in white pattern	IEC 60 068-2-2Bp		
4	Low-Temperature Storage	T = -25℃ for 240 hrs Test in white pattern	IEC 60 068-2-1Ab		
5	High-Temperature, High-Humidity Operation				
6	High Temperature, High- Humidity Storage	T = +60°C, RH=80% for 240hrs Test in white pattern	IEC 60 068-2-3CA		
7	Temperature Cycle	-25℃ →+70℃, 100 Cycles 30min 30min Test in white pattern	IEC 60 068-2-14		
8	Solar radiation test	765 W/m² for 168hrs,40°ℂ Test in white pattern	IEC60 068-2-5Sa		
9	Package Vibration	1.04G, Frequency: 10~500Hz ge Vibration Direction: X,Y,Z Duration: 1 hours in each direction			
10	Package Drop Impact	Drop from height of 122 cm on concrete surface. Drop sequence: 1 corner, 3 edges, 6 faces One drop for each.	full packed for shipment		
11	Electrostatic Effect (non-operating)	(Machine model)+/- 250V 0Ω, 200pF	IEC 62179, IEC 62180		
12	Stylus Tapping	POLYACETAL Pen: Top R:0.8mm Load: 300gf Speed: 2 times/sec Total 13,500times,			

Actual EMC level to be measured on customer application

Note: The protective film must be removed before temperature test.

< Criteria >

In the standard conditions, there is not display function NG issue occurred. (including : line defect ,no image). All the cosmetic specification is judged before the reliability stress.





11.Bar Code definition

1 : EPD model code:.

ED060SCG: E45/E4E/E4F/E4C/E4D/E6F

2 : Internal control codes

DO NOT CARE

3 : FPL reversion code

V220:6; V220E:8

4 : FPL batch code:

01~99	001~099	G0~G9	160~169	Q0~Q9	230~239	X0~X9	300~309
A0~A9	100~109	H0~H9	170~179	R0~R9	240~249	Y0~Y9	310~319
B0~B9	110~119	J0~J9	180~189	S0~S9	250~259	Z0~Z9	320~329
C0~C9	120~129	K0~K9	190~199	T0~T9	260~269		
D0~D9	130~139	L0~L9	200~209	U0~U9	270~279		
E0~E9	140~149	M0~M9	210~219	V0~V9	280~289		
F0~F9	150~159	N0~N9	220~229	W0~W9	290~299		

5 : Year:

N: 2013 / P: 2014 / Q: 2015 / R: 2016 /... / Z: 2024

6 : Month:

1:Jan. 2:Feb. ... 9:Sep. A:Oct. B:Nov. C:Dec.

7 : Serial number

00000-99999

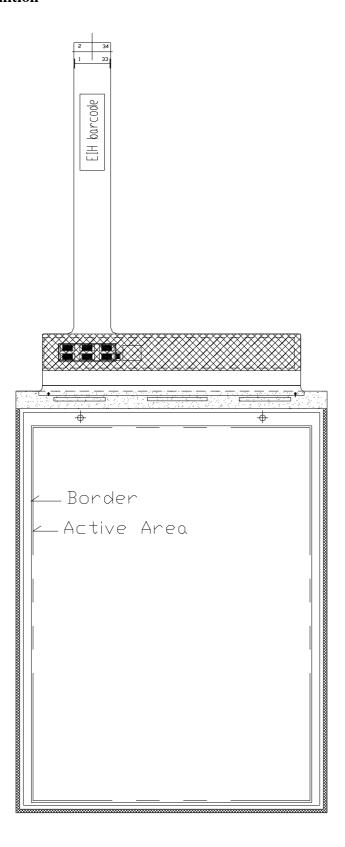
8 : MFG code:

TOC FAB3: T; TOC FAB2: Y; TOC FAB1: K; E Ink Hsinchu: P; MOS: S; Microveiw: G;

TYT FAB5: G; TYT FAB4: L

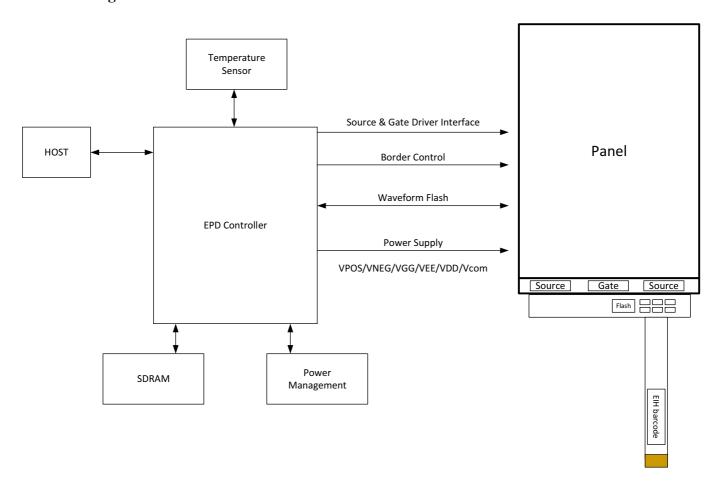


12. Border definition





13.Block Diagram





14.Packing

