

Document Title	HSD070I651-A**-0299 Final Specification	Page No.	1/23
Document No.	DC140-00	Revision	1.0

TO:

Date: Jan, 12 2006

HannStar Product Specification

(Final)

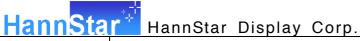
7" Color TFT-LCD Module

Model : **HSD070I651**

-A**-0299

Note:1.Please contact HannStar Display Corp. before designing your product based on this module specification.

- 2.The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.
- 3. The mark " ** " of Model means sub-model code.



Document Title	HSD070l651-A**-0299 Final Specification	Page No.	2/23
Document No.	DC140-00	Revision	1.0

Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	Oct. 13, 2005		HSD070I651-A**-0299 Final specification was first issued.
1.1	Dec. 19, 2005		Cancel P.18 outline dimension end seal size.
1.2	Jan. 12, 2006		1. Modify P.19 lot mark.
1.3	Aug. 02, 2006		1. Change P.18 outline dimension
			2. Change P.19 location of lot mark
1.4	Aug. 31, 2006		Increase P7 Color gamut & Color chromaticity Spec.



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	3/23
Document No.	DC140-00	Revision	1.0

Contents

1.0	General description	p.4
2.0	Absolute maximum ratings	p.6
3.0	Optical characteristics	p.7
4.0	Block diagram	p.10
5.0	Interface pin connection	p.11
6.0	Electrical characteristics	p.12
7.0	Reliability test items	p.17
8.0	Outline dimension	p.18
9.0	Lot mark	p.19
10.0	Package specification	p.10
11.0	General precaution	p.21

Document Title	HSD070l651-A**-0299 Final Specification	Page No.	4/23
Document No.	DC140-00	Revision	1.0

1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD070I651-A**-0299 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel and a driving circuit. This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with 1440 x 234 dot (480 horizontal by 234 vertical pixel) resolution.

1.2 Features

- 7 (16:9 diagonal) inch configuration
- Compatible with NTSC & PAL system
- Image Reversion: UP/DOWN and LEFT/RIGHT

1.3 Applications

- Portable TV
- Portable DVD
- Multimedia applications and Others AV system

1.4 General information

Item		Specification	Unit
Outline Dimension	on	162.6 x 96.6 x 1.69 (Typ.)	mm
Display area		154.08(H) x 86.58(V)	mm
Number of Pixel		480 RGB(H) x234(V)	pixels
Pixel pitch		0.321(H) x 0.370(V)	mm
Pixel arrangeme	nt	RGB Vertical stripe	
Display mode		Normally white	
Surface treatmer	nt	Antiglare, Hard-Coating(3H) with WV film	
Weight		55.4 (Typ.)	g
Power Consumption	Logic System	36.4(Typ.)	mW



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	5/23
Document No.	DC140-00	Revision	1.0

1.5 Mechanical Information

	Item	Min.	Тур.	Max.	Unit
Modulo	Horizontal(H)	162.3	162.6	162.9	mm
Module Size	Vertical(V)	96.3	96.6	96.9	mm
	Depth(D)	_	1.69	1.99	mm
Weight (Without inverter)		_	55.4	58.4	g



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	6/23
Document No.	DC140-00	Revision	1.0

2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

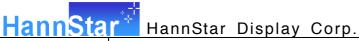
Item	Symbol	Min.	Max.	Unit	Note
	DV_DD	-0.3	6	V	GND=0
	V_{GH}	-0.3	40	V	GND=0
Power cumply voltage	V_{GL}	-20	0.3	V	GND=0
Power supply voltage	V_{GH} - V_{GL}	-0.3	40	V	
	AV_DD	-0.3	7.0	V	AGND=0
	V_{COM}	-1.6	5.2	V	
Analog Signal Input Level	$V_{R,} V_{G,} V_{B}$	-0.2	AV _{DD} +0.2	V	
Logic Signal Input Level	Vı	-0.3	DV _{DD} +0.3	V	

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) Ta =25±2°C

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T_{opa}	-10	60	$^{\circ}\mathbb{C}$	
Storage Temperature	T_{stg}	-20	70	$^{\circ}\!\mathbb{C}$	



Document Title	HSD070l651-A**-0299 Final Specification	Page No.	7/23
Document No.	DC140-00	Revision	1.0

3.0 OPTICAL CHARACTERISTICS

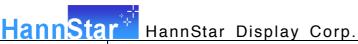
3.1 Optical specification

3.1 Optical specification								
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Transmittance (with POL)		T(%)	_	_	9.00	_	ı	
Contrast		CR	⊖=0 Normal viewing angle	400	500	_		(1)(2)
Response	Rising	T _R			5	7	maga	(1)(2)
time	Falling	T _F		_	20	28	msec	(1)(3)
Color gamut		S(%)			45 %			(C-light)
	White	W _x		0.292	0.312	0.332		(1)(4) CF glass (C-light)
		W _y		0.324	0.344	0.364		
	Red	Rx		0.583	0.603	0.623		
Color chromaticity		Ry		0.325	0.345	0.365		
(CIE1931)	Green	Gx		0.298	0.318	0.338		
		Gy		0.515	0.535	0.555		· • • ·
	Blue	Bx		0.119	0.139	0.159		
	Dide	Ву		0.141	0.161	0.181		
	Hor.	Θ_{L}		50	60	_		
Viewing angle	ΠUI.	Θ_{R}	00 40	50	60	_		
	Ver.	θυ	CR>10	30	40	_		
	Vei.	θр		50	60	_		

3.2 Measuring Condition

■ Measuring surrounding : dark room■ Ambient temperature : 25±2°C

■ 30min. warm-up time.

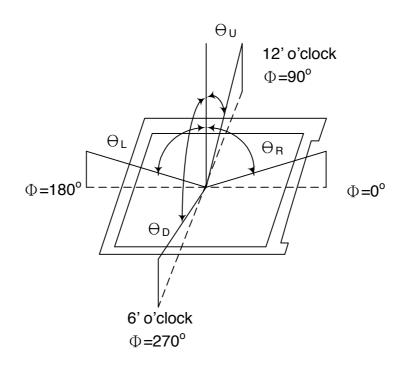


Document Title	HSD070I651-A**-0299 Final Specification	Page No.	8/23
Document No.	DC140-00	Revision	1.0

3.3 **Measuring Equipment**

- Otsuka Electrics Corp., which utilized LCD-7000 for Chromaticity and BM-5 for other optical characteristics.
- Measuring spot size : 10 ~ 12 mm

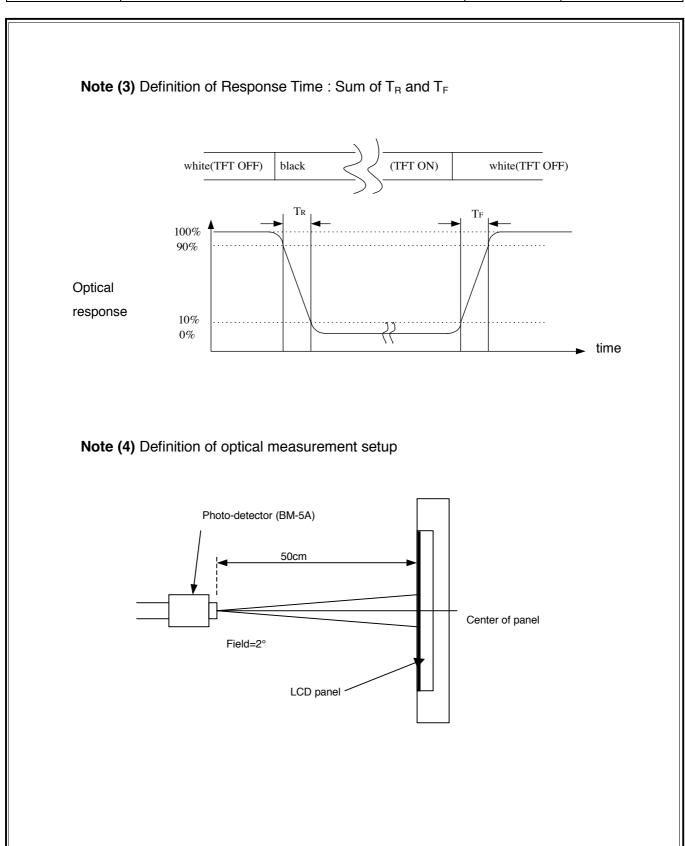
Note (1) Definition of Viewing Angle:



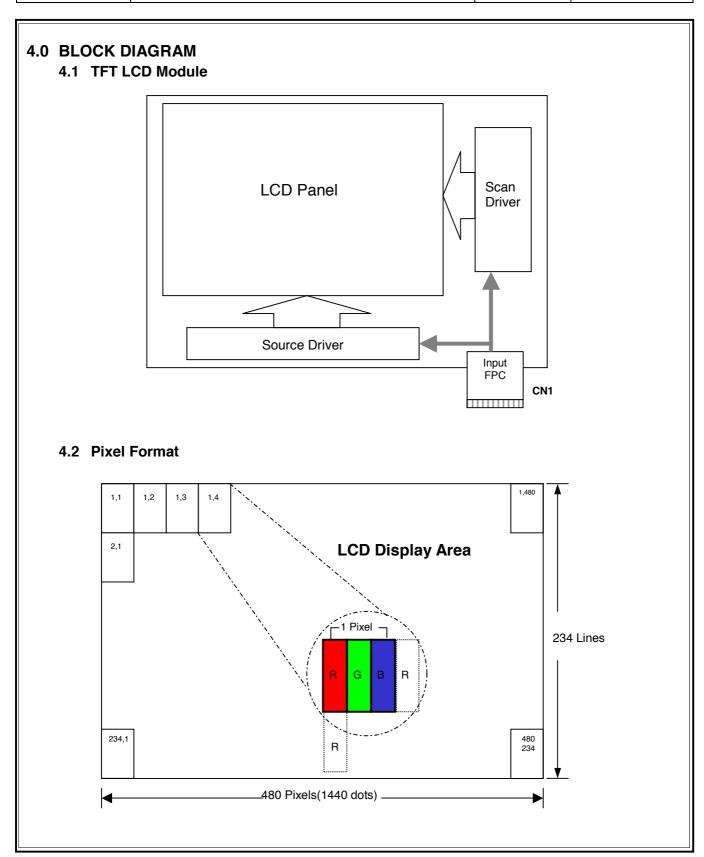
Note (2) Definition of Contrast Ratio(CR): measured at the center point of panel

Luminance with all pixels white CR = -Luminance with all pixels black

Document Title	HSD070I651-A**-0299 Final Specification	Page No.	9/23
Document No.	DC140-00	Revision	1.0



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	10/23
Document No.	DC140-00	Revision	1.0





Document Title	HSD070l651-A**-0299 Final Specification	Page No.	11/23
Document No.	DC140-00	Revision	1.0

5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD Module

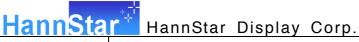
CN1 (Input signal): FPC Down Connector, 26 pins, pitch: 0.5mm

Terminal no.	Symbol	I/O	Function	Note
1	DGND	•	Ground for logic circuit	
2	DV_DD	I	Supply voltage of logic control circuit for scan (Gate) driver	
3	V _{GL}	ı	Negative power for scan (Gate) driver	
4	V _{GH}	ı	Positive power for scan (Gate) driver	
5	STVD	I/O	Vertical start pulse	(1)
6	STVU	I/O	Vertical start pulse	(1)
7	CKV		Shift clock input for scan (Gate) driver	
8	U/D		UP/DOWN scan control input	(1)
9	OEV	ı	Output enable input for scan(Gate) driver	
10	Vсом	ı	Common electrode driving signal	
11	Vсом	ı	Common electrode driving signal	
12	L/R	ı	LEFT/RIGHT scan control input	(1)
13	MOD		Sequential sampling and simultaneous sampling setting	(2)
14	OEH		Output enable input for data (Source) driver	
15	STHL	1/0	Start pulse for horizontal scan (Gate) line	(1)
16	STHR	I/O	Start pulse for horizontal scan (Gate) line	(1)
17	CPH3	I	Sampling and shifting clock pulse for data (Source) driver	(2)
18	CPH2	- 1	Sampling and shifting clock pulse for data (Source) driver	(2)
19	CPH1	- 1	Sampling and shifting clock pulse for data (Source) driver	
20	DV_DD	I	Supply voltage of logic control circuit for data(Source) driver	
21	DGND	-	Ground for logic circuit	
22	V_{R}	I	Alternated video signal input(Red)	
23	V G	ı	Alternated video signal input(Green)	
24	V B	ı	Alternated video signal input(blue)	
25	AV_DD	I	Supply voltage for analog circuit	
26	AGND	-	Ground for analog circuit	

Note (1) Selection of scanning mode (please refer to the following table)

	Note (1) Scientification of scarning mode (pieces refer to the fellowing table)									
Setting of scan control input		IN/OL	JT state	for start	pulse	Scanning direction				
	U/D	L/R	STVD STVU STHR STHL		STHL					
	GND	DV _{DD}	Output	Input	Output	Input	up to down, and from left to right.			
	DV _{DD}	GND	Input	Output	Input	Output	down to up, and from right to left.			
	GND	GND	Output	Input	Input	Output	up to down, and from right to left.			
	DV _{DD}	DV _{DD}	Input	Output	Output	Input	down to up, and from left to right.			

Note (2) MOD=H: Simultaneous sampling.(Please check CPH2 and CPH3 to GND when MOD=H) MOD=L: Sequential sampling.



Document Title	HSD070l651-A**-0299 Final Specification	Page No.	12/23
Document No.	DC140-00	Revision	1.0

6.0 ELECTRICAL CHARACTERISTICS

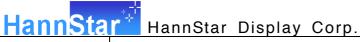
6.1 TFT LCD Module

Item	Symbol	Min.	Тур.	Max.	Unit	Note
	DV_DD	3	3.3	5.5	V	
Cupply Voltage	V GH	14.3	15	15.7	V	
Supply Voltage	V_{GL}	-10.5	-10	-9.5	V	
	AV _{DD}	4.5	5	5.5	V	
Video signal	ViA	0.4	-	AV _{DD} -0.4	V	
amplitude	Viac	-	3	-	V	AC component,
(VR,VG,VB)	ViDC	-	AV _{DD} /2	-	V	DC component
VCOM	VCAC	-	5.6	-	VP-P	AC component
VCOIVI	Vcdc	1.6	1.8	2.0	V	DC component, (1)
Input signal	ViH	0.8DV _{DD}	-	DV _{DD}	V	(2)
voltage	ViL	0	-	0.2 DV _{DD}	V	(2)
	ldd	-	127	-	uA	DV _{DD} =3.3V
Current of power	Iadd	-	7.0	-	mA	AV _{DD} =5V
supply	lgн	-	70	-	uA	V _{GH} =15V
	I GL	-	65	-	uA	V _{GL} =-10V

Note (1): The brightness of LCD panel could be changed by adjusting the AC component of VCOM.

Note (2): STHL, STHR, OEH, L/R, CPH1~CPH3, STVD, STVU, OEV, CKV, U/D Note (3): Be sure to apply the power voltage as the power sequence spec.

Note (4): DGND=AGND=0V,)



Document Title	HSD070l651-A**-0299 Final Specification	Page No.	13/23
Document No.	DC140-00	Revision	1.0

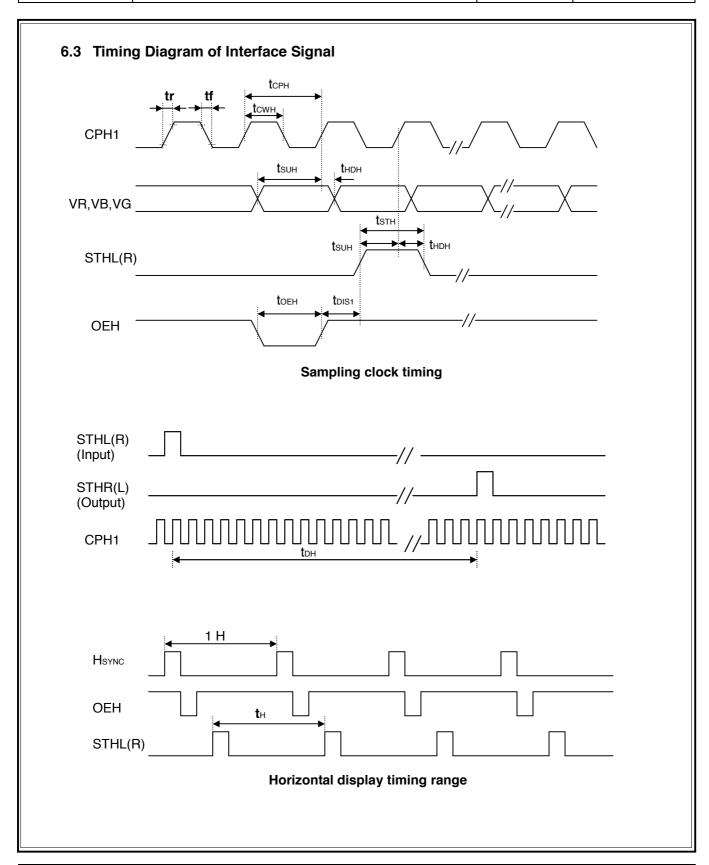
6.2 AC Characteristics

Item	Symbo I	Min.	Тур.	Max.	Unit	Note
Rising time	tr	-	•	10	ns	(1)
Falling time	t f	-	-	10	ns	(1)
High and low level pulse duty	t CPH	100	103	-	ns	CPH1~CPH3
CPH pulse duty	tсwн	40	50	60		CPH1~CPH3
STH setup time	t sun	20	-	-	ns	STHR,STHL
STH hold time	t HDH	10	-	-	ns	STHR,STHL
STH pulse width	t sтн	-	1	-	t CPH	STHR,STHL
STH period	tн	61.5	63.5	65.5	μs	STHR,STHL
OEH pulse width	t oeh	-	1.23	•	μs	OEH
Sample and hold disable time	t DIS1	-	8.19	-	μs	
OEV pulse width	toev	•	4.77	-	μs	OEV
CKV pulse width	t ckv	-	3.91	-	μs	CKV
Clean enable time	t DIS2	-	3.90	•	μs	
Horizontal display timing range	t DH	-	1440	-	t срн/ 3	
STV setup time	t suv	200	•	•	ns	STVD,STVU
STV hold time	t hdv	300	-	-	ns	STVD,STVU
STV pulse width	t stv	-	1	•	tн	STVD,STVU
Horizontal line per field	tv	256	262	268	tн	(2)
Vertical display start	tsv		3	-	tн	
Vertical display timing range	tov		234	-	tн	
VCOM Rising time	trсом		-	5	μs	
VCOM Falling time	t fCOM		-	5	μs	
VCOM delay time	tосом		-	3	μs	
RGB delay time	t DRGB		*	1	μs	

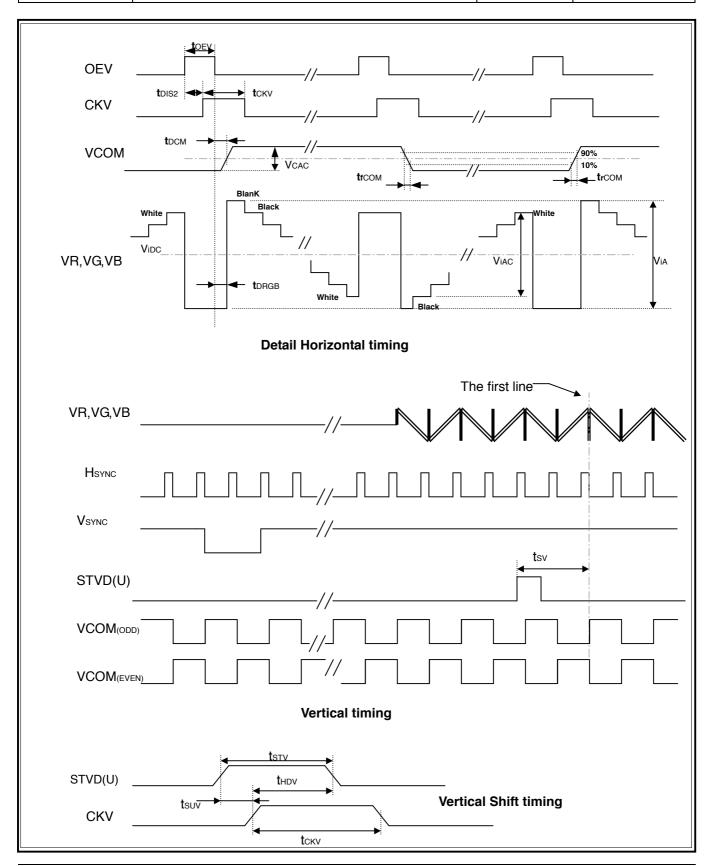
Note (1): For all of the logic signals.

Note (2): Please don't use odd horizontal lines to drive LCD panel for both odd and even filed simultaneously.

Document Title	HSD070I651-A**-0299 Final Specification	Page No.	14/23
Document No.	DC140-00	Revision	1.0



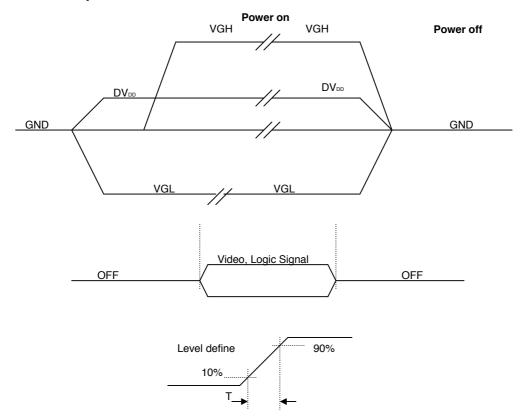
Document Title	HSD070I651-A**-0299 Final Specification	Page No.	15/23
Document No.	DC140-00	Revision	1.0





Document Title	HSD070I651-A**-0299 Final Specification	Page No.	16/23
Document No.	DC140-00	Revision	1.0





Power Sequence: DVDD -> VGL -> VGH

Note Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off. the display may momentarily become white.

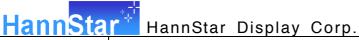


Document Title	HSD070I651-A**-0299 Final Specification	Page No.	17/23
Document No.	DC140-00	Revision	1.0

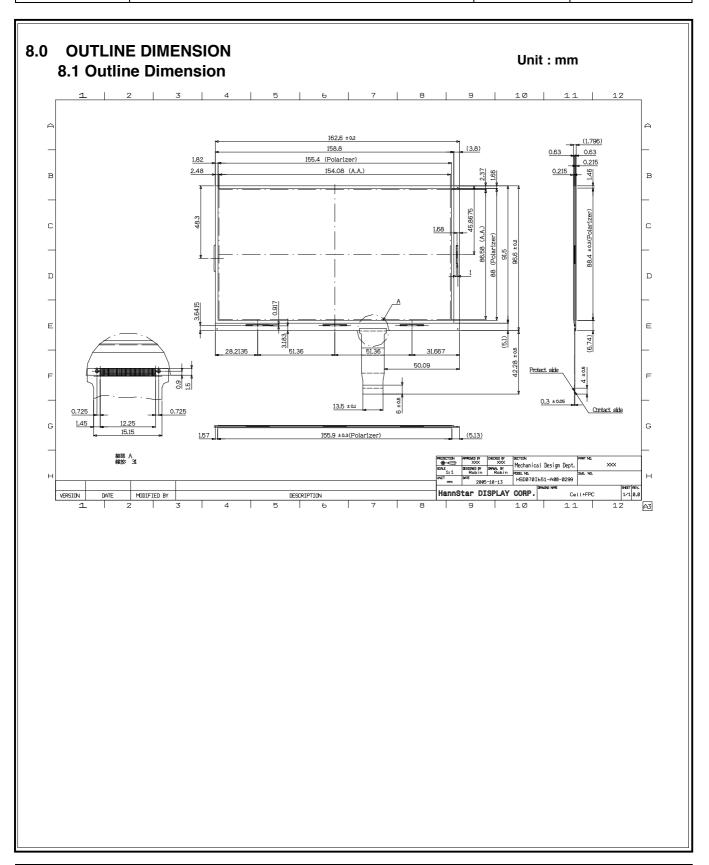
7.0 Reliability test items

No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+70°C, 240hrs	
2	Low Temperature Storage	Ta=-20°C, 240hrs	
3	High Temperature Operation	Ta=+60°C, 240hrs	
4	Low Temperature Operation	Ta=-10°C, 240hrs	
5	High Temperature and High Humidity (operation)	Ta=+60°C, 90%RH, 240hrs	
6	Thermal Cycling Test (non operation)	$-20^{\circ}\text{C}(30\text{min}) \rightarrow +70^{\circ}\text{C}(30\text{min}), 200\text{cycles}$	
7	Electrostatic Discharge	± 200 V,200pF(0 Ω) 1 time/each terminal	
8	Vibration	1.Random:	
		1.04Grms, 10~500Hz, X/Y/Z,	
		30min/each direction	
		2.Sweep sine:	
		1.5G, 5~500Hz, X/Y/Z,	
		30min/each direction	
9	Shock	100G,6ms, ±X, ±Y, ±Z	JIS C7021, A-10
		3 time for each direction	(Condition A)
10	Vibration (with carton)	Random:	
		1.04Grms, 10~500Hz, X/Y/Z 45min/each direction	
		Fixed:	
		5Hz, 1.5Grms, X/Y/Z	
		45min/each direction	
11	Drop (with carton)	Height: 60cm	JIS Z0202
	No. 7	1 corner, 3 edges, 6 surfaces	

Note: There are no display function NG issue occurred, All the cosmetic specification is judged before the reliability stress.



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	18/23
Document No.	DC140-00	Revision	1.0



Document Title	HSD070l651-A**-0299 Final Specification	Page No.	19/23
Document No.	DC140-00	Revision	1.0

8.0 LOT MARK 8.1 Lot Mark

1 2 3 4 5 6 7 8 9 10 11 12

code 1,2: HannStar internal flow control code.

code 3: production location.

code 4: production classification.

code 5: production year. code 6: production month.

Code 7,8,9,10,11,12: serial number.

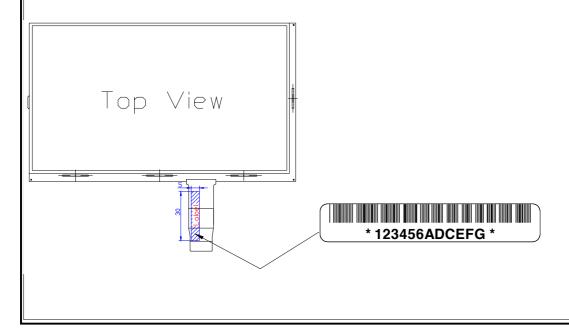
Note (1) Production Year

Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	Мау.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

9.2 Location of Lot Mark





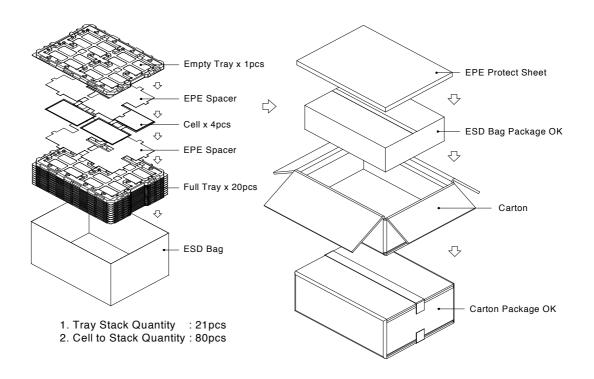
Document Title	HSD070l651-A**-0299 Final Specification	Page No.	20/23
Document No.	DC140-00	Revision	1.0

9.0 PACKAGE SPECIFICATION

packing form

- (1) Package quantity in one carton: 80 pieces.
- (2) Carton size: 464±3 mmx360±3 mmx370±3 mm.
- (3) For domestic transportation only.

9.2 packing assembly drawings





Document Title	HSD070l651-A**-0299 Final Specification	Page No.	21/23
Document No.	DC140-00	Revision	1.0

11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 ASSEMBLY PRECAUTION

- 11.2.1 Please use the mounting hole on the module side in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- 11.2.2 Please design display housing in accordance with the following guide lines.
 - 11.2.2.1 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause on-uniformity even if there is no non-uniformity statically.
 - 11.2.2.2 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- 11.2.3 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- 11.2.4 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- 11.2.5 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- 11.2.6 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color
- 11.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

11.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	22/23
Document No.	DC140-00	Revision	1.0

11.4 Breakage of LCD Panel

- 11.4.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.4.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and 11.4.3 rinse thoroughly with water.
- Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.5 Absolute Maximum Ratings and Power Protection Circuit

- Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3 It's recommended employing protection circuit for power supply.

11.6 Operation

- Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 11.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Static Electricity

- 11.7.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- Because LCD module uses CMOS-IC on TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- Persons who handle the module should be grounded through adequate 11.7.3 methods.

11.8 Disposal

When disposing LCD module, obey the local environmental regulations (temperature 23±5 $^{\circ}$ C; humidity 60±10%).



Document Title	HSD070I651-A**-0299 Final Specification	Page No.	23/23
Document No.	DC140-00	Revision	1.0

11.9 OTHERS

- 11.9.1 A strong incident light into LCD panel might cause display characteristics' changing inferior because of Polarizer film, color filter, and other materials becoming inferior.
 - Please do not expose LCD module direct sunlight Land strong UV rays.
- 11.9.2 Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- 11.9.3 For the packaging box, please pay attention to the followings:
 - 11.9.3.1 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
 - 11.9.3.2 Please do not pile them up more than 6 boxes. (They are not designed so.) And please do not turn over.
 - 11.9.3.3 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - 11.9.3.4 Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)