To : 深耕

Date : Nov. 23, 2009

Formal Product Information

Model : **HSD190MEN4 - A01**

Note: 1. The information contained herein is preliminary and may be changed without prior noticed.
2. Please contact HannStar Display Corp. before designing your product based on this module specification.
3. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	Nov. 23,2009	A01	HSD 190MEN4-A01 Formal Product Information was 1st issued.

Contents

1.0 GENERAL DESCRIPTIONS

1.1 Introduction

HannStar Display model **HSD190MEN4-A01** is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit. This TFT LCD has a 19-inch diagonally measured active display area with SXGA resolution (1024 vertical by 1280 horizontal pixel array) .

1.2 Features

- 19”SXGA TFT LCD Panel
- 2 CCFLs Backlight System
- Supported SXGA (V:1024 lines, H:1280 pixels) Resolution
- Supported to 75Hz Refresh Rate
- LCD Timing Controller
- RoHS Compliance
- VESA Compatible
- Halogen Free

1.3 Applications

- Desktop Monitors
- Display terminals for AV applications
- Monitors for industrial applications

1.4 General information

Item	Specification		Unit
Outline dimension	396 * 324 * 16.5(Typ)		mm
Display area	376.32 (H) x301.056 (V) (19.0” diagonal)		mm
Number of Pixel	1280(H) x 1024(V)		Pixels
Pixel pitch	0.294(H) x 0.294(V)		mm
Pixel arrangement	RGB Vertical Stripe		
Display color	16.7M (6-bits+Hi FRC)		
Color temperature	6500K		
Display mode	Normally white		
Surface treatment	Antiglare, Hard-Coating (3H)		
Weight	1950		g
Back-light	2-CCFLs, Top & bottom edge side		
Input signal	2-ch LVDS		
Power consumption	System	3.5(Typ.)	W
	B/L	9.9(Typ.)	

Remark(1): There are two functions, brightness and contrast tuning, to let luminance to 125cd/m2 in OSD. OSD shouldn't restrict the panel's G-T curve for brightness to be 125cd/m2. The higher contrast, the higher angular uniformity. That is to say, if OSD want to tune the panel's luminance to 125 cd/m2, the suitable way is to only tune the brightness function. And if tuning the brightness function to 125 cd/m2, it would be better only to tuning the inverter, not the gray level.

1.5 Mechanical Information

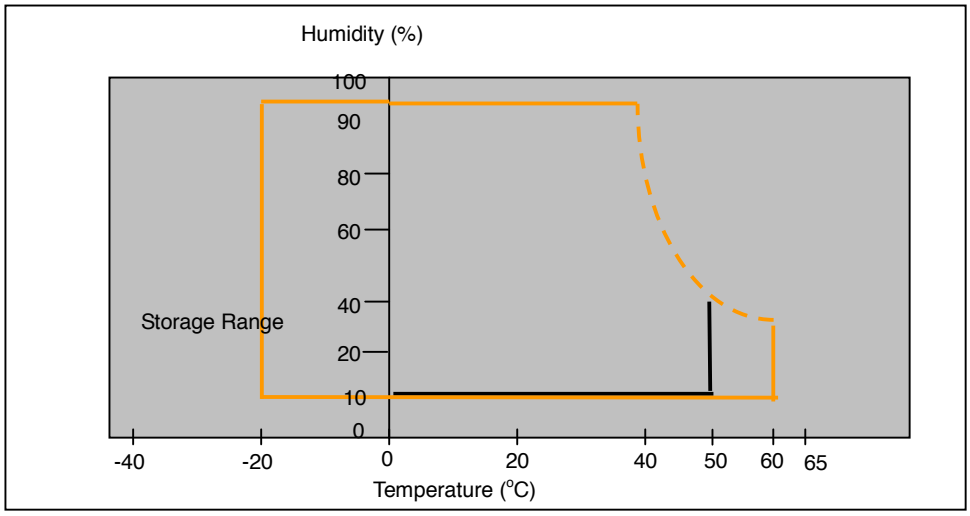
Item		Min.	Typ.	Max.	Unit
Module Size	Horizontal(H)	395.5	396.0	396.5	mm
	Vertical(V)	323.5	324.0	324.5	mm
	Depth(D)		16.5		mm
Weight (without inverter)		--	1950	--	g
Torque of customer screw hole		--	--	3.0	Kgf*Cm

2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T_{STG}	-20	60	°C	
Operating temperature	T_{OPR}	0	50	°C	(1)
Vibration (non-operating)	V_{NOP}	--	1.5	G	(2)
Shock (non-operating)	S_{NOP}	--	70	G	(3)
Storage humidity	H_{STG}	10	90	%RH	(3)
Operating humidity	H_{OP}	10	90	%RH	(4)
Low pressure (operating)	P_{LOP}	697	--	HPa	(5)
Low pressure (non-operating)	P_{LNOP}	116	--	HPa	(6)

Note (1)Storage /Operating temperature



(2) 5-500-5Hz sine wave, X, Y, Z each directions, 30 min/cycle.
(3) 11ms, ±X, ±Y, ±Z direction, one time each. For this shock test,
It is necessary to fill the silicon rubber between the shock jig as buffer.
(4) Max wet bulb temp. =39°C
(5) 2 hrs. (10000 feet)
(6) 24hrs. (50000 feet)

2.2 Electrical Absolute Rating:

2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	Unit.	Note
Power supply Voltage	VDD	-0.3	5.5	V(DC)	(1)(2)

2.2.2 Back Light Unit:

Item	Symbol	Min.	Max.	Unit	Note
Lamp current	I_L	3.0	8.0	mA	(1)(2)(3)
Lamp frequency	f_L	40	80	KHz	(1)(2)(3)

Note: (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under Normal Operating Conditions.

(2) To exceed 7.5mA, life time accelerate drop down and if to exceed 8.0 mA has safety problem. If current lower than 3.0 mA, CCFL would be unstable or damaged.

(3) Within Ta=25±2℃

3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast		CR	$\Theta=0^o$ $\phi=0^o$ Normal viewing angle	640	800	--		(1)(2)
Response time	Rising	TR +TF		--	4	8	msec	(1)(3)
	Falling							
White luminance (center of screen)		Y_L		200	250	--	cd/m^2	(1)(4) (IL=7.5mA)
Color chromaticity (CIE1931)	Red	Rx		-0.03	0.646	+0.03		(1)(4)
		Ry			0.334			
	Green	Gx			0.289			
		Gy			0.631			
	Blue	Bx			0.141			
		By			0.071			
	White	Wx			0.313			
		Wy			0.329			
Viewing angle	Hor.	Θ_L	CR>10	75	85	--		
		Θ_R		75	85	--		
	Ver.	Θ_H		65	75	--		
		Θ_L		75	85	--		
Viewing angle	Hor.	Θ_L	CR>5	75	85	--		
		Θ_R		75	85	--		
	Ver.	Θ_H		75	85	--		
		Θ_L		75	85	--		
Brightness uniformity		B_{UNI}	$\Theta=0^o$ $\phi=0^o$	75	--	--	%	(6)

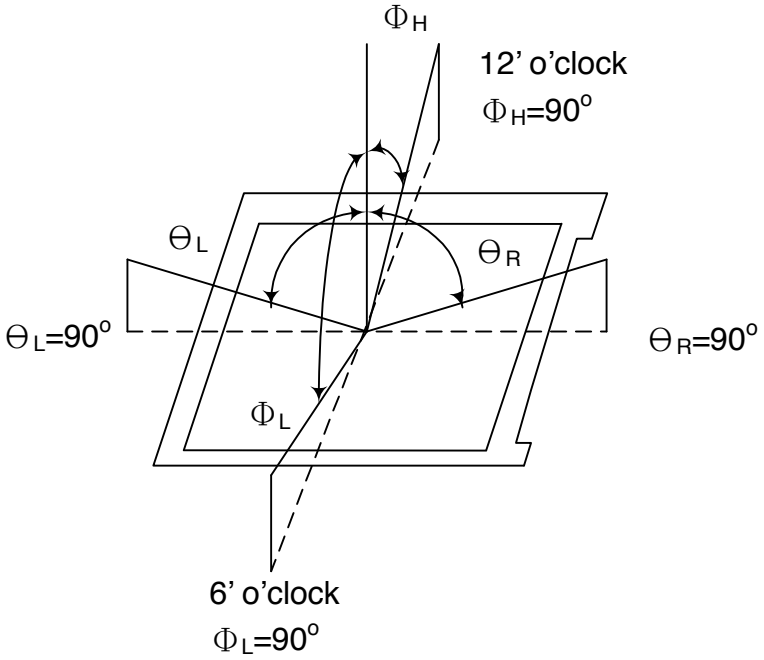
3.2 Measuring Condition

- Measuring surrounding: dark room
- Lamp current I_{BL}: (7.5) mA, Inverter: TBD
- V_{DD1}=5.0V, f_V=60Hz, f_{DCLK}=54MHz
- Surrounding temperature: 25±2°C
- 30min. Warm-up time.

3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 20~21mm

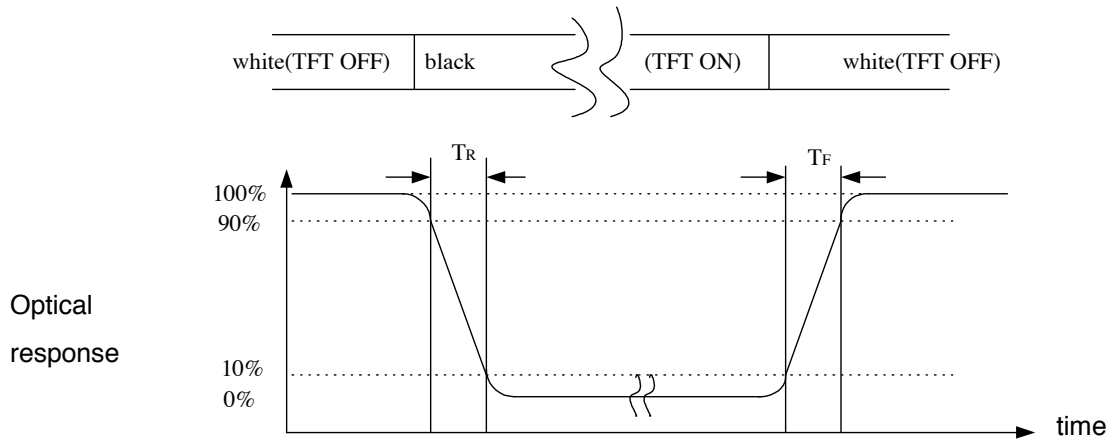
Note (1) Definition of Viewing Angle:



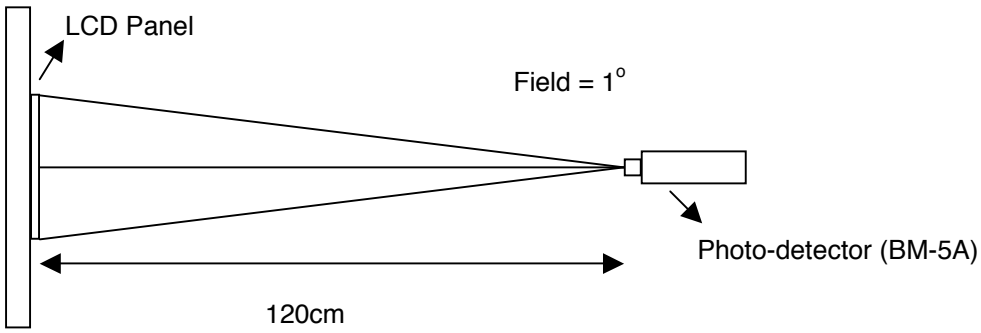
Note (2) Definition of Contrast Ratio(CR) :
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white (L255)}}{\text{Luminance with all pixels black (L0)}}$$

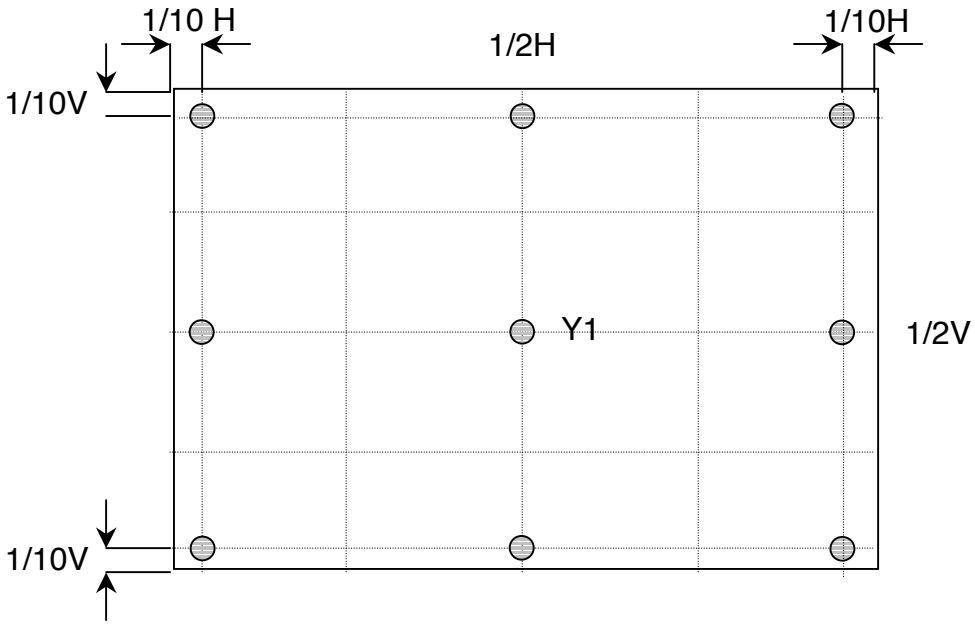
Note (3) Definition of Response Time: Sum of T_R and T_F



Note (4) Optical characteristic measurement setup



Note (5) Definition of Center Luminance of White (center)
Center Luminance= Y1

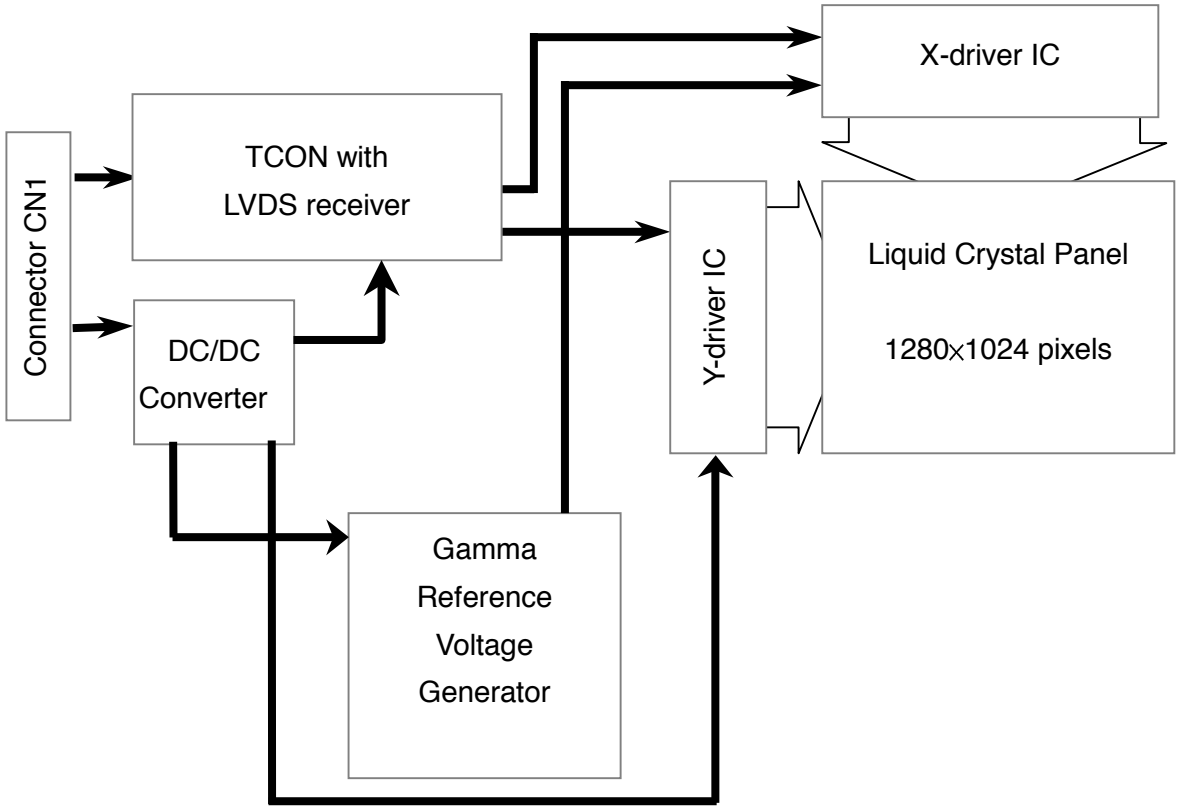


Note (6) Definition of brightness uniformity

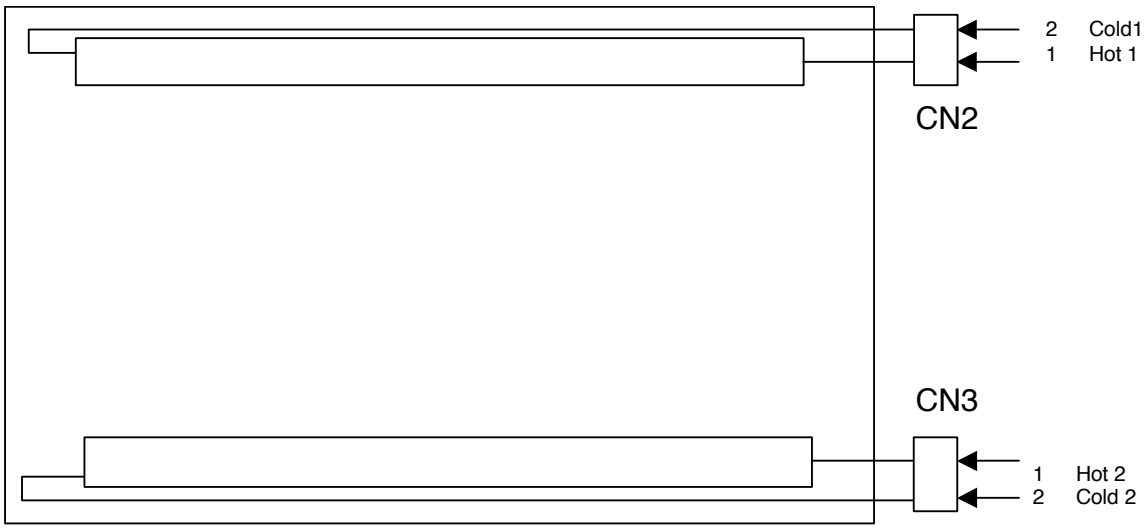
$$\text{Luminance uniformity} = \frac{\text{(Min Luminance of 9 points)}}{\text{(Max Luminance of 9 points)}} \times 100\%$$

4.0 BLOCK DIAGRAM

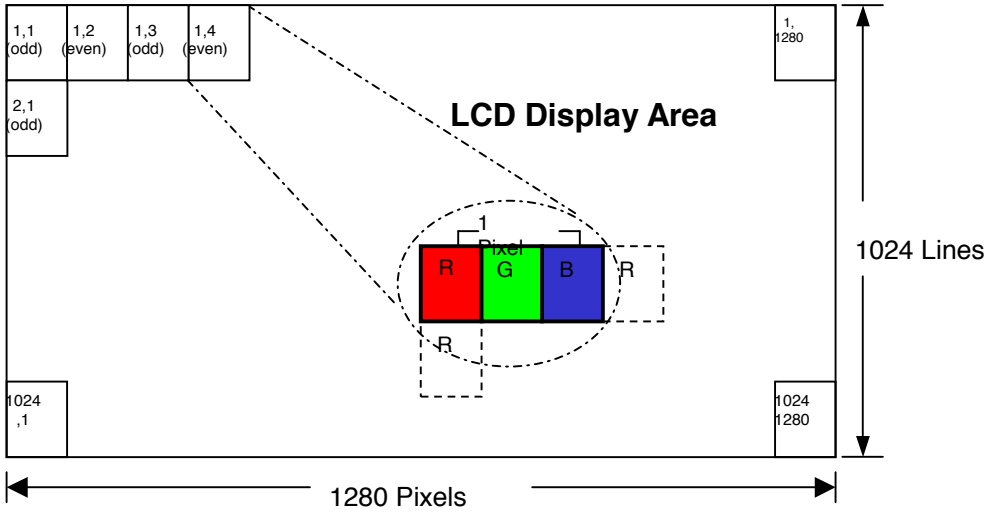
4.1 LCD Module Block Diagram:



4.2 Back Light Unit



4.3 Pixel Format



4.4 Relationship Between Displayed Color and Input

	Display	MSB R7	R6	R5	R4	R3	R2	R1	LSB R0	MSB G7	G6	G5	G4	G3	G2	G1	LSB G0	MSB B7	B6	B5	B4	B3	B2	B1	LSB B0	Gray scale Level
Basic color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	Light Blue	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
Gray scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
	↓	:								:								:								L3…L251
	Light	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252
		H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253
		H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
Gray scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L2
	↓	:								:								:								L3…L251
	Light	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L252
		L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L253
		L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L254
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	Green L255
Gray scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L2
	↓	:								:								:								L3…L251
	Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L252
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L253
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L254
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	Blue L255
Gray scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L2
	↓	:								:								:								L3…L251
	Light	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	L252
		H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	L253
		H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	L254
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L255

5.0 I/O CONNECTION PIN ASSIGNMENT

5.1 **Interface Connector** (30-pins, HRS MDF76URW-30S-1H or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Ground
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	VSS	Ground
26	NC	NC
27	VSS	Ground
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)

5.2 **Back Light Unit (CCFL) Connectors:**

CN2, 3: CCFL Power Source (Yeonho 35001HS-02 or equivalent)

Pin No.	Symbol	Color	Function
1	Hot1	Pink	CCFL power supply (High voltage)
2	Cold1	White	Ground

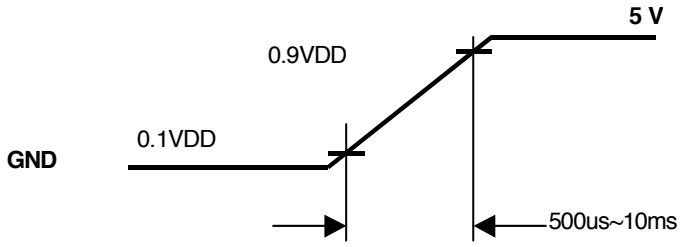
6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module:

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of power supply	V_{DD}	4.5	5.0	5.5	V	
Current of power supply	I_{DD0}	650	750	850	mA	(1)
Vsync frequency	f_V	50	60	76	Hz	(2)
Hsync frequency	f_H	53.3	64	80	KHz	
Frequency	f_{DCLK}	50	54	67.5	MHz	
Input rush current	I_{RUSH}	--	--	3.0	A	(3)

Note (1) V_{DD} =5.0V, Black pattern (L0)

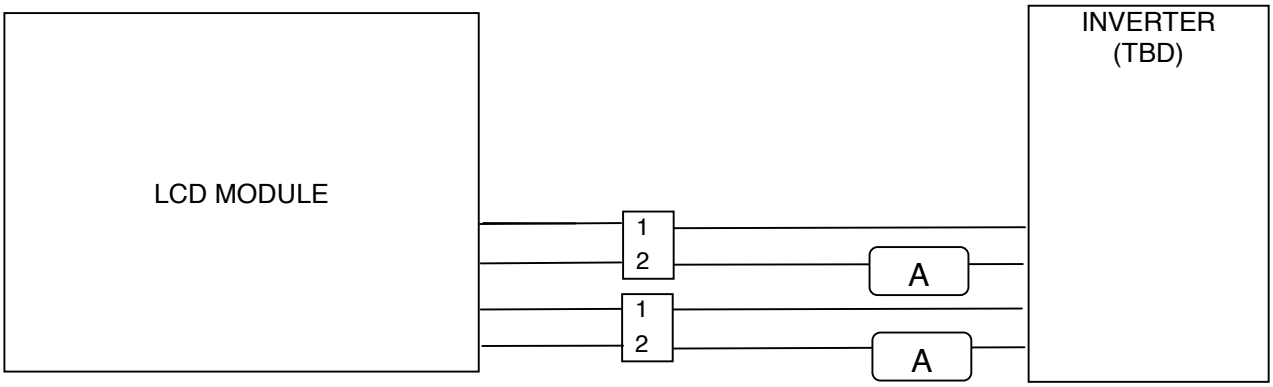
Note (2) When fv is too low, a flicker may be occurred on the display.
Note (3) Input Rush Current condition



6.2 Back-Light Unit

The back-light system is an edge-lighting type with 2 CCFL (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp current	IL	3.0	7.5	8.0	mA(rms)	(1)
Lamp voltage	VL	594	660	726	V(rms)	I_L=7.5mA
Frequency	fL	40	50	80	KHz	(2)
Operating Lifetime	Hr	40,000	--	--	Hour	7.5mA(3)
Startup voltage	Vs	1400	--	--	V(rms)	at 25°C
		1650				at 0°C



Note (1)

Lamp current is measured with current meter for high frequency as shown below. Specified values are for a single lamp. To exceed 7.5 mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

Note (2)

Lamp frequency may produce interference with horizontal synchronous frequency and this may cause ripple noise on the display. Therefore lamp frequency shall be kept away from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

Note (3)

Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition : Ta=25±3°C, Typical IL value indicated in the above table and fL=48 kHz until the brightness becomes less than 50%

Note (4)

CCFL inverter should be able to provide a voltage over specified value (Vs) in the above table. Lamp units need at least Vs value shown above to ignition.

Note (5)

The voltage over specified value (Vs) should be applied to the lamp more than 1 second after startup. Otherwise, the lamp may not be turned on. The used lamp current is the lamp typical current.

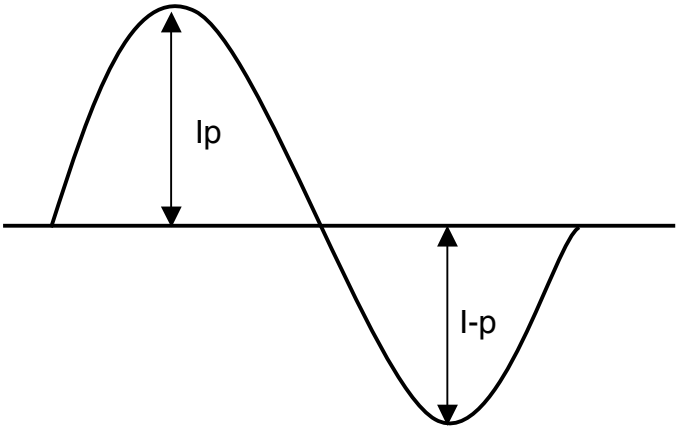
Note (6)

The output voltage waveform and current waveform of the inverter must be symmetrical (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and current waveform, and spike waveform. The inverter design which can provide the best optical performance, power efficiency, and lamp life should under the following conditions.

a. The asymmetry rate of the inverter waveform should be less than 10%.

b. The distortion tae of the waveform should be within $\sqrt{2}$±10%.

c. The inverter output waveform should be better similar to the ideal sine wave.

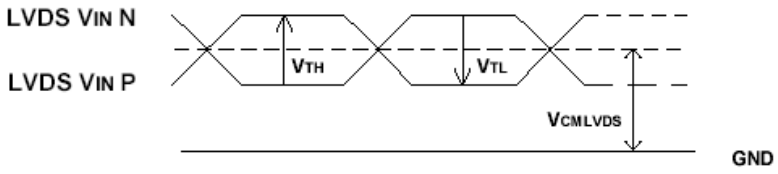


Asymmetry rate = $|I_p - I_{-p}| / I_{rms} \times 100\%$

Distortion rate = I_p (or I_{-p}) / I_{rms}

6.3 Switching Characteristics for LVDS Receiver

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Differential Input High Threshold	Vth	—	—	+100	mV	V_{CMLVDS}=1.25V
Differential Input Low Threshold	Vtl	-100	—	—	mV	
Input Current	I_{IN}	-10	—	+10	uA	V_{IN}=2.4V/0V, V_{DD}=3.6V
Differential input Voltage	$\|V_{ID}\|$	0.1	—	0.6	V	
Common Mode Voltage Offset	V_{CM}	1.15	—	1.35	V	
Clock Frequency	fc	50	54	67.5	MHz	

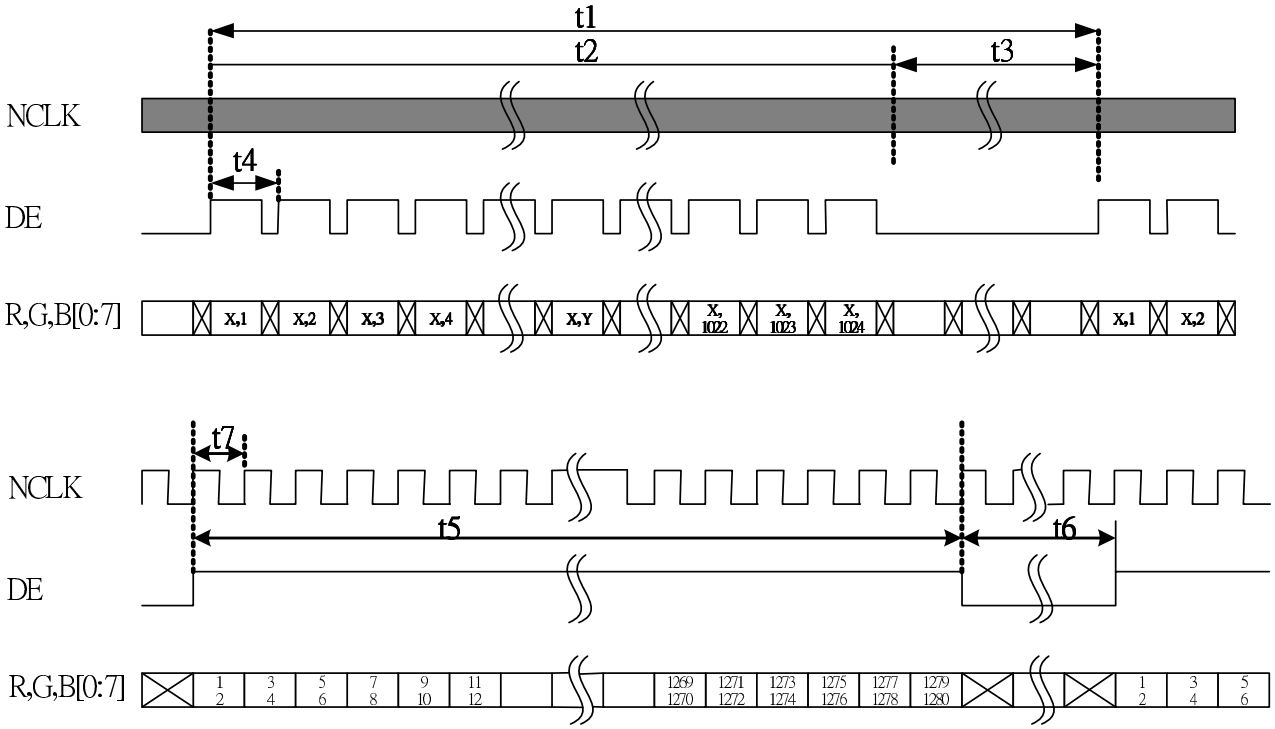


DC Timing Diagram 1: LVDS single end waveform

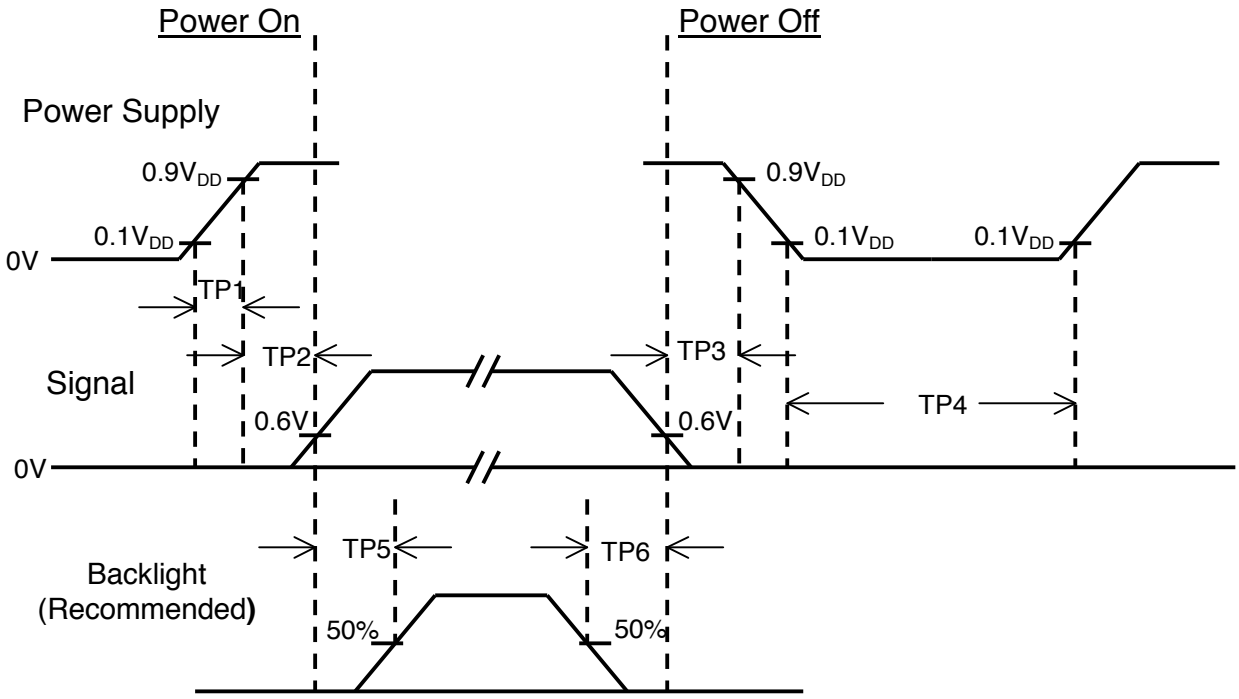
6.4 Interface Timing (DE mode)

Item	Symbol	Min.	Typ.	Max.	Unit
Frame Rate	--	50	60	76	Hz
Frame Period	t1	1029	1066	1150	line
Vertical Display Time	t2	1024	1024	1024	line
Vertical Blanking Time	t3	5	42	126	line
1 Line Scanning Time	t4	720	844	875	clock
Horizontal Display Time	t5	640	640	640	clock
Horizontal Blanking Time	t6	80	204	235	clock
Clock Rate	t7	50	54	67.5	MHz

Timing Diagram of Interface Signal (DE mode)



6.5 Power On / Off Sequence：



Item	Min.	Typ.	Max.	Unit	Remark
TP1	0.5	--	10	msec	
TP2	0	--	50	msec	
TP3	0	--	50	msec	
TP4	500	--	--	msec	
TP5	200	--	--	msec	
TP6	200	--	--	msec	

Note：(1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

(2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.

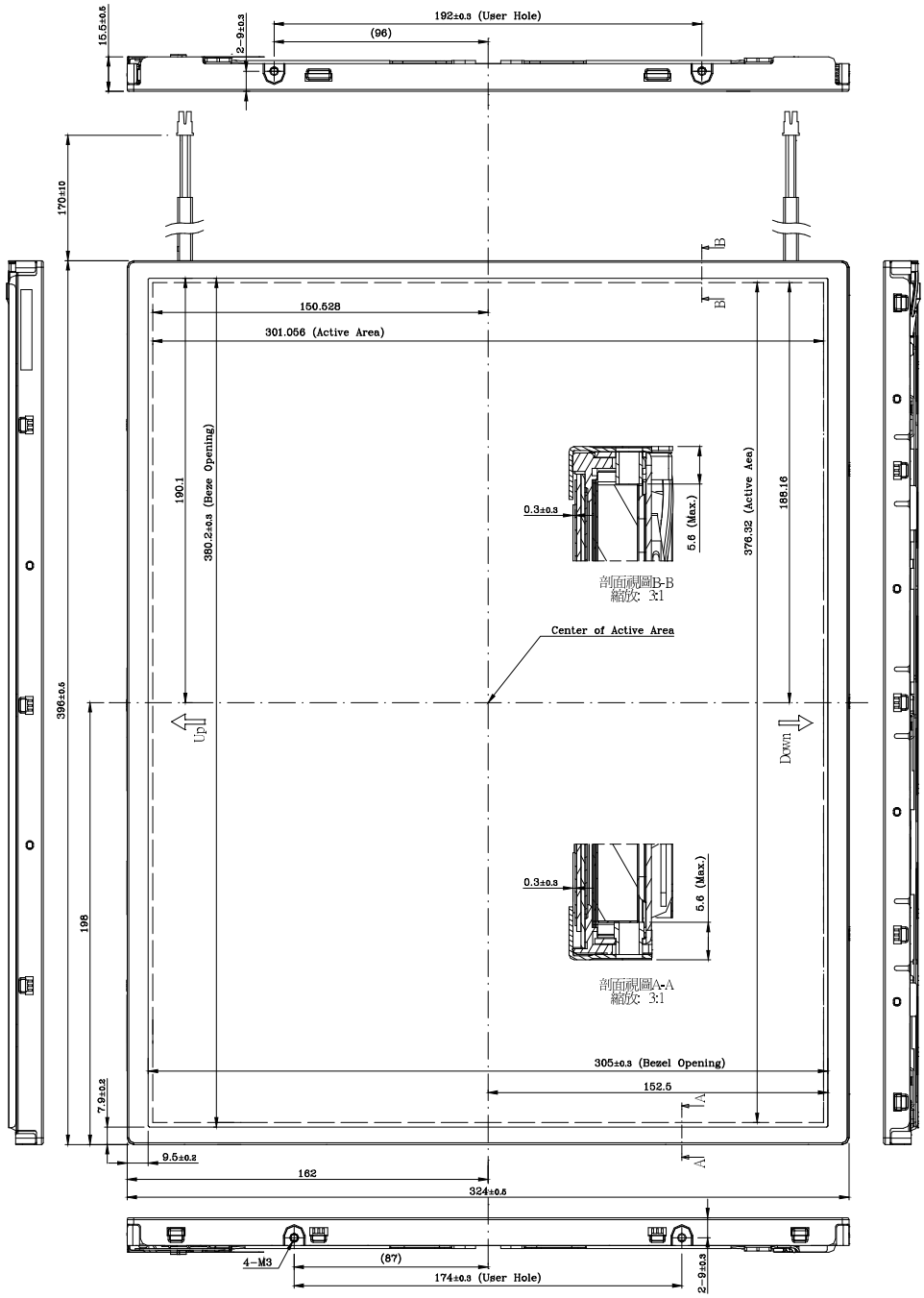
(3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.

(4) TP4 should be measured after the module has been fully discharged between power off and on period.

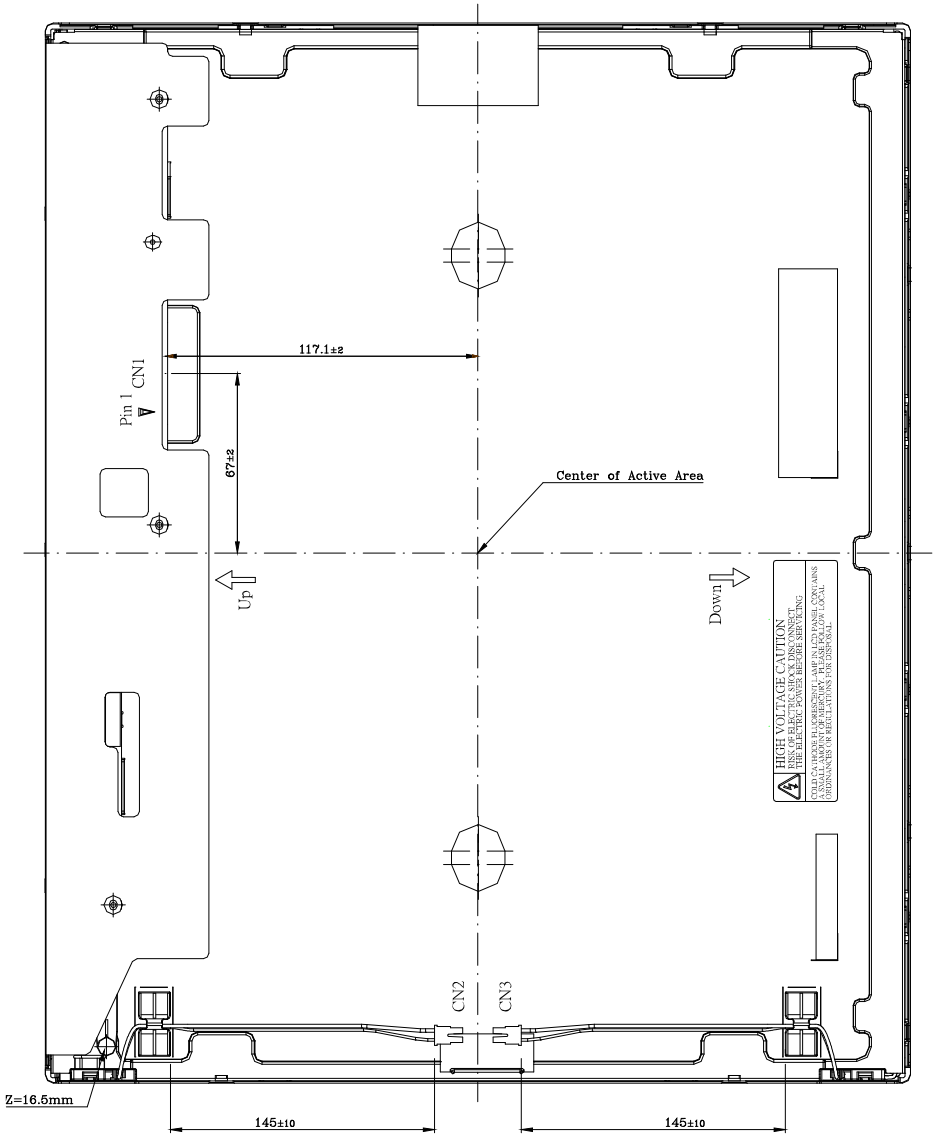
(5) Interface signal shall not be kept at high impedance when the power is on.

7.0 OUTLINE DIMENSION

7.1 Front View: Date: 2008.12.1



7.2 Back View: **Date: 2008.12.1**



1.Backlight: 4 CCFL
2. I/F Connector Specification (CN1):
FI-XB30SSRL-HF16 or Equivalent
3. Lamp Connector (CN2~CN5) / Wire Specification:
Yeon-Ho 35001HS-02L or Equivalent / 2Pin x L170mm
4. User Mounting Torque Spec: 3 Kgf-cm Max.
5. Unspecificed Tolerance is ±0.5mm.

8.0 LOT MARK

8.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

code 1,2,3,4,5,6: HannStar internal flow control code.
code 7: production location.
code 8: production year.
code 9: production month.
code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year. For example:

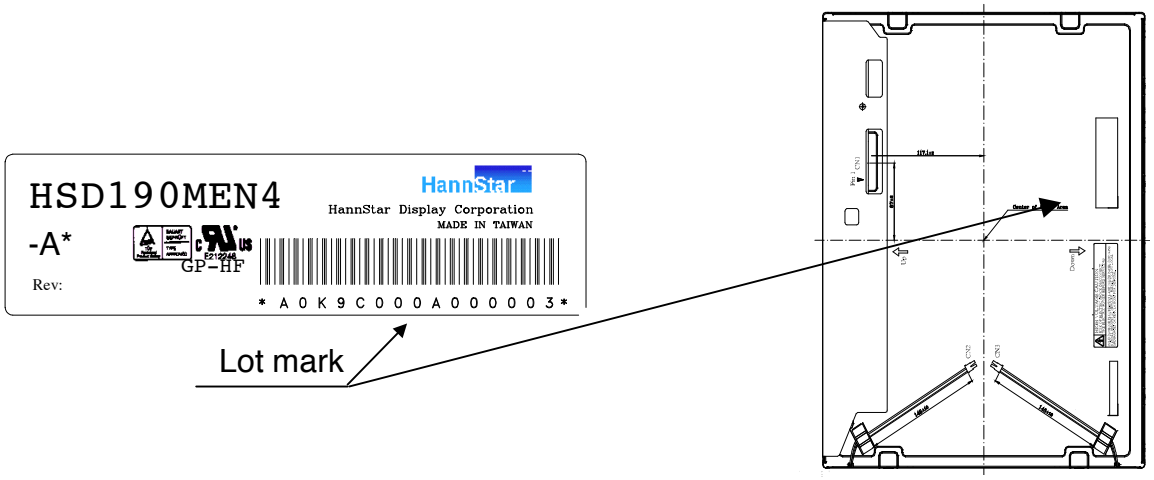
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

8.2 Location of Lot Mark

(1) The label is attached to the backside of the LCD module.
(2) This is subject to change without prior notice.



9.0 PACKAGE SPECIFICATION

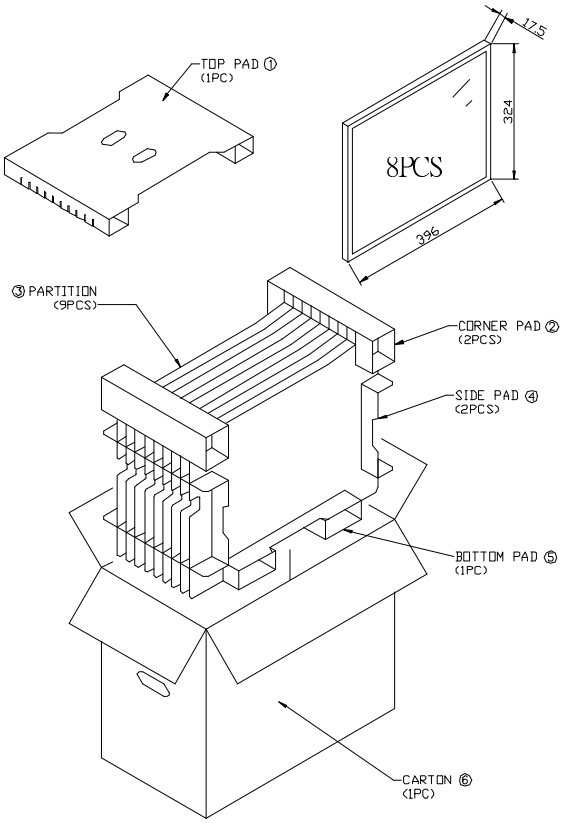
9.1 Packing form

(1) package quantity in one carton: 8 pieces.

(2) carton size: 544 mm×302 mm×446^{H} mm.

(3) for domestic transportation only.

9.2 Packing assembly drawings



10.0 GENERAL PRECAUTION

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

10.3 Breakage of LCD Panel

10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.

10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.4 Electric Shock

10.4.1 Disconnect power supply before handling LCD module.

10.4.2 Do not pull or fold the CCFL cable.

10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

10.5 Absolute Maximum Ratings and Power Protection Circuit

10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

10.5.3 It's recommended employing protection circuit for power supply.

10.6 Operation

10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.

Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

11.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

10.8 Static Electricity

10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.

10.8.3 Persons who handle the module should be grounded through adequate methods.

10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

10.10 Disposal

When disposing LCD module, obey the local environmental regulations.