

Hitachi LMG6912RPFC-3

Model	LMG6912RPFC-3
Manufacturer	Hitachi
Resolution	320x240
Number of Colors	Mono
Technology	STN-Mono
Interface	Digital

Filename	LC_LMG69.XXX
Bios code	EF
Bios/Table version	V1.0/V1.0
Used Controller	535/540/545/548/550/554
Date of adaption	3.01
Size	

Notes

LCD signal	LCD connector pin	Controller signal
CP	9	CLK
Frame	6	FLM
Load	8	LP
VEE*	12	EVEE*
VDD	5,10	EVDD
VSS	11	GND
VO	13	Externally
D0	1	P3
D1	2	P2
D2	3	P1
D3	4	P0

Voltage description	Voltage level	Maximum current	Note
VDD	+5V DC	30mA	
VEE	-22V DC	15mA	

The voltages for the lcd have to be switched in correct power-up and -down sequence. Use EVEE pin on the board to switch the external generated VEE. On some boards, this output is in TTL level only. An external fet or relais is needed for save switching of VEE. **Never connect VEE direct to the board or LCD!** Ask Digital-Logic LCD-Support for further informations.

For contrast adjustment connect a 10kOhm potentiometer between VEE and VDD. Connect the wiper to VO of LCD.