

HITACHI LMG5042XUFC

PANEL CHARACTERISTICS

Unit : LCD DD 8 BITS
 Display format : 640 X 480
 Colors : Mono

TEK932 BIOS & JUMPER INFORMATION

BIOS : VAE_AFA
 Jumper: W10: Short 1-2

BIOS not tested with the Panel at TEKNOR.

TECHNICAL INFORMATION

DISPLAY INTERCONNECTION (or use a dapter - see before)

TEK932		EXTERNAL	FLAT PANEL	
Signal Name	J23: PIN #		PIN #	Signal Name
ENAVDD	1			
Vcc (+5V)	2		15	VDD
ENVEE	3		5	DISP.OFF
STNDBYE*	4			
ENABLK	5			
M/DE	7		7	DF
ACTI	8			
LP/DE	10		8	LOAD
FLM	11		6	FRAME
SHFCLK	13		9	CP
P0	15		4	UD3
P1	16		3	UD2
P2	18		2	UD1
P3	19		1	UD0
P4	21		13	LD3
P5	22		12	LD2
P6	24		11	LD1
P7	25		10	LD0
P8	27			
P9	28			
P10	30			
P11	31			
P12	33			
P13	34			
P14	36			
P15	37			
P16	39			
P17	40			
P18	42			
P19	43			
P20	45			
P21	46			
P22	48			
P23	49			
		Vadj	18	VO
		VLCD	17	VEE
GND	6-9-12-14-17-20-23-26-29-32-35-38-41-44-47-50		14-16	VSS