

Kaohsiung Opto-Electronics Inc.

FOR MESSRS:	DATE: May 1 st ,	,2012

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX09D70VM1CEA

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ACCEPTED BY:	PROPOSED BY: Leullen

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RECORD OF REVISION

DATE	SHEET No.		SUMMARY					
May.13,'08	7B64PS 2712-	12.2 Location of lot						
Way. 13, 00	TX09D70VM1CEA-2		rint on FPC → Barcode lab	el				
	PAGE 12-1/1	Est mark sharigs. I fint off I To The Baroods labor						
Jan.18,'11	7B63PS 2709 –	9. OUTLINE DIMEN	NSIONS					
	TX09D70VM1CEA-3	The connectors or	r FPC changed.					
	Page 9 – 1/1							
	7B64PS 2712 –	Added						
	TX09D70VM1CEA-3 Page 12 – 1/1	12.2 REVISION (RI	EV.) CONTROL					
		REV No.	ITEM	NOTE				
		А	-	-				
		В	Connectors changed	PCN0804				
May 01,'12	All pages	Company name cha	anged:					
			ACHI ELECTRONICS CO.,I	_TD.				
			↓ ↓					
		KAOHSIUNG OPT	TO-ELECTRONICS INC.					

3.GENERAL DATA

The specifications are applied to the following TFT-LCD (Transmissive with micro reflectance) module with Back-light unit.

Note: Driving circuit for LED, timing controller and power unit is not built in this module.

(1) Part Name TX09D70VM1CEA 64.0(W)mm x 86.0(H)mm x 4.0(D)mm typ. (2) Module Dimensions (Except FPC Area) (3) Effective Display Area 53.64(W)mm x 71.52(H)mm (Diagonal:9cm) (4) Dot Pitch 0.0745mm x 3(R,G,B)(W) x 0.2235(H)mm 240 x 3(R,G,B)(W) x 320 (H) dots (5) Resolution (6) Color Pixel Arrangement R,G,B Vertical Stripe Transmissive Color TFT LCD (Normally White) (7) LCD Type (8) Display Type Active Matrix 262^K Colors (R,G,B 6 Bit Digital each) (9) Number of Colors Light Emitting Diode (LED) x 6 (10) Backlight (11) Weight 44g (12) Interface 50 pin C-MOS 6 O'clock (The direction it's hard to be discolored) (13) Viewing Direction

(14) Touch Panel

Resistance type. The surface is anti-glare.

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF LCD

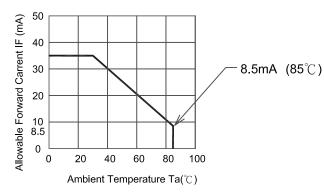
	ITEM		SYMBOL	MIN.	MAX.	UNIT	REMARKS
Power	Supply for Logic		DVDD	-0.3	4.6	V	
	Power Supply for LCD		AVDD	-0.3	11.0	V	Note1
Source	Innut Valtage	High	Vih	-0.3	AVDD+0.3	V	Note2
	Input Voltage	Low	VIL	-0.3	DVDD+0.3	V	
	Power Supply for Gate		Vgн	-0.3	V _{GL} +35	V	
Gate	l ower oupply for oate	Low	VgL	-10	+0.3	V	
	Input Voltage		Vig	-0.3	DVDD+0.3	V	Note3
	Forward Current		lF	-	35	mA	Note4
LED	Pulse Forward Current		IFP	-	100	mA	Note5
	Reverse Voltage		VR	-	5	V	
Stat	tic Electricity		-	-	±2	kV	Note6,7

Note 1: AVDD-0.2 \ge V0, V9 \ge GND+0.2

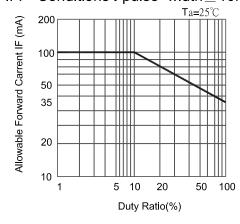
2: LOAD(CL1), CL2, R0~R5, G0~G5, B0~B5, M, POL, STH

3: CL3, DISP, STV





5:IFP Conditions: pulse width \leq 10ms and Duty \leq 1/10



6: Make certains you are grounded when handling LCM.

7 : Testing condition : 200pF - 0Ω , 25° C - 70%RH.

4.2 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	CONDITION	REMARKS
Supply Voltage	7.0	V	DC	
Endurance Voltage	25	V	DC	Note1

Note 1: Waiting 1 minute.

4.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARKS	
I I □IVI	Min.	Min. Max. Min. Max.		KEWAKKS		
Ambient Temperature	-20 ℃	70 ℃	-30 ℃	80 ℃	Note2,3,6,7,9,10	
Humidity	(Not	te 1)	(Note 1)		Without condensation	
Vibration	-	2.45m/s ² (0.25G)	-	11.76m/s ² (1.2G)	Note4,5	
Shock	-	29.4m/s ² (3G)	-	490m/s ² (50G)	Note5,8	
Corrosive Gas	Not Acc	ceptable	Not Acceptable			

Note 1 : Ta ≤ 40°C : 85%RH max.

Ta>40°C: Absolute humidity must be lower than the humidity of 85%RH at 40°C.

Note 2 : For storage condition Ta at -30° C < 48h, at 80° C < 100h.

For operating condition Ta at -20° C < 100h

Note 3: Background color changes slightly depending on ambient temperature.

This phenomenon is reversible.

Note 4: 5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7: Only operation is guarantied at operating temperature. Contrast, response time,

another display quality are evaluated at +25°C.

Note 8: Pulse Width: 10ms

Note 9: This is panel surface temperature, not ambient temperature.

Note 10: If LED is drove by high current, the life time of LED will be reduced, also high

temperature and high humidity.

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C

ITEM	ITEM		CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply for Logic		DVDD	-	2.3	3.3	3.6	V
Source		AVDD	-	7.5	8.48	9.5	
Power Supply for LCD	Coto	VGH	-	13.0	15.0	17.0	V
	Gate	VGL	-	-8.0	-5.0	-2.0	
Power Supply Current		IDD		-	0.56	2.0	
		IAVDD	16 Vertical	-	2.3	4.0	m Λ
		IGH	Gray scale	-	0.033	0.06	mA
		IGL		-	0.028	0.06	
Frame Frequency (No	fFLM		52	60	68	Hz	

Note 1: Need to make sure of flickering and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF BACK LIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
LED Input Voltage	VF	IF=20mA	ı	3.2	3.5	٧	LED / Part
LED Forward Current	IF	-	-	20	25	mA	LED / Part
LED Reverse Current	IR	VR=5V	-	-	50	μ A	LED / Part

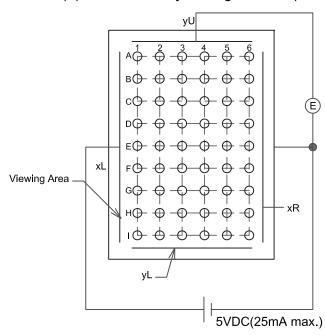
5.3 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	
Decistance between Townships	xR - xL	200 - 650	ohm
Resistance between Terminal	yU - yL	250 - 500	ohm
Insulance Resistance (Note 1)	x - y	10M min.	ohm
Lincovity (Ninto 2.2)	х	1.5 max.	%
Linearity (Note 2,3)	У	1.5 max.	%
Chattering		10 max.	ms

Note 1: Operating Voltage 25V DC.

Note 2: Test Condition.

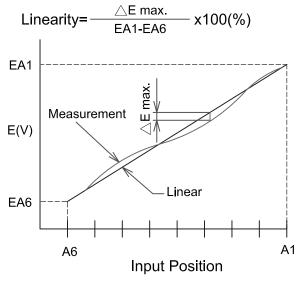
(a) Y axis linearity testing method (with tip radius 0.8, polaycetal pen). VxL-xR=5V, VOUT=VyU.



(b) X axis linearity method VyU-yL=5V , VOUT=VxL.

Note 3 : Calculation

(a) Y axis linearity



5.4 MECHANICAL CHARACTERISTICS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	REMARKS
Pen Input Pressure	0.1 - 1.3	N	R0.8mm Polyacetal pen
Surface Hardness	3H min.	-	JIS K 5400

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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD (BACK LIGHT ON)

Ta=25°C

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
Brightness		В	$\phi = 0^{\circ} \theta = 0^{\circ}$	270	320	-	cd/m ²	Note1
Uniformity		-	$\phi = 0^{\circ} \theta = 0^{\circ}$	70	-	-	%	Note2,3,4
		θ x	ϕ =0 $^{\circ}$,K \geq 5.0	-	70	-		
Viouring Angle		$\theta \mathbf{x}'$	ϕ =180°, K \geq 5.0	-	70	-	doa	NotoF 6
Viewing Angle		θ y	ϕ =90 $^{\circ}$,K \geq 5.0	-	80	-	deg	Note5,6
		θ y	ϕ =270 $^{\circ}$,K \geq 5.0	-	60	-		
Contrast Ratio		K	$\phi = 0^{\circ} \theta = 0^{\circ}$	180	300	-	-	Note4
Response Time (r	ise-fall)	tr+tf	$\phi = 0^{\circ} \theta = 0^{\circ}$	-	(30)	-	ms	Note8
Color Tone	Dod	х		0.55	0.60	0.65	-	
(Primary Color)	Red	у		0.29	0.34	0.39	-	
	Croon	х		0.28	0.33	0.38	-	
	Green	у	4 0° 0 0°	0.54	0.59	0.64	-	Note 4
	Dlue	х	$\phi = 0^{\circ} \theta = 0^{\circ}$	0.09	0.14	0.19	-	Note4
	Blue	у		0.07	0.12	0.17	-	
	\\/hita	х		0.27	0.32	0.37	-	
	White	у		0.29	0.34	0.39	-	

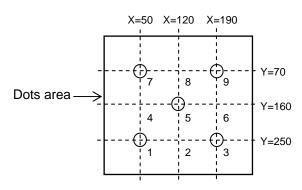
(Measurement condition: KOE standard)

Note $(4)\sim(7)$: See page 6-2/2

Note 1: Active area center

Note 2: Driving Condition Display Pattern : White Raster LED Current: 20mA / Part Measurement of the following

5 places on the display.



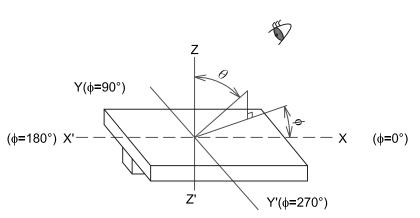
Note 3: Definition of the brightness uniformity

Min. brightness x 100% Max. brightness

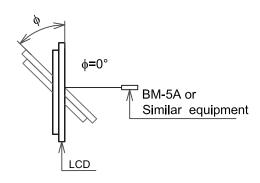
Note 4: Measurement Condition

BM-5A (Measurement field 1°) LCD

Note 5 : Definition of θ and ϕ (Normal) Viewing direction



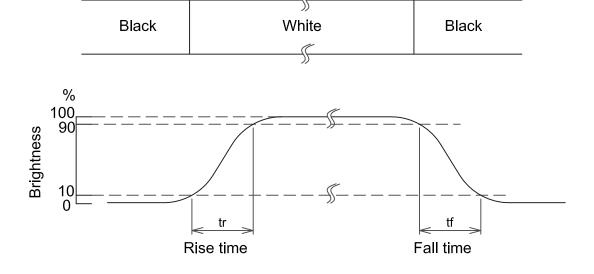
Note 6: Definition of Viewing angle



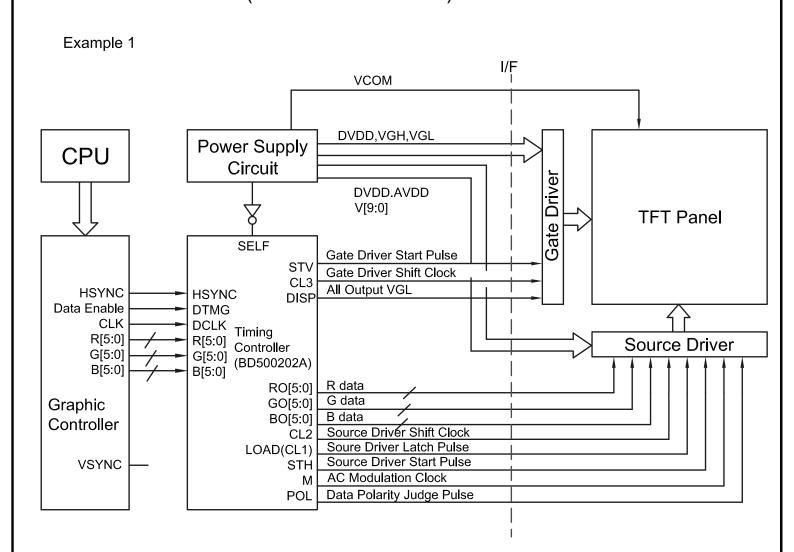
Note 7: Definition of contrast "K"

 $K = \frac{\text{White Brightness}}{\text{Black Brightness}}$

Note 8: Definition optical response time



7. BLOCK DIAGRAM (REFERENCE ONLY)



7B64PS 2707-TX09D70VM1CEA-4

Example 2 I/F **VCOM** DVDD,VGH,VGL **Power Control** Power Circuit Supply Driver DVDD,AVDD V[9:0] **TFT Panel** Gate I **ENVDD ENVEE** STV : Gate Driver Start Pulse **FGSP** CL3: Gate Driver Shift Clock PWM1 **Graphic Controller CPU** Source Driver With **Timing Controller** FD[23:18] R data FD=15:10] G data FD[7:2] B data FSCLK CL2: Source Driver Shift Clock FLCLK LOAD(CL1): Source Driver Latch Pulse FSSP STH: Source Driver Start Pulse FMODE M: AC Modulation Clock POL: Data Polarity Judge Pulse

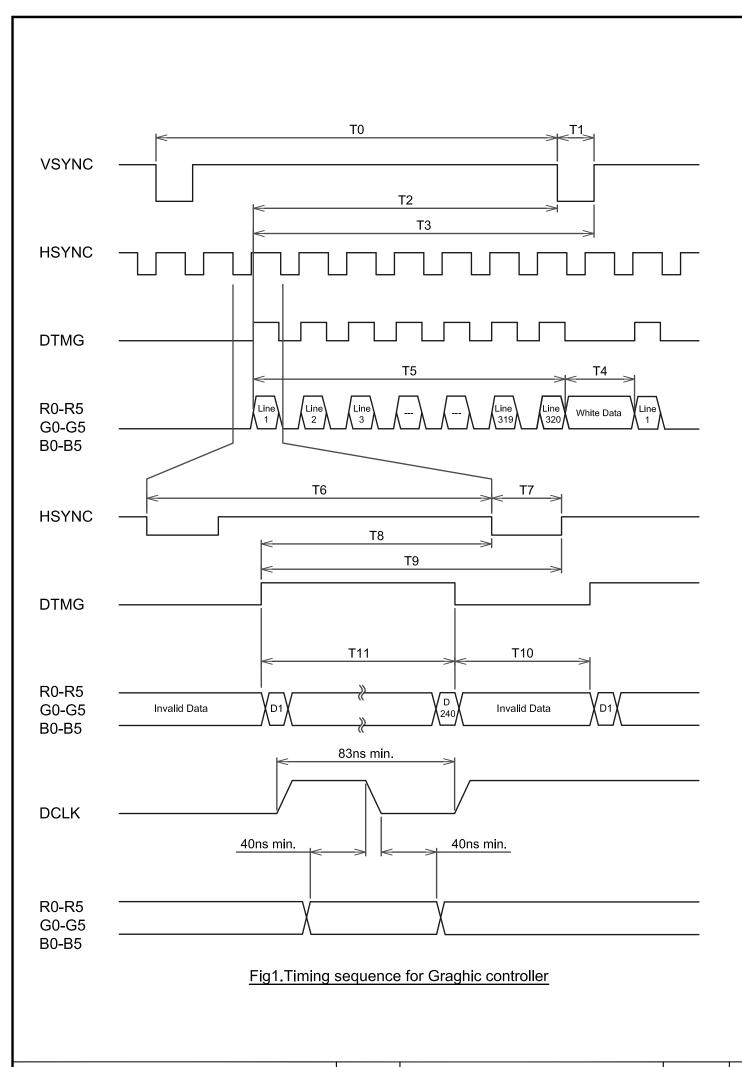
7B64PS 2707-TX09D70VM1CEA-4

8. INTERFACE TIMING

8.1 INTERFACE TIMING

	MIN.	TYP.	MAX.	UNIT	SYMBOL
Vertical Total	-	327	-	Line	T0
Vertical Sync Width	1	1	-	Line	T1
Vertical Sync Start	-	322	-	Line	T2
Vertical Sync End	-	323	-	Line	T3
Vertical Blank Time	5	7	-	Line	T4
Vertical Display End	-	320	-	Line	T5
Horizontal Total	265	273	509	Pixel Clock	T6
Horizontal Sync Width	4	5	10	Pixel Clock	T7
Horizontal Sync Start	244	251	307	Pixel Clock	T8
Horizontal Sync End	248	256	317	Pixel Clock	T9
Horizontal Blank Time	25	33	269	Pixel Clock	T10
Horizontal Display End	-	240	-	Pixel Clock	T11

Note: Vertical Total should be set to odd.



	Item	Symbol	Min.	Тур.	Max.	Unit.	Remark
	Clock cycle time	Trate	100	-	-	ns	
	Clock low level width	Tcwl	35	-	-	ns	
	Clock high level width	Tcwh	35	-	-	ns	
	Data set up time	Tds	25	-	-	ns	
l ler	Data hold time	Tdh	25	-	-	ns	
Source Driver	Start pulse set up time	Tss	25	-	-	ns	
Ice	Start pulse hold time	Tsh	25	-	-	ns	
Sou	CL1 high level width	Tcl1wh	10	-	-	us	
	CL1 start pulse	Tscl1	100	-	-	ns	
	STH start pulse	Tssth	100	-	-	ns	
	M set up time	Tms	50	-	-	ns	
	M hold time	Tmh	50	-	-	ns	
	CL3 cycle time	Tcl3	2	3	-	us	
iver	CL3 high level width	Tcl3wh	2	-	-	us	
Gate Driver	CL3 low level width	Tcl3wl	2	-	-	us	
 Gatı	STV set up time	Tstvs	250	-	-	ns	
	STV hold time	Tstvh	250	-	-	ns	

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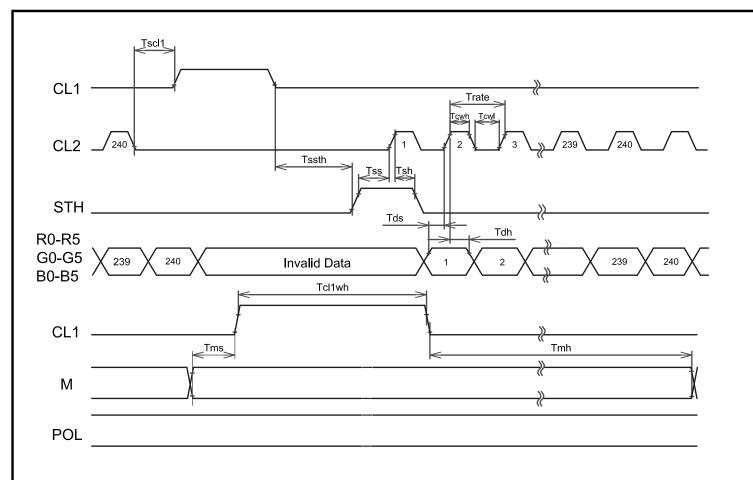


Fig2 . Horizontal Timing Sequence for non Tcon

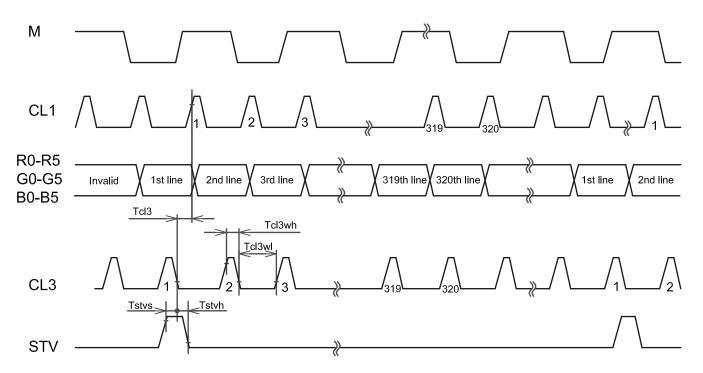
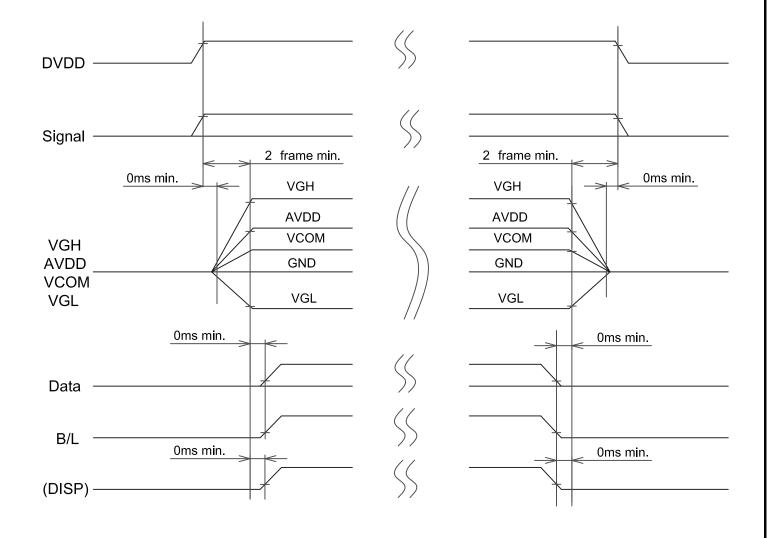


Fig3 . Vertical Timing Sequence for non Tcon

Note: 0.8xDVDD 0.2xDVDD

Note: $0.8 \times DVDD$

8.3 POWER ON/OFF SEQUENCE



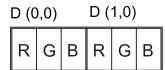
7B64PS 2708-TX09D70VM1CEA-4

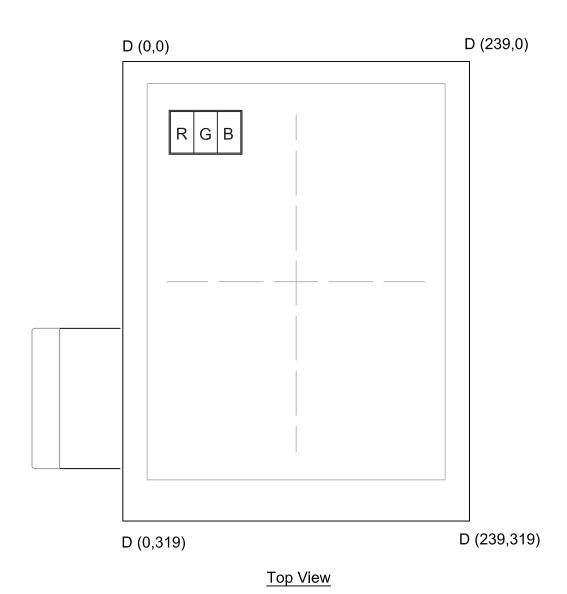
8.3 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA 8.3.1 Display Colors

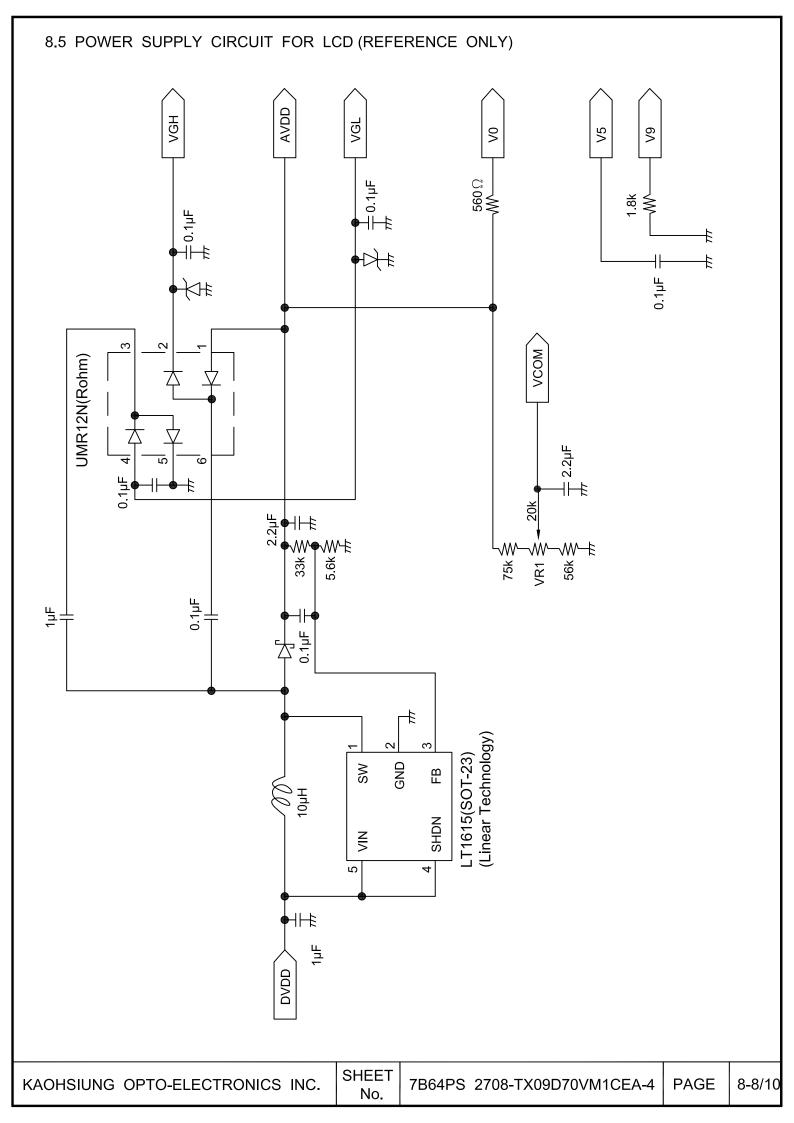
			F	Red	Data	a			G	reen	Da	ıta			Е	Blue	Dat	а	
	Input	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G	B5	B4	В3	B2	B1	B0
color		MSI	3			L	SB	MS	В			L	.SB	MS	В			L	SB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	• •	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Neu	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(2)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Orceri	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(2)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Dide	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

SHEET
NO.

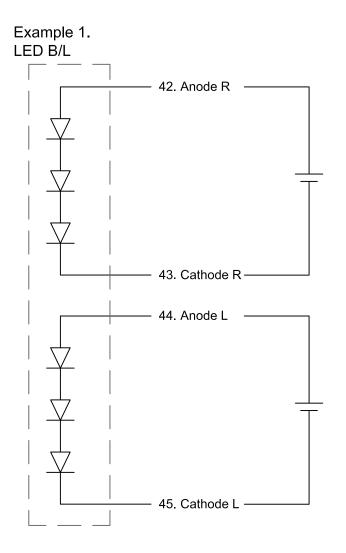
8.4 Data address

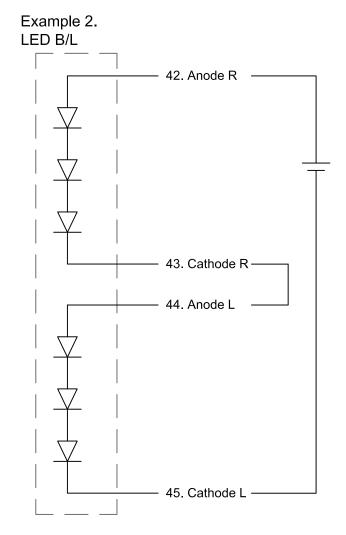






8.5 POWER SUPPLY CIRCUIT FOR LED BL (REFERENCE ONLY)





8.6 INTERNAL PIN CONNECTION

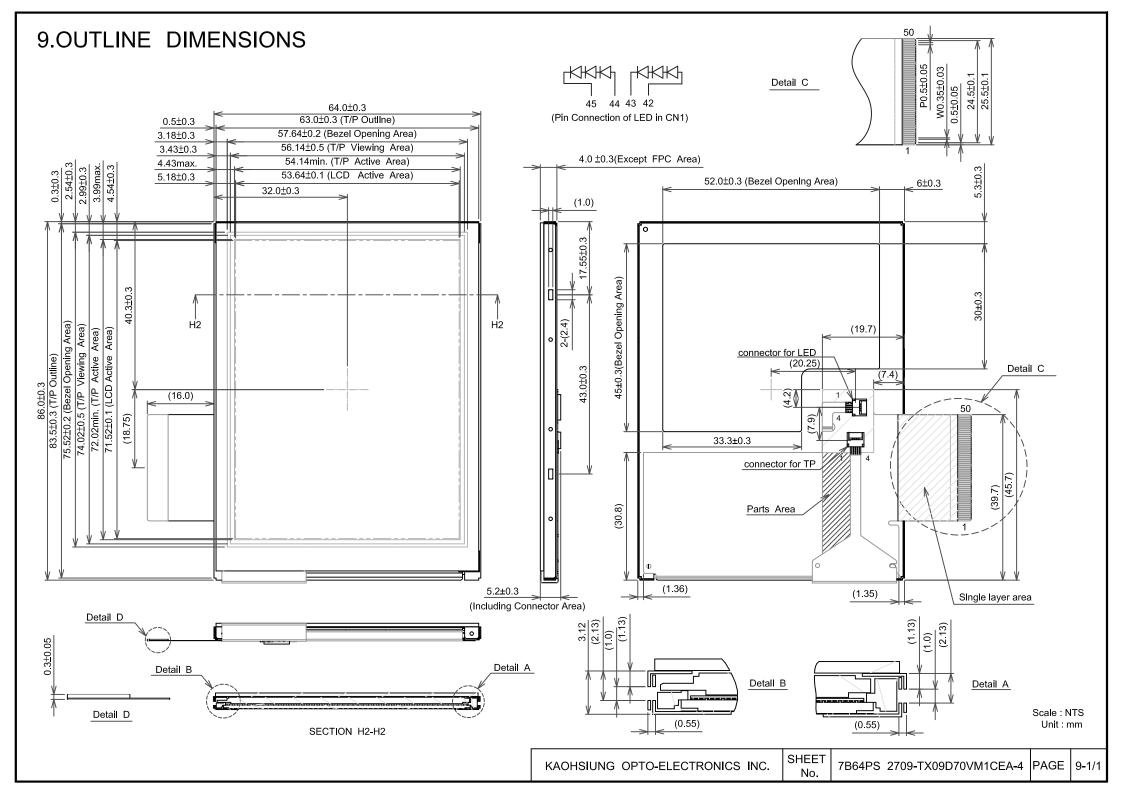
Suitable connector: FH12-50S-0.5P

No	SYMBOL	FUNCTION	No	SYMBOL	FUNCTION
1	VGH	Power Supply for Gate Driver (High)	26	B5	Blue Data
2	DISP	Display on/off (Note1.)	27	POL	Data Polarity Judge pulse (Note2.)
3	CL3	Gate Driver Shift Clock	28	М	AC Modulation Clock
4	STV	Gate Driver Start Pulse	29	LOAD(CL1)	Source Driver Latch Pulse
5	VSS	GND	30	CL2	Source Driver Shift Clock
6	VGL	Power Supply for Gate Driver (Low)	31	V0	Gray Scale Voltage
7	VSS	GND	32	V4	No Connection (Note3.)
8	STH	Source Driver Start Pulse	33	AVDD	Power Supply for Source Driver
9	R0	Red Data	34	AVDD	Power Supply for Source Driver
10	R1	Red Data	35	V5	Gray Scale Voltage
11	R2	Red Data	36	V9	Gray Scale Voltage
12	R3	Red Data	37	DVDD	Power Supply for Logic
13	R4	Red Data	38	DVDD	Power Supply for Logic
14	R5	Red Data	39	VCOM	Common Voltage
15	G0	Green Data	40	VCOM	Common Voltage
16	G1	Green Data	41	VSS	GND
17	G2	Green Data	42	Anode R	LED Power Supply (+)
18	G3	Green Data	43	Cathode R	LED Power Supply (-)
19	G4	Green Data	44	Anode L	LED Power Supply (+)
20	G5	Green Data	45	Cathode L	LED Power Supply (-)
21	B0	Blue Data	46	VSS	GND
22	B1	Blue Data	47	xR	Touch Panel Right Side
23	B2	Blue Data	48	yL	Touch Panel Lower Side
24	В3	Blue Data	49	xL	Touch Panel Left Side
25	B4	Blue Data	50	yU	Touch Panel Upper Side

Note 1: If you don't use Tcon IC, please follow the page 8-5/10 to set the DISP's timing.

Note 2: If you don't use Tcon IC, the POL must be connected to GND.

Note 3: Keep open electrically, please follow the page 8-8/10.

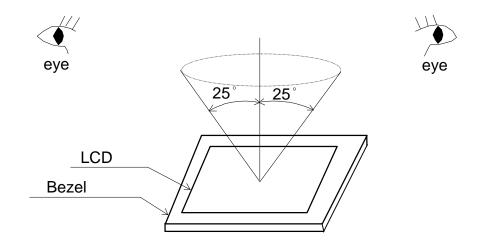


10. APPEARANCE STANDARD

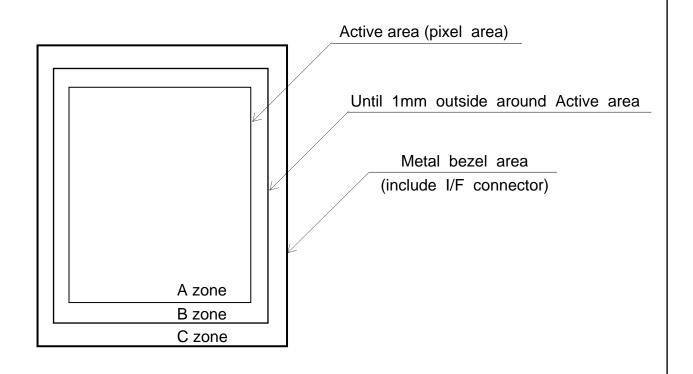
10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the following condition.

- (1) The inspection should be done in a dark room. (More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure. Viewing angle≤25°



10.2 DEFINITION OF ZONE



10-1/4

10.3 APPEARANCE SPECIFICATION

(1)LCD Appearance

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and KOE) will discuss the matter in detail.

No.	ITEM			CRITE	ERIA		APPLIED ZONE	
	Scratches	Length L(mm)	,	Width W(mm)		Maximum number acceptable		
		L≦2.0		W≦0.	03	ignored	A,B	
		L≦2.0	0.03	<w≦0< td=""><td>.05</td><td>4</td><td></td></w≦0<>	.05	4		
		L>2.0	0.0)5 <w< td=""><td></td><td>none</td><td>1</td></w<>		none	1	
	Dent		Seriou	ıs one is	not allo	owed	Α	
	Wrinkles in polarizer		Seriou	ıs one is	not allo	owed	Α	
	Bubbles	Average	diamete	r	M	laximum number		
			nm)			acceptable	A	
			0.3			2		
		0.3	< D			none		
	Stains		Filame	entous	(Line sh	nape)	_	
	Foreign	Length		Width		Maximum number		
	Materials	L(mm)		W(mm)		acceptable	A,B	
	Б	L<2.0				4		
L	Dark spot	L≦1.0		0.05 <w≦0.1 2<="" td=""></w≦0.1>				
-				ound(Do	•	•	_	
С		Average diar	Average diameter D(mm)			laximum number acceptable		
			<0.45			 		
D			<u>≤0.15</u>		6		A,B	
		0.15 <d< td=""><td></td><td></td><td></td><td>4</td><td>_</td></d<>				4	_	
		0.2 <d< td=""><td></td><td></td><td></td><td>none</td><td>_</td></d<>				none	_	
		The total				mentous + Round=9	_	
	D . D	Those wiped out easily are acceptable				1		
	Dot Defect					Maximum		
						number acceptable		
		Sparkle mod		1 .	dot	4	_	
		Sparkle mod	-		dots	2(sets)		
			L		otal	4		
		Black mode	,		dot	4	A , B	
		Biddik illidd			dots	2(sets)		
			L		otal	4	1	
		Sparkle mod	е				7	
		& Black mod		2 c	2 dots 2(sets)			
				To	otal	6		

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(2)Touch panel appearance

Visual inspection should be done under the following condition.

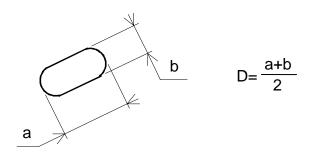
- *) The inspection should be done in a dark room. (more than 500 (lx) and non-directive)
- *) The distance between eyes of an inspector and the LCD module is 30 cm.
- *) The viewing angle ≤ 60°.

No.	ITEM		CRITE	RIA		APPLIED ZONE	
	Scratches	Length Width L(mm) W(mm)			Maximum number acceptable		
		-	, ,	0.05	ignored	A,B	
Т		10 <l< td=""><td>0.05≦W<</td><td>(0.1</td><td>none</td><td>, ,</td></l<>	0.05≦W<	(0.1	none	, ,	
0		-	0.1≦W	1	none		
U	Foreign		Filamentous ((Line sh	ape)		
С	C Materials	Length	Width		Maximum number		
Н		L(mm)	W(mm)		acceptable	_ ^ D	
	Dark Spot	-	W < 0.05		Ignored	A,B	
Р		L>3	0.05≦W≦	0.1	none		
Α		-	W≧0.1		Round		
N			Round(Do	t shape	2)		
E L		Average diame	eter D(mm)	Maximum number acceptable		A,B	
		D≦0.25		ignored			
		0.25 <d≦0.35< td=""><td colspan="2">6</td><td>В</td></d≦0.35<>		6		В	
		0.35 < D			none	A,B	

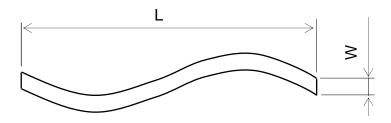
(3) Glass indentation

ITEM	SPECIFIC	SPECIFICATIONS					
Common Indentation	Y	$ \begin{array}{ c c c c }\hline X & Y & Z \\ \leq 5.0 & \leq 3.0 & \leq t \\ \hline \end{array} $					
Corner Broken	Z	X Y Z ≤3.0 ≤3.0 ≤t					
Proceeding Crack		None					

Note 1 : Definition of average diameter (D)



Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

(a) Dot Defect : Defect Area > 1/2 dot

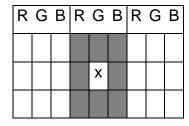
(b) Sparkle mode: Brightness of dot is more than 30% at Black raster.

(c) Black mode: Brightness of dot is less than 70% at R.G.B raster.

(d) 1 dot: Defect dot is isolated, not attached to other defect dot.

(e) N dot: N defect dots are consecutive.

(N means the number of defect dots.)



2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



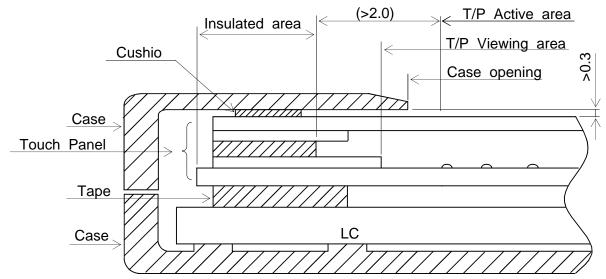
(f) Counting definition of adjacent dots(1 sets): same as 1 dot defect.

(g) Those wiped out easily are acceptable

11. PRECAUTION IN DESIGN

11.1 MOUNTING PRECAUTION

(1) When assembling the Touch Panel and you case, please refer to the figure below.



- (2) The clearance between the Touch Panel and case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (3) The case shall be designed not to touch the tail portion (FPC for Touch Panel).
- (4) The boundary space between the effective area and the insulated area is unstable. Touching this area may effect the operation of the Touch Panel. The case must be designed so that it does not touch the boundary space.

11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

11.3 HANDLING PRECAUTIONS

(1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched, pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 3H. (2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use: normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly.
 - Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.
 - When you need to take out the LCD module from some place at low temperature for test, etc.
 - It is required to be warmed them up to temperature higher than room temperature before taking them out.
- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands.

 (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than 1.96×10⁴ Pa.

 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N.
- (9) Since the metal width is narrow on these locations (see page 9-1/1), please careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.
 Hard wiping accumulated dust will leave scars on the surface even using a cloth.

11.4 OPERATION PRECAUTION

(1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.

LCM module's should usually be used under recommended operating conditions shown in chapter 4 and chapter 5. Exceeding any of these conditions may adversely affect its reliability.

SHEET NO.

- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
 - However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance range: Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (polyacetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between -30° C and 80° C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

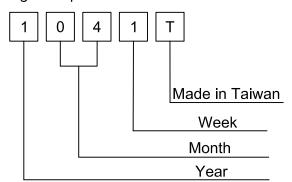
11.6 SAFETY

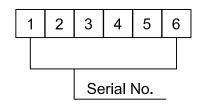
Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

12.DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 4 digits for production lot 6 digits for production control..





Year	Mark
2012	2
2013	3
2014	4
2015	5
2016	6

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Mark	01	02	03	04	05	06
Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	07	08	09	10	11	12

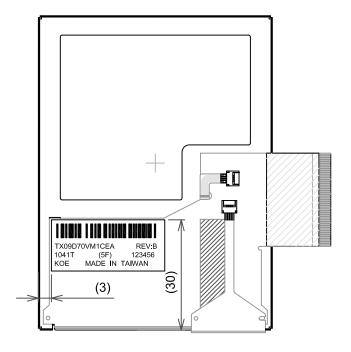
Week (Day In Calendar)	Figure In Lot Mark
01~07	1
08~14	2
15~21	3
22~28	4
29~31	5

12.2 REVISION (REV.) CONTROL

Rev. is the column for manufacturing convenience A-Z except I and O maybe written on this column.

REV.No	ITEM	NOTE	
Α	=	-	
В	Connectors Changed	PCN0804	

12.3 Location of lot mark: On the FPC



13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
 - Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - 1) When a question is arisen in the specifications.
 - 2) When a new problem is arisen which is not specified in this specifications.
 - 3) When an inspection specifications change or operating condition change by customer is reported to KOE, and some problem is arisen in the specification due to the change.
 - 4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with KOE.