HITACHI

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DATE: Mar.15,2006

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX14D11VM1CBB

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[&]quot;When product will be discontinued, oustomer will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;	PROPOSED BY:	Cho	7
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RECORD OF REVISION

DATE	SHEET No.	SUMMARY

7B64PS 2702-TX14D11VM1CBB-1 PAGE 2-1/1

KAOHSIUNG HITACHI

ELECTRONICS CO.,LTD.

DATE Mar.15,'06

3.GENERAL DATA

(9) Number of Colors

(1) Part Name TX14D11VM1CBB

(2) Module Dimensions 167.0(W)mm x 109.0(H)mm x (9.2)max.(D)mm

(3) LCD Active Area 115.2(W)mm x 86.4(H)mm

(4) Dot Pitch 0.12(W)mm x 3(R,G,B)(W) x 0.36(H)mm

(5) Resolution 320x3(R,G,B)(W)x240(H) dots

(6) Color Pixel Arrangement R,G,B Vertical stripe

(7) LCD Type Transmissive Color TFT LCD (Normally White)

(8) Display Type Active Matrix

(10) Backlight Cold Cathode Fluorescent Tube (U type CFL) x 1

262k Colors (R,G,B 6bit parallel)

(11) Weight 165g (typ.)

(12) Interface 40pin (C-MOS)

(13) Power Supply Voltage 3.3V only (Include Timing Controller and Power Unit)

(14) Viewing Direction 6 O'clock

4. ABSOLUTE MAXIMUM RATINGS

2	1.1 ELECTRICAL ABSOLUTE MA	AXIMUM RA	LINGS OF	LCD	VSS	3=0V
	ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
	Power Supply for Logic	VDD	-0.3	4.0	V	
	Innut Voltage	171	0.0	VDD (A)		(Nata 4)

			1 1417 0 44	1 0.4	OCIVILOIDIA
Power Supply for Logic	VDD	-0.3	4.0	V	
Input Voltage	VI	-0.2	VDD+0.2		(Note 1)
Input Current	li	0	1	Α	
Static Electricity	VESD0	-	±100	V	(Note 2,3)
	VESD1	_	±8	kV	(Note 2,4)

Note 1: DTMG,DCLK,RD0~RD5,GD0~GD5,BD0~BD5.

Note 2 : 200pF-250Ω 25°C - 70%RH

Note 3: Interface Pin Connector.

Note 4: The surface of metal bezel and LCD panel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STOF	RAGE	COMMENT
I I E IVI	MIN.	MIN. MAX.		MAX.	COMMENT
Temperature	(-20)	(70)	(-30)	(80)	(Note 2,3,6,7,8,10,12)
Humidity	(No	te 1)	(No	te 1)	Without condensation
Vibration	-	4.9m/s ² (0.5G)	-	19.6m/s ² (2G) (Note 5)	(Note 4)
Shock	-	29.4m/s ² (3G)	-	490m/s ² (50G) (Note 5)	XYZ directions (Note 9)
Corrosive Gas	Not Ac	ceptable	Not Acceptable		
CFL Life Time	50,000 h (Average) (Note 11)		-		At 25℃, IL=4.0mA max.

Note 1 : Ta ≤ 40°C :85%RH max.

Ta>40°C :Absolute humidity must be lower than the humidity of 85%RH at 40°C.

Note 2 : For storage condition Ta at -30° C < 48h, at 80° C < 100h. For operating condition Ta at -20°C < 100h

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4:5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower at low temperature.

Note 7: Only operation is guarantied at operating temperature. Contrast, response time. another display quality are evaluated at +25%.

Note 8: When LCM is operated over 60°C ambient temperature, the ICFL of LCM should be adjusted to 3mA max.

Note 9: Pulse Width: 10ms

Note 10: This is panel surface temperature, not ambient temperature.

Note 11: When brightness reached 50% of initial brightness.

Note 12: When LCM be operated less than 0°C, the life time of CFL will be reduced. The rise time of CFL ON will be longer when the ambient temperature below 0°C and confirming the characteristics of inverter is necessary.

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KAOHSIONG HITACHI	<u> </u>	4 4 7 100	O11.			4 4 4 4
	DATE	Mar.15,'06		7B64PS 2704-TX14D11VM1CBB-1	PAGET	4-1/1
ELECTRONICS CO.,LTD.			No.			
LELOTRONICO CO.,ETD.			INO.			

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C,VSS=0V

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	- ···,	3.0	3.3	3.6	V
Input Voltage for Logic	VI	"H" level	2.0	- -	VDD	V
(Note 1)	(Note 1)		VSS	-	0.8	V
Power Supply Current (Note 2)	IDD	VDD-VSS=3.3V	ŧ	150	-	mA
Vsync Frequency	fV	•	52	60	68	Hz
Hsync Frequency	fH	-	13.1	15.2	17.7	kHz
DCLK Frequency	fCLK	-	4.85	5.85	(7.0)	MHz

- Note 1: DTMG,DCLK, RD0~RD5,GD0~GD5,BD0~BD5.
- Note 2 : f V=60Hz,Ta=25℃, Pattern used as display pattern : All Black.
- Note 3: Need to make sure of flickering and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE		
Lamp Voltage	VL	<u></u>	760	-	Vrms	Ta=25°C		
Frequency	fL	-	55	-	kHz			
Lamp Current (1Lamp)(Note 6)	IL	3.0	4.0	6.0	mΑ	Ta=25 ℃		
Starting Discharge Voltage	VS (Note 2)	1300	1	ua.	Vrms	Ta=5°C		

- Note 1 : Please design your lamp driving circuit (inverter) according to the above specifications, and inform HITACHI about it.
- Note 2 : Starting discharge voltage is increased when LCM is operating under low temperature.
 - Please check the characteristics of your inverter before applying to your set.
- Note 3 : Average life time of CFL will be decreased when LCM is operating under low temperature.
- Note 4: Under lower driving frequency of an inverter, a certain Backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and noise.
- Note 5: When IL is over 6.0mA, it may cause uneven contrast near CFL location, due to heat dispersion from CFL.
- Note 6: We recommend to equip protection circuit (To stop output) which works under abnormal operation to the inverter for CFL

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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25[°]C (Backlight on)

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		θх	<i>φ</i> =0°,K≧5.0	-	65	-	deg	1~5
Migueina Argo			<i>φ</i> =180°,K≧5.0	-	65	-	deg	1~5
Viewing Area		. <i>θ</i> y	<i>φ</i> =90°,K≥5.0	_	70	-	deg	1~5
·		θ y	<i>φ</i> =270°,K≥5.0	-	50	-	deg	1~5
Contrast Ratio		K	ϕ =0°, θ =0°	120	350	1	-	5
Response Time (ri	se+fall)	tr+tf	ϕ =0°, θ =0°	-	(45)	-	ms	6
Color Tone	Dod	x		0.56	0.61	0.66	-	
(Primary Color)	Red	у		0.28	0.33	0.38	-	
	Green	х		0.25	0.30	0.35	-	
	Green	у	4-0° 0-0°	0.52	0.57	0.62	-	
	Diuo	х	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.09	0.14	0.19	-	
	Blue	у		0.03	0.08	0.13	-	
	\A/bito	х		0.24	0.29	0.34	ı	
	White	У		0.24	0.29	0.34	-	

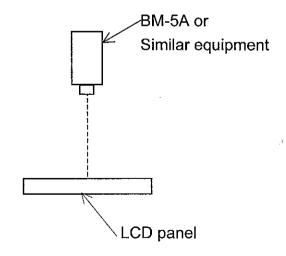
(Measurement condition: HITACHI standard) (Note 3~6): See next page.

Note 1 : Driving Condition

Display Pattern : White Raster

ICFL Current: (4)mA

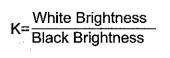
Note 2 : Measurement Condition (Transmitance)



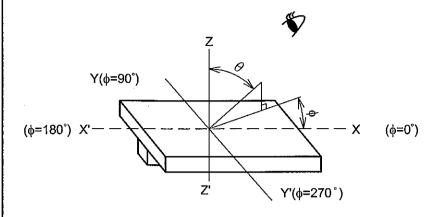
KAOHSIUNG HITACHI		Mar.15,'06	h. TREADS	2706-TX14D11VM1CBE	1 DACE	6-1/3
ELECTRONICS CO.,LTD.	DATE	N		2700-1X14D11VWITCBE	-I PAGE	0-1/3

Note 3 : Definition of θ and ϕ (Normal)

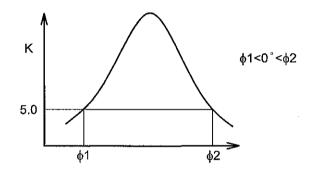
Viewing direction



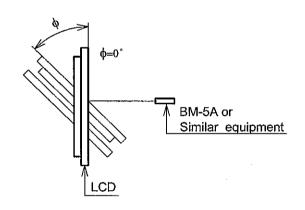
Note 5 : Definition of contrast "K"



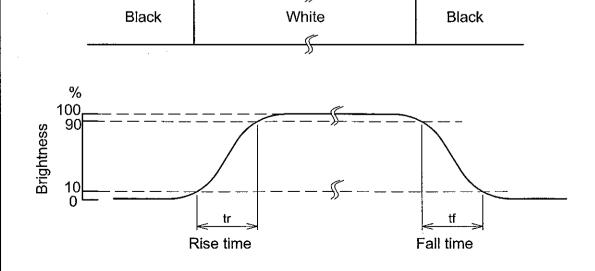
Note 4 : Definition of Viewing angle $\phi 1$ and $\phi 2$



Contrast ratio "K" vs Viewing angle "φ"



Note 6: Definition optical response time



KAOHSIUNG HITACHI	DATE	Mar 45 106	Sh.	7DC4DC 970C TV44D44VM4CDD 4	DAGE	0.00	ĺ
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6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	_	(600)	•	cd/m ²	IL=4.0mA (Note 1,2)
Rise Time	_	3	-	Minute	IL=4.0mA Brightness 80%
Brightness Uniformity	<u>-</u>	_	±25	%	Under mentioned (Note 1,3)

(Measurement condition: HITACHI standard)

CFL:0h operation, Ta=25°C

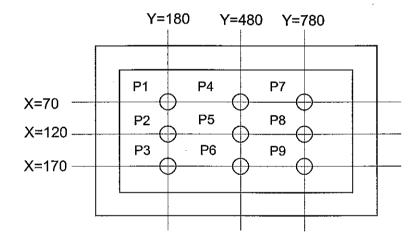
Display data should all be "ON"

Note 1 : Measurement after 10 minutes from CFL operating.

Average value of 9 points (Note 3)

Note 2: Brightness control: 100%.

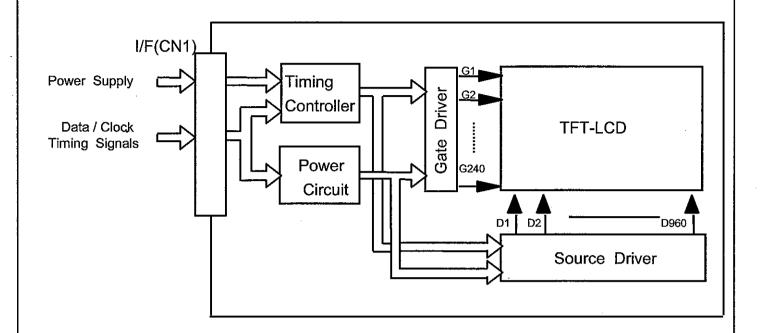
Note 3: Measurement of the following 9 places on the display.

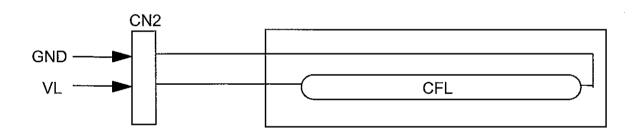


Note 4: Definition of the brightness tolerance.

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7.BLOCK DIAGRAM





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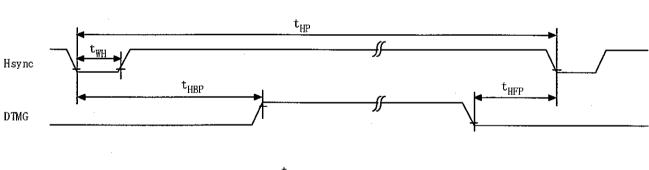
8.INTERFACE TIMING 8.1 INTERFACE TIMING

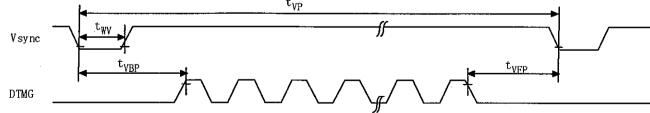
	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS
DCLK	Cycle time	(142)	(171)	(206)		tclk	
	Low level Width	12	-	-		twcL	
	High level Width	12	-	_	ns	twch	
	Rise time	-	-	25		trclk	
	Fall time	-	-	25		tfCLK	
	Duty	0.45	0.5	0.55	-	D	D= tclkl/clk
Hsync	Set up time	5	_	-	ns	tsн	for DCLK
	Hold time	10	-	-	119	tнн	IOI DOLK
	Cycle	370	(385)	397	tclk	the	
	Valid width	4	(5)	-	ICLK	twн	
	Rise/Fall time	_	-	30	ns	T _{Hr} ,t _{Hf}	
Vsync	Set up	0	-	-	tclk	tsv	for Hsync
	Hold	2	-		ICLK	t⊬∨	ioi risylic
	Cycle	251	(253)	261	tHP	tvp	
	Valid width	. 2	(2)	-	unp	tw∨	
	Rise/Fall time	-	-	50	ns	t∨r,t∨f	
DTMG	Set up time	5	-	-	ns	tsı	for DCLK
	Hold time	10	_		119	t⊦ı	IOI DOLK
	Rise/Fall time	-	-	30	ns	Tır,tıf	
	Horizontal back porch	28	(35)	-	tclk	tнвр	
	Horizontal front porch	22	(30)	_	ICLK	tHFP	
	Vertical back porch	6	(7)	-	tHP	tvвр	
	Vertical front porch	5	(6)	-	IHP	tvfp	
Data	Set up time	5	-	-	ns	tsp	for DCLK
	Hold time	10	_	_	110	t HD	IOI DOLK
	Rise/Fall time	-	-	25	ns	Tor,tof	

Note: Vsync Cycle No. should be set to odd.

KAOHSIUNG HITACHI	DATE	M 45 100	Sh.	7D04D0 0700 TV44D44 040 DD 4 D	14.OF	0.4/5
ELECTRONICS CO.,LTD.	DATE	Mar.15,'06	No.	7B64PS 2708-TX14D11VM1CBB-1 P.	AGE	8-1/5

8.2 Timing Chart (Data is latched negative edge trigger of DCLK) t_{Hf}, t_{Vf} t_{Hr} , t_{Vr} t_{If}, t_{Df} VSYNC, HSYNC, DTMG, R0~5, G0~5, B0~5 _{IL}max. DCLK Invalid Data DATA Invalid Data $t_{\rm HI}$ DTMG DCLK t_{SH} Hsync t_{HV} Vsync



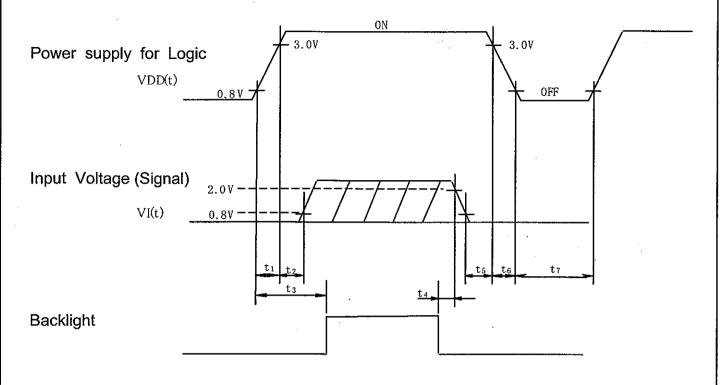


Note 1: DTMG is definition of the above timing for Hsync and Vsync.

Note 2: No matter when Hsync and Vsync is inputted ,this LCM can be driven only DTMG Signal. DTMG should be set to low level when it is not input valid data.

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8.3 POWER ON/OFF SEQUENCE



$$\begin{array}{c} \underline{\text{POWER ON}} \\ & t_1 \leqq 15\,\text{ms} \\ \\ 0\,\text{ms} < t_2 \leqq 45\,\text{ms} \\ \\ 0. \ 1s \leqq t_3 \end{array}$$

 $\begin{array}{c} \underline{\text{POWER OFF}} \\ 5\,\text{ms} & \leq t_4 \\ 0\,\text{ms} & \leq t_5 \leq 45\,\text{ms} \\ 0\,\text{ms} & \leq t_6 \leq 20\,\text{ms} \\ 0.\,\,4\,\text{s} & \leq t_7 \end{array}$

Note 1: $0V \le VI(t) \le VDD(t)$

VI(t) and VDD(t) is a surfeit of condition for power on/off.

Note 2: Input Voltage(Signal) should not be set high impedance when power on.

8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

	COLOR & GRAY	GRAY SCALE								DA	TA S	SIGN	IAL.							
	SCALE	LEVELS	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	В3	B4	B5
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Cyan	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red		1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	•	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS1	1	1_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	1					<u> </u>						<u> </u>						<u> </u>		
1100	↓	.				<u> </u>						<u> </u>						<u> </u>		
<u> </u>	Brighter	GS61	1_	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	V	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	<i>,</i> 0	0	0	0	0	0	0	0	0	0	0
	1	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Green	· 1	↓	<u> </u>			<u> </u>						<u> </u>						<u> </u>		
0.00	 	V				Ų.	· -					<u> </u>						Į		
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	·	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0.	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Blue	Î .	<u> </u>				<u> </u>						<u> </u>					,	<u> </u>		
		<u> </u>				<u> </u>						 					,	,		
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	······································	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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ELECTRONICS CO.,LTD.	DATE		No.	700463	2700-1X14D11VW1CB	B-1 FAGE	0-4/5

8.5 INTERNAL PIN CONNECTION

CN1 JAE: FA5B040HF1(Suitable FPC: t0.3±0.03mm, 0.5±0.03mm pitch)

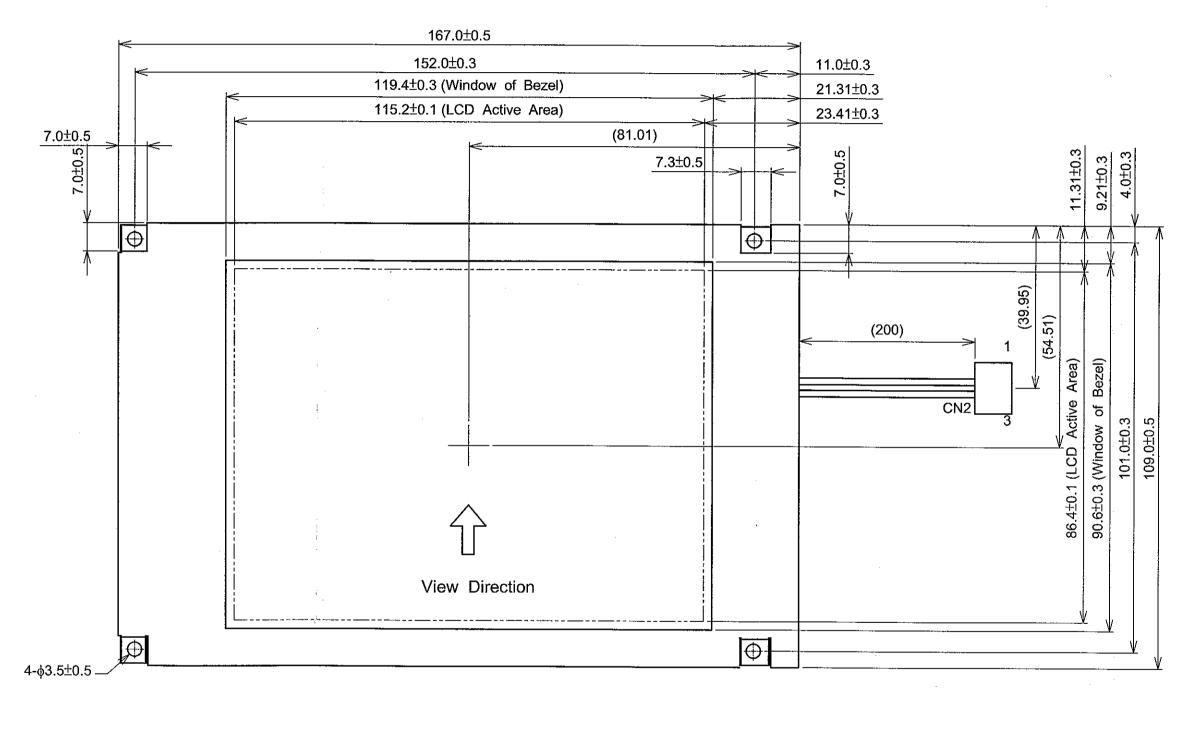
PIN No.	SIGNAL	FUNCTION
1	VDD	Power Supply for Logic
2	VDD	Power Supply for Logic
3	VDD	Power Supply for Logic
4	VDD	Power Supply for Logic
5	NC	No Connection
6	DTMG	Timing Signal for Data
7	VSS	GND
8	DCLK	Dot Clock
9	VSS	GND
10	NC	No Connection
11	VSS	GND
12	B5	
13	B4	Blue Data
14	В3	<u> </u>
15	VSS	GND
16	B2	
17	B1	Blue Data
18	В0	
19	VSS	GND
20	G5	
21	G4	Green Data
22	G3	
23	VSS	GND
24	G2	
25	G1	Green Data
26	G0	
27	VSS	GND
28	R5	
29	R4	Red Data
30	R3	
31	VSS	GND
32	R2	
33	R1	Red Data
34	R0	
35	(IC)	No Connection
36	VSS	GND
37	NC	No Connection
38	NC NC	No Connection
39	NC NC	No Connection
40	NC	No Connection

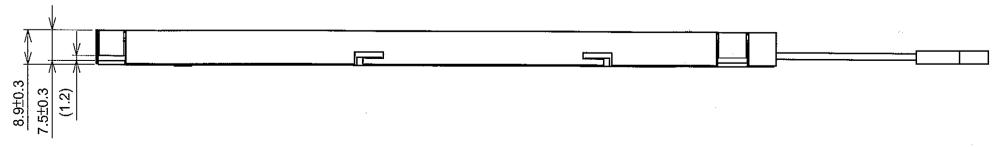
CN2 JST Housing : BHR-03VS-1

PIN	SIGNAL	LEVEL	FUNCTION
No.			
1	VCFL	_	Power Supply for CFL
2	NC	-	No connection
3	VSS	-	GND for CFL

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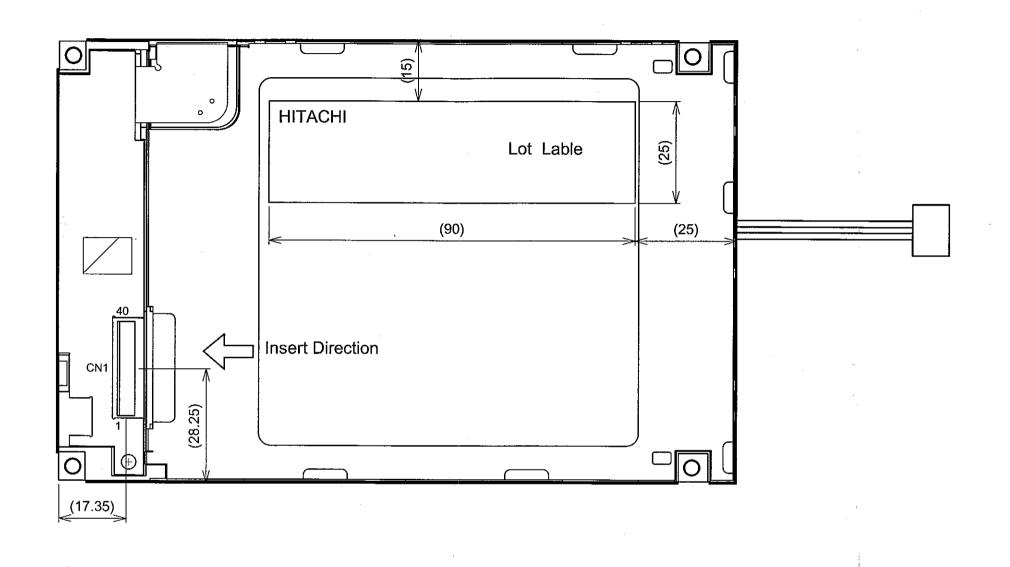
9. DIMENSIONAL OUTLINE





Scale : NTS Unit : mm

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Scale : NTS Unit : mm

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10. APPEARANCE STANDARD

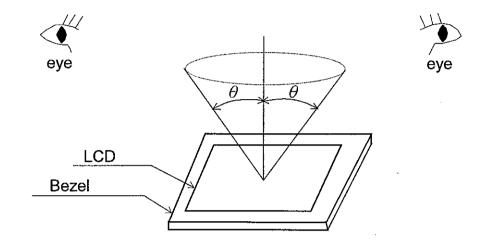
10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the following condition.

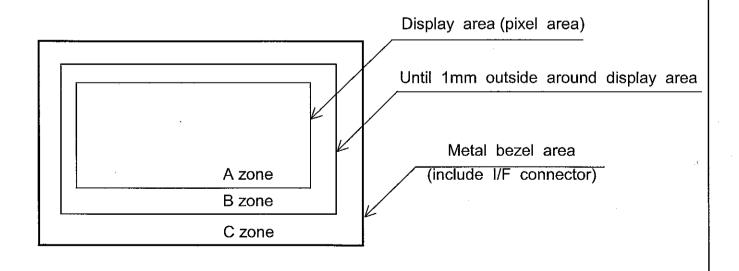
- (1) The inspection should be done in a dark room.(More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure.

The θ is defined as $\theta \leq 45^{\circ}$ for LCM power off

 $\theta \leq 5^{\circ}$ for LCM power on



10.2 DEFINITION OF ZONE



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10.3 APPEARANCE SPECIFICATION

(1)LCD Appearance

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

No.	ITEM	CRITERIA					
	Scratches	Length L(mm)	Width W(mm)	Maximum number acceptable	Minimum space	ZONE	
		Ignored	W≦0.02	Ignored		A,B	
			0.02 <w≦0.04< td=""><td>10</td><td>_</td><td colspan="2"></td></w≦0.04<>	10	_		
		L≦20	W≦0.04	10	_	1	
	Dent		Distinguished one is acceptable (To be judged by HITACHI standard)				
	Wrinkles in Polarizer	Same as above	/e			Α	
	Bubbles	D(ı	diameter mm)		n number ptable		
			≦0.2	· Ign	ored	A	
		0.2 <d≦< td=""><td></td><td>1</td><td>12</td><td>] ^ </td></d≦<>		1	12] ^	
		0.3 <d≦< td=""><td>≦0.5</td><td></td><td>3</td><td></td></d≦<>	≦0.5		3		
		0.5 <d< td=""><td></td><td>no</td><td>one</td><td></td></d<>		no	one		
	Stains		Filamentous (Line shape)			
	Foreign	Length	Width	1	num number		
	Materials	L(mm)	W(mm)		acceptable		
L	Dark Snot	L≦2.0	W≦(Ignored		
	Dark Spot	L≦3.0	0.03 <w≦0< td=""><td></td><td>6</td><td> </td></w≦0<>		6		
c		L≦2.5	0.05 <w≦0< td=""><td></td><td></td><td></td></w≦0<>				
			Round(Dot	'' '		-	
D		Average diamet		1	num Space		
		D(mm) D<0.2	acceptabl	e		-	
		0.2≦D<0.3	lgnored 10		 10 mm	A,B	
		0.2≦D<0.3 0.3≦D<0.4	5		30 mm	A,D	
		0.4≦D	none		-		
	•	The total numb		nentous + Roui	nd=10	1	
			ut easily are accep		10	1	
	Color Tone		by HITACHI ST			Α	
	Color Uniformity	Same as above	•			Α	
	Dot Defect				laximum number cceptable		
		Sparkle mode	1 dot		4		
			2 dots		1	1	
			Total (Note.(3	3)-(f))	5	A	
		Black mode	1 dot		5	7	
			2 dots		2	1	
			Total (Note.(3	3)-(f))	5		
			Total (Note.(3		10		

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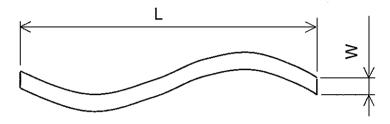
(2) CFL BACKLIGHT APPEARANCE

No.	ITEM		APPLIED ZONE			
С	Dark Spots White Spots	Average diam D(mm)	eter	Maximun	_	
F	Foreign Materials	D≦0.4			ignored	Α [
	(Snot) 0.4 < D				none	
	Foreign Materials (Line)	Width W(mm)		ngth mm)	Maximum number acceptable	
Α		W≦0.2	L≦	≦2.5	1	Α
C		VV <u>≦</u> U.∠	<u></u>	5 <l< td=""><td>None</td><td></td></l<>	None	
K	.,,,,	0.2 <w< td=""><td colspan="2">_</td><td>none</td><td></td></w<>	_		none	
L	Scratches	Width	Le	ngth	Maximum number	
1		W(mm)	L(r	nm)	acceptable	
G		W≦0.1	_		ignored	
H		0.1 <w≦0.2< td=""><td>L≦</td><td>11.0</td><td>1</td><td>Α</td></w≦0.2<>	L≦	11.0	1	Α
T		U. I \ VV ≦U.Z	11.0 <l< td=""><td>None</td><td></td></l<>		None	
		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none	

Note 1: Definition of average diameter (D)

$$D = \frac{a+b}{2}$$

Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

(a) Dot Defect: Defect Area > 1/2 dot

(b) Sparkle mode: Brightness of dot is more than 30% at Black raster.

(c) Black mode: Brightness of dot is less than 70% at R.G.B raster.

(d) 1 dot: Defect dot is isolated, not attached to other defect dot.

(e) N dot: N defect dots are consecutive (fig.1).

(N means the number of defect dots.)

R	G	В	R	G	В	R	G	В
				Х	(Antier			

2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



(f) Counting definition of adjacent dots (1 set): same as 1 dot defect.

(g) Those wiped out easily are acceptable.

11. PRECAUTION IN DESIG

11.1 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

11.2 HANDLING PRECAUTIONS

(1) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use:

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

- (2) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly. Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (3) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (4) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.

When you need to take out the LCD module from some place at low temperature for test, etc.

It is required to be warmed them up to temperature higher than room temperature before taking them out.

- (5) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands.

 (Some cosmetics are detrimental to polarizer's.)
- (6) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (7) Maximum pressure to the surface must be less than 1.96×10⁴ Pa.

 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N.
- (8) Since the metal width is narrow on these locations (see page 9-1/2), please careful with handling.
- (9) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses. Hard wiping accumulated dust will leave scars on the surface even using a cloth.

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11.3 OPERATION PRECAUTION

- (1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.
 - LCM module's should usually be used under recommended operating conditions shown in chapter 4. Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
 - However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.

11.4 STORAGE

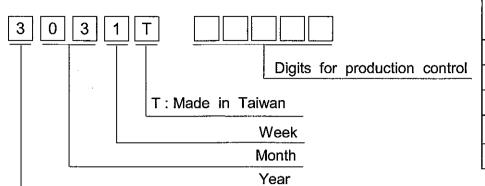
In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between 10°C and 35°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 5 digits for production control.



Year	Figure in
	lot mark
2006	6
2007	7
2008	8
2009	9
2010	0

Month	Figure in	Month	Figure in lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

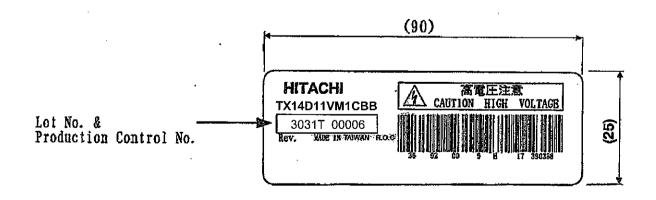
Week	Figure in		
(day in calendar)	lot mark		
1~ 7	1		
8~14	2		
15~21	3		
22~28	4		
29~31	5		

12.2 SERIAL No.

Serial No. is consisted of 5 digits number (00001~99999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.



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13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
 Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with HITACHI.