

Kaohsiung Opto-Electronics Inc.

FOR MESSRS :

DATE : May 1<sup>st</sup> ,2012

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

# TX16D11VM2CCA

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ACCEPTED BY:\_\_\_\_\_

PROPOSED BY:

		RECORD	DF RI	EVI	SION																		
DATE	SHEET No.				SUMMARY																		
Apr.13,'05	7B64PS 2706- TX16D11VM2CCA-2	6.1 OPTICAL CHARACTERISTICS OF LCD Revised:																					
	Page 6-1/3	ITEM		CONDITION	MIN	TYP	MAK	UNIT	NOTE														
				$\theta \mathbf{x}$	$\phi$ = 0 $^{\circ}$ , K $\geq$ 5.0	-	(60)	-	deg	1~5													
		Viewing A	rea	$\theta  \mathbf{x'}$	$\phi$ =180 $^{\circ}$ , K $\geq$ 5.0		(60)		deg	1~5													
			Ca	-	$\phi$ =90 $^{\circ}$ , K $\geq$ 5.0		(45)		deg	1~5													
				-	φ=270° , K≧5.0	-	(60)	-	deg	1~5													
			Red	X		-	(0.62)	-	-														
				y v		-	(0.34)	-	-														
		Color Tone	Green	x y		-	(0.59)	-	-														
		(Primary Color)		x	$\phi = 0^{\circ} , \theta = 0^{\circ}$	-	(0.00)	-	-														
			Blue	у		-	(0.09)	-	-														
			white	х	]	-	(0.29)	-	-														
			writte	у		-	(0.31)	-	-														
										]													
			M	1	CONDITION	MIN		MAK		NOTE													
					$\phi = 0$ °, K $\geq$ 5.0	-	70	-	deg	1~5													
		Viewing A	rea		$\phi = 180^{\circ} , K \ge 5.0$	-	70	-	deg	1~5													
					$\phi = 90^{\circ}, K \ge 5.0$ $\phi = 270^{\circ}, K \ge 5.0$	-	60 70	-	deg deg	1~5 1~5													
				x	φ-270 × R≥3.0	0.57	-	0.67	-	1.5													
			Red	y		0.29		0.39	_														
			0	x		0.25		0.35	-														
		Color Tone (Primary Color)										Color Tone (Primary Color)			- I	Green	у	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.54	4 0.59	0.64	-	
																(Primary Color)	Blue	х	$\varphi = 0 \rightarrow 0 = 0$	0.09		0.19	-
			у	-		4 0.09		-															
			White	X				0.34	-														
				у			6 0.31	1	-														
7B64PS 2706- TX16D11VM2CCA-2 Page 6-3/3		6.2 OPTICAL Revised :	CHAF	<b>ΚΑ</b> C	TERISTICS	OF	BACK	LIGF	11														
		ITEM			TYP																		
		BriRghtnes	SS		(200)																		
		ITEM			TYP																		
		BriRghtnes	SS		(280)																		
KAOHSIUNG	OPTO-ELECTRONICS IN	NC. SHEET NO.	7B64	IPS 2	2702-TX16D11V	M2C	CA-6	F	AGE	2-1/3													

RECORD OF REVISION					
DATE	SHEET No.	SUMMARY			
Aug.06,'05	7B64PS 2709- TX16D11VM2CCA-3 Page 9-1/2~2/2	9. DIMENSIONAL OUTLINE Changed the length of CN2 $  \stackrel{(15)}{\longrightarrow}  $ $CN2 \rightarrow$ $CN2$ $CN2$			
May.13,'08	7B64PS 2708- TX16D11VM2CCA-4 PAGE 8-6/7 7B64PS 2709- TX16D11VM2CCA-4 PAGE 9-2/2 7B64PS 2712- TX16D11VM2CCA-4 PAGE 12-1/1	CN1 JAE : FA5B040HP1 (Sn plating) → FA5B040HP1R3000 (Au plating) 9. DIMENSIONAL OUTLINE			
Jan.12,'10	7B64PS 2704- TX16D11VM2CCA-5 PAGE 4-1/1	HITACHI       MADE IN TAIWAN       I         Added : 12.4 REVISION(Rev.) CONTROL       ITEM       IEM         A       CN1 JAE : FA5B040HP1R3000       ITEM         4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS Changed       ITE M       OPERATING       STORAGE         I       T       E       MIN.       MAX.       MIN.       MAX.         Ambient Temperature       0°C       50°C       -20°C       60°C         I       T       E       MIN.       MAX.       MIN.       MAX.         I       T       MIN.       MAX.       MIN.       MAX.         I       T       MIN.       MAX.       MIN.       MAX.         Ambient Temperature       -20°C       70°C       -20°C       70°C			
KAOHSIUNG	OPTO-ELECTRONICS	INC. SHEET 7B64PS 2702-TX16D11VM2CCA-6 PAGE 2-2/3			

		RECORD	OF REVISION			
DATE Jan.12,'10	SHEET No. 7B64PS 2712- TX16D11VM2CCA-5 PAGE 12-1/1	Changed Added : 12.4 F	SUMMARY DN OF LOT MARK (26) TX16D11VM2CCA REV: 8041T. (5D) 123456. HITACHI. MADE IN TAIWAN (26) (14) (14) (26) (14) (14) (26) (14) (26) (14) (15) (15)	NOTE		
May 01,'12	All pages 7B64PS 2705- TX16D11VM2CCA-6 PAGE 5-1/2	Company nam KAOHSIUNG 5.1 ELECTRIC Added : N 5.2 ELECTRIC	HITACHI ELECTRONICS CO., ↓ OPTO-ELECTRONICS INC. CAL CHARACTERISTICS OF ote4	LCD		
		Added : N	ote i			
	OPTO-ELECTRONICS	INC. SHEET NO.	7B64PS 2702-TX16D11VM2		PAGE	2-3

## 3.GENERAL DATA

(1)	Part Name	TX16D11VM2CCA
(2)	Module Dimensions	173.0(W)mm x 70.0(H)mm x (8.6)max.(D)mm
(3)	LCD Active Area	148.8(W)mm x 53.76(H)mm
(4)	Dot Pitch	0.0775(W)mm x 3(R,G,B)(W) x 0.224(H)mm
(5)	Resolution	640 x 3(R,G,B))(W) x 240(H) dots
(6)	Color Pixel Arrangement	R,G,B Vertical stripe
(7)	LCD Type	Transmissive Color TFT LCD (Normally White)
(8)	Display Type	Active Matrix
(9)	Number of Colors	262k Colors (R,G,B 6bit parallel)
(10)	Backlight	Cold Cathode Fluorescent Tube (CFL) x 1 Average life time 50kh at 25 $^\circ\!\!{\rm C}$ IL=5mA
(11)	Weight	140g
(12)	Interface	40pin (C-MOS)
(13)	Power Supply Voltage	3.3V only (Include Timing Controller and Power Unit)
(14)	Touch Panel	Resistance Type
		The surface is antiglare type.
(15)	Viewing Direction	12 O'clock

## 4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF LCD					5=0V
ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARKS
Power Supply for Logic	VDD	-0.3	4.0	V	
Input Voltage	VI	-0.2	VDD+0.2		Note1
Input Current	li	0	1	Α	
Static Electricity	VESD0	-	±100	V	Note2,3
	VESD1	-	±8	kV	Note2,4

Note 1 : DTMG, DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

Note 2 : 200pF-250 Ω 25°C - 70%RH

Note 3 : Interface Pin Connector.

Note 4 : The surface of metal bezel and LCD panel.

#### 4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

	OPERATING		STORAGE			
ITEM	MIN.	MAX.	MIN. MAX.		REMARKS	
Ambient Temperature	<b>-20</b> °C	<b>70</b> °C	<b>-20</b> ℃	<b>70</b> °C	Note2,3,6	
Humidity	(N	lote 1)	1)	Note 1)	Without condensation	
Vibration	-	4.9m/s <sup>2</sup> (0.5G)	-	19.6m/s <sup>2</sup> (2G) (Note 5)	Note4	
Shock	-	29.4m/s <sup>2</sup> (3G)	-	490m/s <sup>2</sup> (50G) (Note 5)	XYZ directions Note7,8	
Corrosive Gas	Not A	cceptable	Not /	Acceptable		

Note 1 : Ta  $\leq$  40°C :85%RH max.

 $Ta > 40^{\circ}C$  : Absolute humidity must be lower than the humidity of 85%RH at 40°C.

Note 2 : Ta at -20 $^\circ\!\mathrm{C}$  for 48h , at 60 $^\circ\!\mathrm{C}$  for 168h.

- Note 3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.
- Note 4 : 5Hz~100Hz(Except resonance frequency)

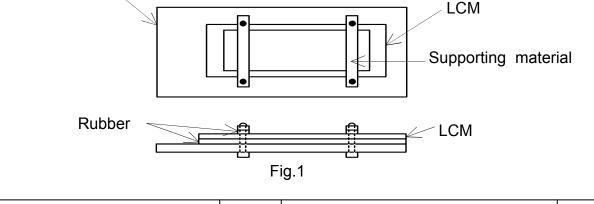
Note 5 : This LCM will resume normal operation after finishing the test.

Note 6 : The response time will be slower as low temperature.

Note 7 : Pulse Width : 10ms

Note 8 : The module has no mounting hole.

It should be fixed by the may of sandwiching-like method. (Fig.1) Base



SHEET

NO.

## 5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHAF	1 ELECTRICAL CHARACTERISTICS OF LCD						
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V	
Input Voltage for Logic	VI	"H" level	2.0	-	VDD	V	
(Note 1)	VI	"L" level	VSS	-	0.8	v	
Power Supply Current	IDD for HVGA Display		-	94	_		
(Note 2)	Mode	VDD-VSS=3.3V		01		mA	
	IDD for VGA Display	VDD V00-0.0V	_	110	_		
	Mode			110			
Vsync Frequency	fV	-	52	60	68	Hz	
	fH for HVGA Display Mode		12.8	15.1	36.1	<b>∠</b> ⊔→	
Hsync Frequency	fH for VGA Display Mode - 25.3	29.5	36.1	kHz			
	fCLK for HVGA Display Mode		8.7	10.7	26.7	MHz	
DCLK Frequency	fCLK for VGA Display Mode	-	17.2	20.9	26.7		

Note 1 : DTMG, DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

- Note 2 : f V=60Hz,Ta=25°C, Pattern used as display pattern : All Black.
- Note 3 : Need to make sure of flickering and rippling of display when setting the frame frequency in your set.
- Note 4: 0.4A fuse is applied in the module for IDD. For display activation and protection purpose, power supply is recommended larger than 1.0A to start the display and break fuse once any short circuit occurred.

#### 5.2 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

#### 5.2.1 ELECTRICAL CHARACTERISTICS

ITEM		SPECIFICATION	REMARKS			
ResistanceX-axisBetween TerminalY-axis		<b>50~300</b> Ω				
		50~50012				
Insulation Resistance		<b>20M</b> $\Omega$ min.	Operating Voltage : 25V DC			
Lincority	Х	2.5% max.				
Linearity	Y	5% max.				
Chattering		10ms max.				

#### 5.2.2 MECHANICAL CHARACTERISTICS

ITEM	SPECIFICATION	REMARKS
Pen Input Pressure	0.5N max.	R1mm polyacetal pen
Surface Hardness	3H min.	JIS K5400

#### 5.2.3 OPTICAL CHARASTERISTICS

ITEM	SPECIFICATION	REMARKS
Transparency	80% min.	

Note 1 : UV protection is recommended to avoid the possibility of performance degrading when touch panel is likely applied under UV environment for a long period of time.

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5.3 ELECTRICAL CHARACTERISTICS OF BACKLIGHT											
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARKS					
Lamp Voltage	VL	-	450	-	Vrms	<b>Ta=25</b> ℃					
Frequency	fL	-	55	-	kHz						
Lamp Current (1Lamp)(Note 7)	IL	2.0	5.0	7.0	mA	Ta=25℃					
Starting Discharge Voltage	VS (Note 2)	1300	-	-	Vrms	<b>Ta=0</b> ℃					

Note 1 : Please design your lamp driving circuit (inverter) based on the above specifications, and inform KOE about it.

Note 2 : Starting discharge voltage is increased when LCM is operating under low temperature.

Please check the characteristics of your inverter before applying to your set.

- Note 3 : Average life time of CFL will be decreased when LCM is operating under low temperature.
- Note 4 : Under lower driving frequency of an inverter, a certain Backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and noise.

Note 5 : When IL is over 7.0mA, it may cause uneven contrast near CFL location, due to heat dispersion form CFL.

Note 6 : We recommend to equip protection circuit (To stop output) which works under abnormal operation to the inverter for CFL.

## 6. OPTICAL CHARACTERISTICS

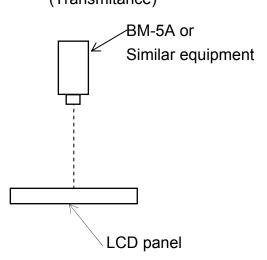
## 

6.1 OPTICAL CHA	RACTER	ISTICS OI	LCD		Ta=25°C (Backlight on)				
ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS	
		θΧ	φ <b>=</b> 0°,K≧5.0	_	70	-	deg	Note1~5	
Viowing Aroa		$\theta \mathbf{x}$	φ=180°,K≧5.0	-	70	-	deg	Note1~5	
Viewing Area		$\theta$ y	φ=90°,K≧5.0	-	60	-	deg	Note1~5	
		$\theta \mathbf{y}$	φ=270°,K≧5.0	-	70	-	deg	Note1~5	
Contrast Ratio		К	$\phi$ =0°, $\theta$ =0°	100	200	-	-	Note5	
Response Time (ri	se+fall)	tr+tf	$\phi$ =0°, $\theta$ =0°	-	45	-	ms	Note6	
Color Tone	Red	х		0.57	0.62	0.67	-		
(Primary Color)		у		0.29	0.34	0.39	-		
	Green	x		0.25	0.30	0.35	-		
	Gleen	у	$\phi = 0^\circ$ , $\theta = 0^\circ$	0.54	0.59	0.64	-		
	Blue	x	$\varphi = 0$ , $\theta = 0$	0.09	0.14	0.19	-		
	Diue	у		0.04	0.09	0.14	-		
	\\/hito	x		0.24	0.29	0.34	-		
	White	У		0.26	0.31	0.36	-		

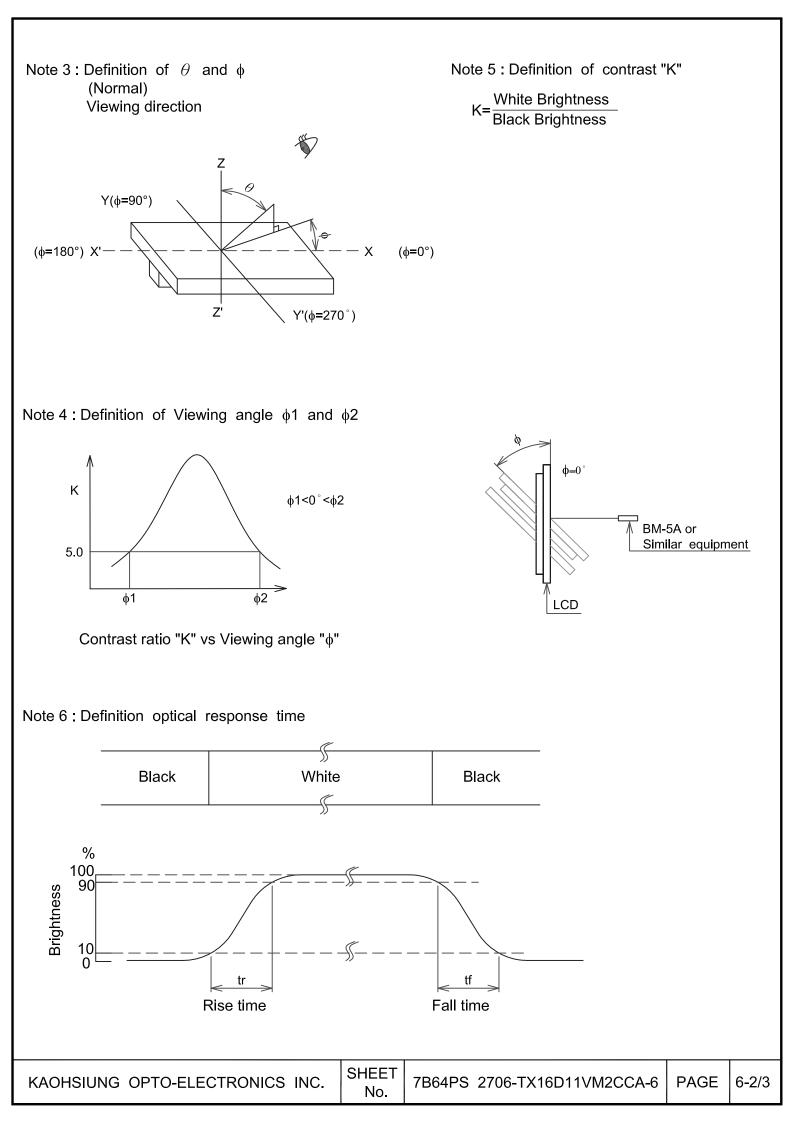
(Measurement condition : KOE standard) (Note 3~6): See next page.

Note 1 : Driving Condition Display Pattern : White Raster ICFL Current : (5)mA

Note 2 : Measurement Condition (Transmitance)



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#### 6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	REMARKS							
Brightness	-	280	-	cd/m <sup>2</sup>	IL=5mA (Note 1,2)							
Rise Time	-	3	-	Minute	IL=5mA Brightness 80%							
Brightness Uniformity	-	-	±25	%	Under mentioned (Note 1,3,4)							

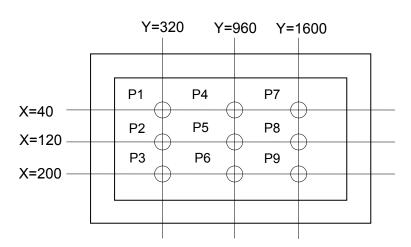
(Measurement condition : KOE standard)

CFL: 0h operation, Ta=25°C

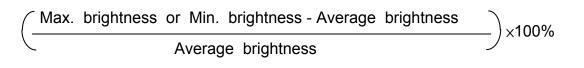
Display data should all be "ON"

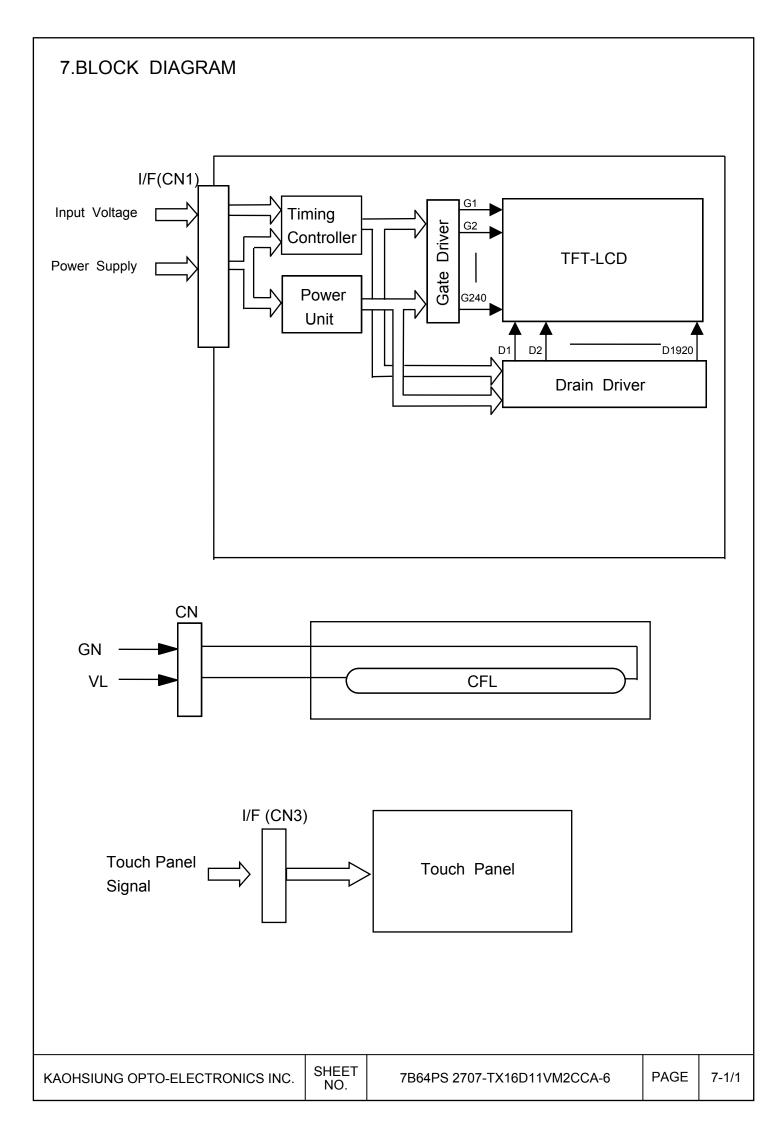
Note 1 : Measurement after 10 minutes from CFL operating. Average value of 9 points (Note 3)

Note 2 : Brightness control : 100%. Note 3 : Measurement of the following 9 places on the display.



Note 4 : Definition of the brightness tolerance.





## 8.INTERFACE TIMING

8.1.1 INTERFACE TIMING FOR HVGA DISPLAY MODE

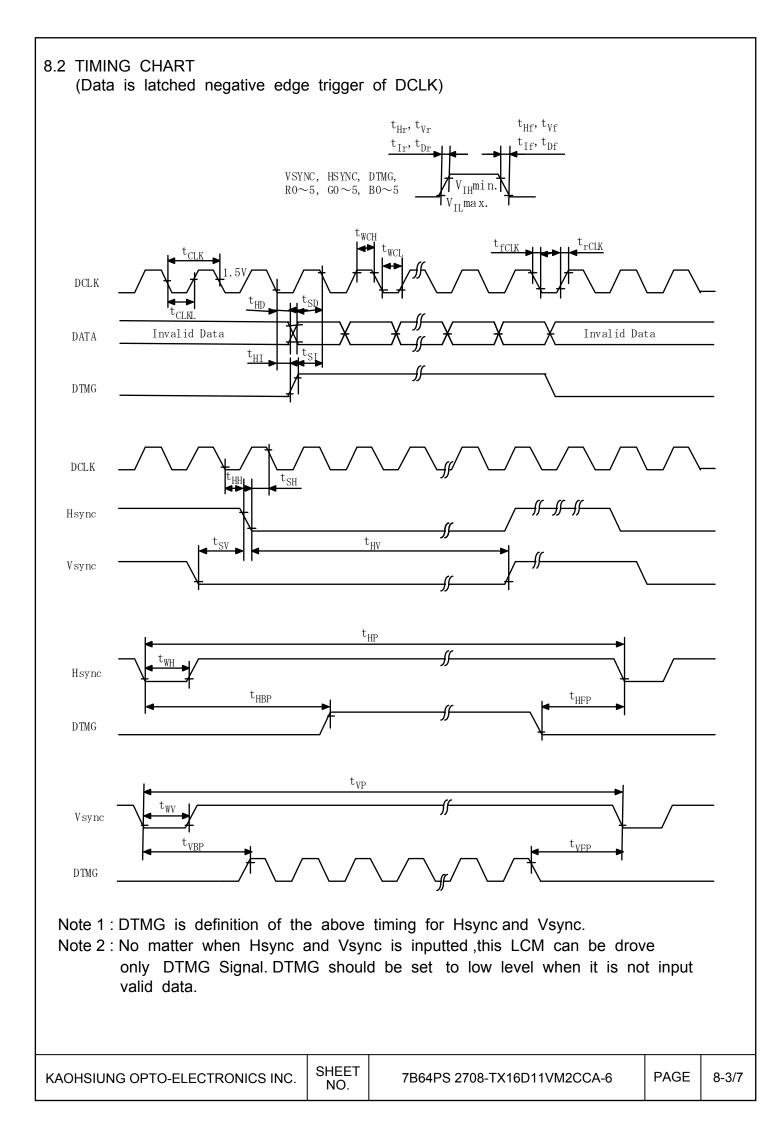
	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS
DCLK	Cycle time	37.5	94	114.9		tc∟ĸ	
	Low level Width	15	-	-		twc∟	
	High level Width	15	-	-	ns	twcн	
	Rise time	-	-	25		<b>tr</b> c∟ĸ	
	Fall time	-	-	25		t <sub>fCLK</sub>	
	Duty	0.45	0.5	0.55	-	D	D= tclkl/ clk
Hsync	Set up time	5	-	-	nc	tsн	for DCLK
	Hold time	10	-	-	ns	tнн	IOI DOLK
	Cycle	679	709	739	<b>t</b> c∟ĸ	tнр	
	Valid width	4	5	5	ICLK	twн	
	Rise/Fall time	-	-	30	ns	tHr,tHf	
Vsync	Set up	0	-	-	<b>t</b> CLK	tsv	for Hsync
	Hold	2	-	-	ICLK	tн∨	
	Cycle	245	251	533	tнр	t∨P	
	Valid width	2	2	2	LHP	tw∨	
	Rise/Fall time	-	-	50	ns	t∨r,t∨f	
DTMG	Set up time	5	-	-	nc	tsi	for DCLK
	Hold time	10	-	-	ns	tнı	IUI DOLK
	Rise/Fall time	-	-	30	ns	tir,tif	
	Horizontal back porch	24	37	50	<b>t</b> CLK	<b>t</b> hbp	
	Horizontal front porch	15	32	49	ICLK	<b>t</b> hfp	
	Vertical back porch	4	7	196	tнр	tvbp	
	Vertical front porch	1	4	97	LHP	<b>t</b> vfp	
Data	Set up time	5	-	-	200	tsd	for DCLK
	Hold time	10	-	-	ns	<b>t</b> hd	
	Rise/Fall time	-	-	25	ns	tdr,tdf	

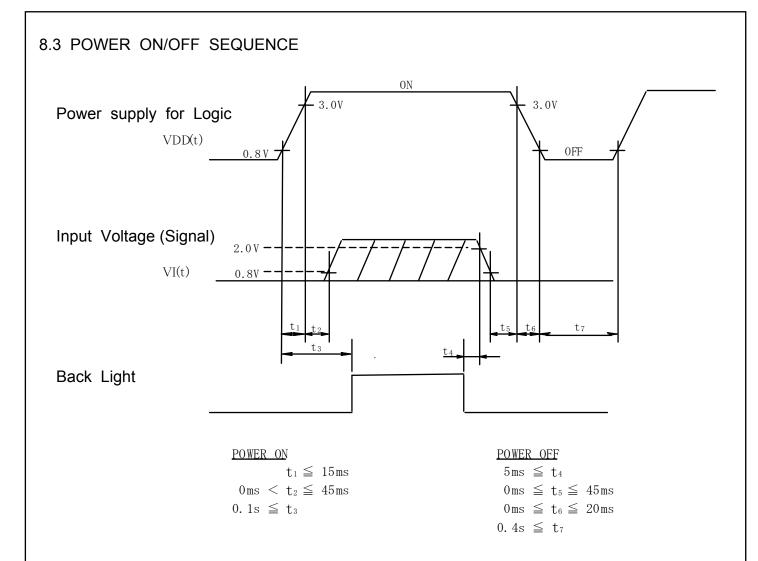
Note: Vsync Cycle No. should be set to odd.

## 8.1.2 INTERFACE TIMING FOR VGA DISPLAY MODE

	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS
DCLK	Cycle time	37.4	47.8	58.1		tc∟ĸ	
	Low level Width	15	-	-		twc∟	
	High level Width	15	-	-	ns	twcн	
	Rise time	-	-	25		<b>tr</b> c∟ĸ	
	Fall time	-	-	25		t <sub>fCLK</sub>	
	Duty	0.45	0.5	0.55	-	D	D= tclkl/ clk
Hsync	Set up time	5	-	-	20	tsн	for DCLK
	Hold time	10	-	-	ns	tнн	IUI DOLK
	Cycle	679	709	739	tour	tнр	
	Valid width	4	5	5	<b>t</b> CLK	twн	
	Rise/Fall time	-	-	30	ns	thr,thf	
Vsync	Set up	0	-	-	tour	tsv	for Hourse
	Hold	2	-	-	tclk	thv	for Hsync
	Cycle	485	491	533	tup	tvp	
	Valid width	2	2	2	thp	twv	
	Rise/Fall time	-	-	50	ns	tvr,tvf	
DTMG	Set up time	5	-	-	20	tsi	for DCLK
	Hold time	10	-	-	ns	thi	IUI DOLK
	Rise/Fall time	-	-	30	ns	tir,tif	
	Horizontal back porch	24	37	50	tour	<b>t</b> hbp	
	Horizontal front porch	15	32	49	tclk	<b>t</b> hfp	
	Vertical back porch	4	7	28	t <sub>HP</sub>	tvbp	
	Vertical front porch	1	4	25	LHP	<b>t</b> ∨fp	
Data	Set up time	5	-	-	20	tsd	for DCLK
	Hold time	10	-	-	ns	<b>t</b> hd	
	Rise/Fall time	-	-	25	ns	tdr,tdf	

Note: Vsync Cycle No. should be set to odd.





Note 1:  $0V \leq VI(t) \leq VDD(t)$ 

VI(t) and VDD(t) is a surfeit of condition for power on/off. Note 2 : Input Voltage(Signal) should not be set high impedance when power on. 8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

		<b>a - .</b>																		
	COLOR & GRAY	GRAY								DA	TA S	SIGN	JAL							
	SCALE	SCALE LEVELS	R0		R2	R3	R4		<u></u>	G1	<u></u>	00	<u> </u>	G5	B0	B1	B2	B3		DE
		LEVELS						R5				G3							B4	
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
<b>_</b> .	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Cyan	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\uparrow$	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	↑ I	$\checkmark$				↓ ↓						¥					V			
Rea	↓	$\checkmark$				↓ ↓						↓		i i			↓	i		
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	V	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\uparrow$	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Green	1	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
Green	$\checkmark$	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	$\checkmark$	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Dive	$\uparrow$	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
Blue	$\downarrow$	$\downarrow$				$\downarrow$						Ļ					$\downarrow$			
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	$\downarrow$	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

#### 8.5 INTERNAL PIN CONNECTION

CN1 JAE : FA5B040HP1R3000 (Suitable FPC : t0.3±0.05mm , 0.5±0.05mm pitch)

CN1 J	JAE : FA5	B040HP1R3	000 (Suitable FPC : $t0.3\pm0.05$ mm $, 0.5\pm0.05$ mm pitch)
	PIN No.	SIGNAL	FUNCTION
	1	VDD	Power Supply for Logic
	2	VDD	Power Supply for Logic
	3	VDD	Power Supply for Logic
	4	VDD	Power Supply for Logic
	5	NC	No Connection
	6	DTMG	Timing Signal for Data
	7	VSS	GND
	8	DCLK	Dot Clock
	9	VSS	GND
	10	NC	No Connection
	11	VSS	GND
	12	B5	
	13	B4	Blue Data
	14	B3	
	15	VSS	GND
	16	B2	
	17	B1	Blue Data
	18	B0	
	19	VSS	GND
	20	G5	
	21	G4	Green Data
	22	G3	
	23	VSS	GND
	24	G2	
	25	G1	Green Data
	26	G0	
	27	VSS	GND
	28	R5	
	29	R4	Red Data
	30	R3	
	31	VSS	GND
	32	R2	
	33	R1	Red Data
	34	R0	
	35	Vcom	Common Voltage (Generated by LCM)
	36	VSS	GND
	37	NC	No Connection
	38	NC	No Connection
	39	NC	No Connection
	40	NC	No Connection
CN2 J	ST Housi		R-02VS-1 (Suitable Connector : JST SM02B-BHSS-1)
		-	IS-002T-P0.5
	PIN No.	SIGNAL	LEVEL FUNCTION
	1	VSS	- GND for CFL
	2	VCFL	- Power Supply for CFL
	L —		·····

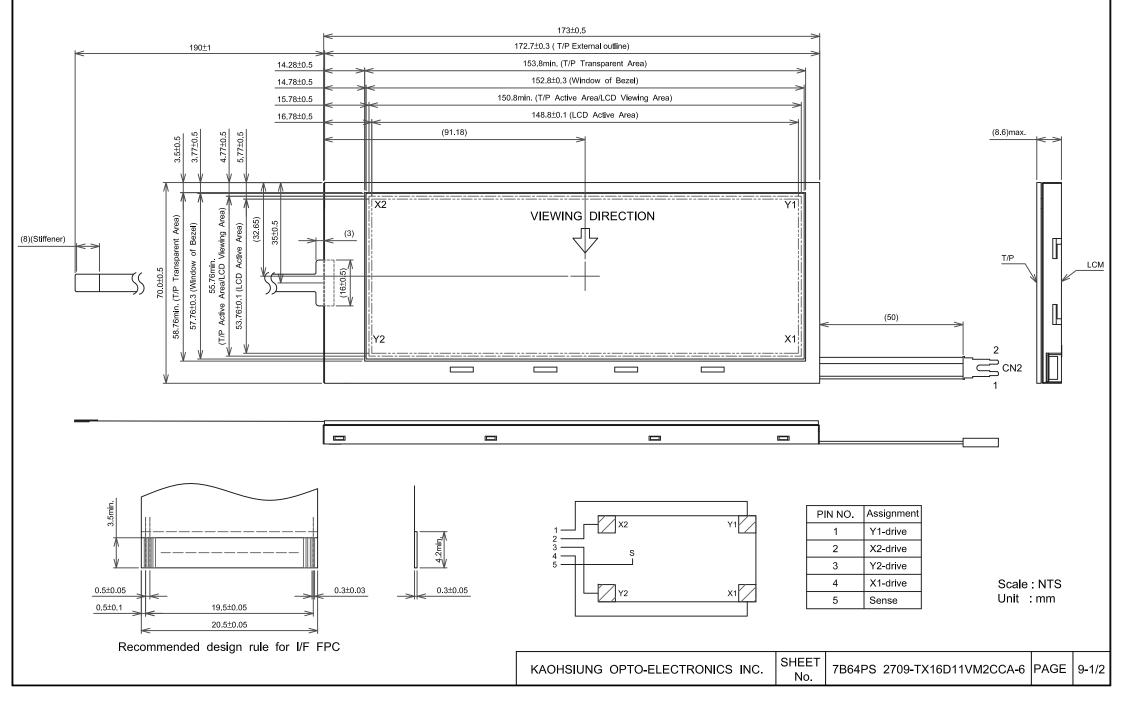
SHEET NO.

#### CN3 FPC CONNECTION

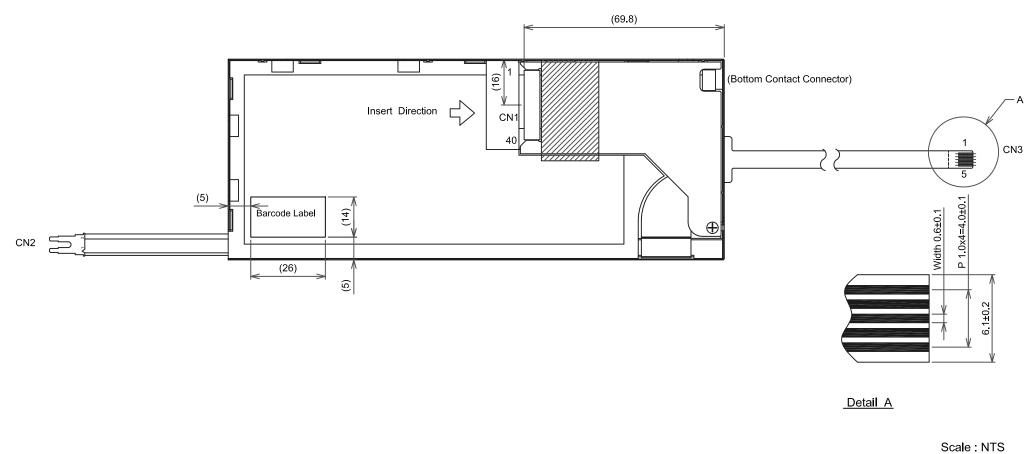
PIN No.	SIGNAL	FUNCTION
1	Y1	Analog Signal Touch Panel
2	X2	Analog Signal Touch Panel
3	Y2	Analog Signal Touch Panel
4	X1	Analog Signal Touch Panel
5	SENSE	Analog Signal Touch Panel

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### 9. DIMENSIONAL OUTLINE 9.1 FRONT VIEW

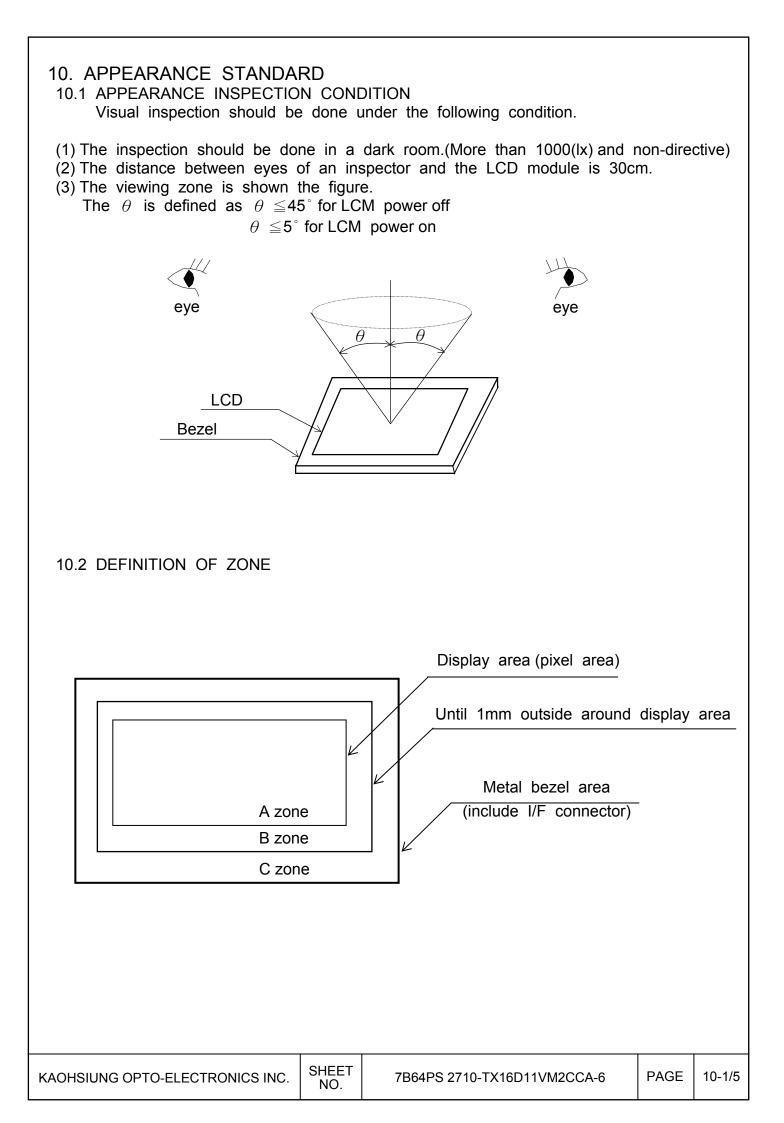






Unit : mm

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#### 10.3 APPEARANCE SPECIFICATION

#### (1)LCD Appearance

\*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and KOE) will discuss the matter in detail.

No.	ITEM			CRITE	RIA			APPLIED ZONE
	Scratches	Length L(mm)		Width W(mm)	n	aximum umber ceptable	Minimum space	
		Ignored		W≦0.02	Ignored		_	A,B
		L≦40	0.0			10	-	-
		L≦20		W≦0.04		10	_	
	Dent			Serious one is	not al			Α
	Wrinkles in polarizer			Serious one is				
	Bubbles	Averag	e di (mm			Maximum accep		
			≦0.	/		Igno		
		0.2 <d< td=""><td>≦0.</td><td>3</td><td></td><td>12</td><td></td><td>A</td></d<>	≦0.	3		12		A
		0.3 <d< td=""><td>≦0.</td><td>5</td><td></td><td>3</td><td></td><td></td></d<>	≦0.	5		3		
		0.5 <d< td=""><td></td><td></td><td></td><td>nor</td><td>ne</td><td></td></d<>				nor	ne	
	Stains			Filamentous (	Line s			
	Foreign	Length		Width			um number	
	Materials	L(mm)		W(mm)		acc	eptable	
L		L≦2.0		W≦0	.03	lg	nored	A,B
с	Dark Spot	L≦3.0		$0.03 < W \le 0$	0.05		6	
C		L≦2.5		$0.05 \! < \! W \! \le \! 0$	.1		1	
D								
		Average diame	eter	Maximum nur	mber	Minim	um Space	
		D(mm)		acceptable	е		_	
		D<0.2		Ignored			-	
		0.2≦D<0.3		10			0 mm	A,B
		0.3≦D<0.4		5		3	0 mm	
		0.4≦D		none			-	
		The total num				us + Round	d=10	_
		Those wiped c	out e	asily are accep				
	Dot Defect					Maximum		
		Charlda mada		1 404		accept		-
		Sparkle mode		1 dot		4		_
		l	Tota	$\frac{2 \text{ dots}}{2 \text{ (Noto (3) (f))}}$		<u>1</u> 5		
		Black mode	TUL	al (Note.(3)-(f)) 1 dot		ວ 5		A
		DIACK HOULE		2 dots		2		-
		l	Tota	2 dots al (Note.(3)-(f))		2		
				al (Note.(3)-(f))				-

## (2) CFL BACKLIGHT APPEARANCE

No.	ITEM		CRITERIA								
	Dark Spots White Spots	Average diam D(mm)	eter	Maximum	number acceptable	•					
F	Foreign Materials	D≦0.4			ignored	A					
L	(Spot)	0.4 <d< td=""><td></td><td></td><td>none</td><td></td></d<>			none						
	Foreign Materials	Width	Ler	ngth	Maximum number						
В	(Line)	W(mm)	L(mm)		acceptable						
А		W≦0.2	L≦	2.5	1	Α					
С		vv≧0.2	<u>≥</u> 0.2 2.5		None						
κ		0.2 <w< td=""><td></td><td>-</td><td colspan="2">none</td></w<>		-	none						
L	Scratches	Width	Ler	ngth	Maximum number						
Ι		W(mm)	L(n	าm)	acceptable						
G		W≦0.1		-	ignored						
Н		0.1 <w≦0.2< td=""><td>L≦</td><td>11.0</td><td>1</td><td>A</td></w≦0.2<>	L≦	11.0	1	A					
Т		$0.1 \le VV \ge 0.2$	11.(	) <l< td=""><td>None</td><td></td></l<>	None						
		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none						

## (3) TOUCH PANEL APPEARANCE

ITEM	CRITERIA		
Scratch -	W>0.05	L>3	None
	0.05≧W	L≦3	ignored
	W>0.05	L>10	None
Dust(Linear)	0.05≧W	L≦10	ignored
	D>0.35		None
Dust(Circular)	0.35≧D>0.25		Maximum 6pcs
	0.25≧D		ignored

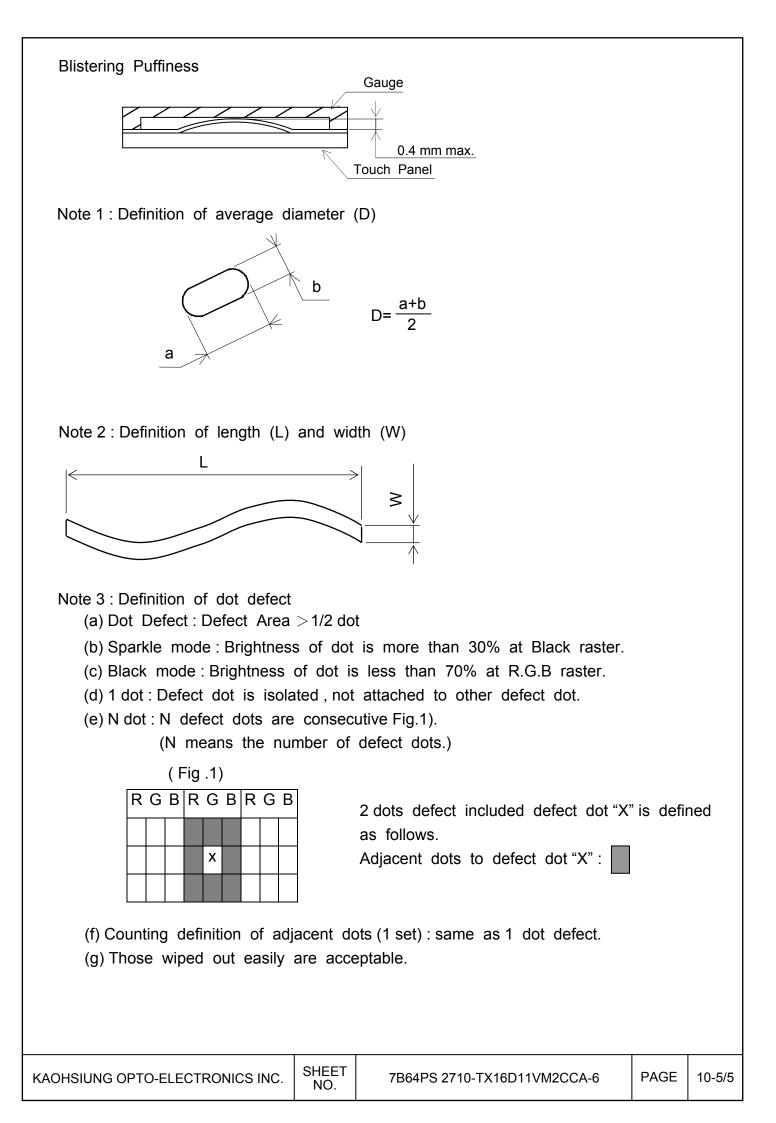
Applied only in the active area. Scratches or dusts in the outside of the active area are acceptable unless the electrical characteristics are affected.

• Dirt

Acceptable if not noticeable on a black mat.

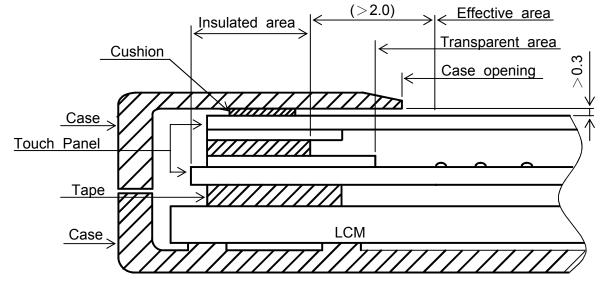
• Tip, crack (applicable to glass only)

ITEM	CRITERIA			
	XX Y	х	≦3.0	
Tip Corner		Y	≦3.0	Not acceptable if the film is damaged
		Z	≦1.1	
	x z	х	≦3.0	Not acceptable if the film is damaged
Tip Side z		Y	≦3.0	
	* E	Z	≦1.1	
Crack				None



## 11. PRECAUTION IN DESIGN

- **11.1 MOUNTING PRECAUTION**
- (1) When assembling the Touch Panel and you case, please refer to the figure below.



- (2) The clearance between the Touch Panel and case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (3) The case shall be designed not to touch the tail portion (FPC for Touch Panel).
- (4) The boundary space between the effective area and the insulated area is unstable. Touching this area may effect the operation of the Touch Panel. The case must be designed so that it does not touch the boundary space.

#### 11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

#### **11.3 HANDLING PRECAUTIONS**

- (1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched. pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 2H.
- (2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use :

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

NO.

- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly. Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.When you need to take out the LCD module from some place at low temperature for test, etc.It is required to be warmed them up to temperature higher than room temperature before taking them out.
- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands. (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than  $1.96 \times 10^4$  Pa. And if the pressure area is less than  $1 \text{ cm}^2$ , maximum pressure must be less than 1.96N.
- (9) Since the metal width is narrow on these locations (see page 9-1/2), please careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.Hard wiping accumulated dust will leave scars on the surface even using a cloth.
- 11.4 OPERATION PRECAUTION
- (1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.
   LCM module's should usually be used under recommended operating conditions shown in chapter 4.
   Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
   However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.

SHEET

NO.

- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance range : Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (poly acetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

#### 11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

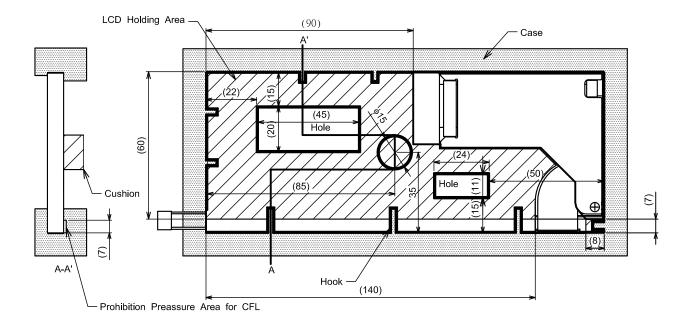
- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between  $10^{\circ}$ C and  $35^{\circ}$ C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

#### 11.6 SAFETY

Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

### **11.7 MOUNTING PRECAUTION**

- (1) When assembling the LCM Module, please refer to the below.
- (2) The use of cushion is recommended in order to protect the module from shock.
- (3) To pervent the module cover from being pressed, the distance between the case and cushion, should be shorter than 1.0mm.

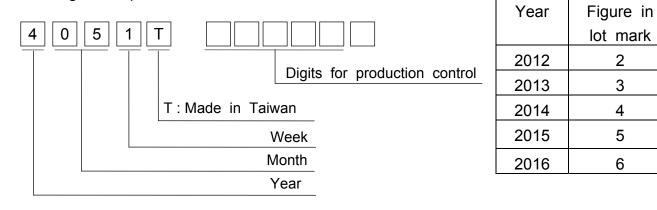


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## 12. DESIGNATION OF LOT MARK

#### 12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Month	Figure in lot mark	Month	Figure in lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
Мау	05	Nov.	11
Jun.	06	Dec.	12

l		
Week	Figure in	
(day in calendar)	lot mark	
1~ 7	1	
8~14	2	
15~21	3	
22~28	4	
29~31	5	

#### 12.2 SERIAL No.

Serial No. is consisted of 6 digits number (000001~999999).

#### 12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

#### 12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM	NOTE
A	CN1 JAE : FA5B040HP1R3000	-
В	Touch Panel (Change)	PCN No.0776

SHEET

NO.

TX16D11VM2CCA REV:	
	В
8041T (5F) 1234	56
KOE MADE IN TAIW	AN

## 13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
  - (1) When a question is arisen in the specifications.
  - (2) When a new problem is arisen which is not specified in this specifications.
  - (3) When an inspection specifications change or operating condition change by customer is reported to KOE, and some problem is arisen in the specification due to the change.
  - (4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with KOE.