

PROPRIETARY NOTE

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TITLE: 8inch AFFS+ for automotive

Preliminary Product Specification Rev. P2

HYDIS TECHNOLOGIES

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TFT-LCD PRODUCT	P2	2010.10.27

REVISION HISTORY

REV.	ECN NO.	DESCRIPTION OF CHANGES	DATE	PREPARED
P0		Initial Release	10.10.12	SW KANG
P1		Modify the information of pin assignment (Page 10. & 28)	10.10.12	SW KANG
P2		Update response time at low temperature (Page 8)	10.10.27	SW KANG

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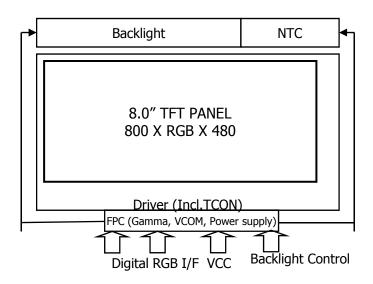


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1.0 GENERAL DESCRIPTION

1.1 Introduction

8.0" AFFS+ TFT-LCD is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as active switching devices. This module has a 8.0 inch diagonally measured active area with WVGA resolutions (800 horizontal by 480 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16,777,216 colors. The TFT-LCD panel used for this module is a low reflection and higher color type.



1.2 Features

- AFFS+ Technology
- Digital RGB 24bits Interface
- 6-bit+FRC color depth, Display 16,777,216 colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- Support DE (Data Enable) mode and SYNC mode
- SLG (Single Level Gate) function use
- Rear Mounting Frame
- SMD LED (27EA) Array (Bottom Side/Horizontal Direction)
- Operation Temp. / Storage Temp. $(-30 \,^{\circ} \sim 85 \,^{\circ} / -40 \,^{\circ} \sim 95 \,^{\circ})$

1.3 Application

Automotive display

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1.4 General Specifications

PARAMETER	SPECIFICATION	UNIT	REMARK
Active area	174.0(H) X 104.4(V)	mm	
Number of pixels	800(H) × RGB × 480(V)	pixels	
Pixel pitch	0.2175(H) × 0.2175(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16,777,216	colors	
Display mode	Normally Black		
Dimensional outline	190.0 ±0.3(H) X 120.0 ±0.3(V) X 10.0 ±0.3(D)	mm	Note : 1.
Weight	300 Тур	gram	
Back-light	SMD LED(27 EA) Array		
Color Gamut	58% (typ)	%	
Surface treatment	AG 13% / AG 13%		

Note: 1. At FPCB Side

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2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

PARAMETER		SYMBOL	MIN.	MAX.	UNIT	REMARK
Logic Power Supply		V_{DD}	VSS-0.3	4.0	V	Ta = 25 ±2 ℃
Logic Input Voltage		V_{IN}	VSS-0.3	V _{DD} +0.3	V	
Back-light Power Supply	Voltage	HV _{DD}	-0.3	40	V	
Back-light LED	25 ℃	ILED	-	70	mA	
Current	95℃	ILED	-	33	mA	
Back-light LED Reverse	Voltage	VR	-	5	V	
Operating Temperature		T _{OP}	-30	+85	°C	Note 1.
Storage Temperature		T _{SP}	-40	+95	$^{\circ}$	

Note 1. Operating temperature range on panel surface

Note 2. Temperature and relative humidity range are shown in the figure below.

90% RH Max. (40°C ≥ Ta)

Maximum wet - bulb temperature at 39° C or less. (> 40° C) No condensation.

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3.0 ELECTRICAL SPECIFICATIONS

Ta = 25±2℃

PARAMETER		MIN.	TYP.	MAX.	UNIT	REMARK
Logic Power Supply Voltage	V DD	3.0	3.3	3.6	V	Note 1
Logic Power Supply Current	Idd	-	240	-	mA	Note 1
Back-light PWM Frequency	Fpwm	100	-	500	Hz	
High Level Input Signal Voltage	VIH	0.7V DD	3.3	-	V	
Low Level Input Signal Voltage	VIL	-	0	0.3V _{DD}	V	
Back-light LED Voltage / Back-light LED Total Voltage	VBL	-	3.2/ 28.8	3.6/ 32.4	V	
Back-light LED Current / Back-light LED Total Current	ILED	-	40 /120	42/ 126	mA	
Lamp Life		20,000	-	-	hrs	Note 6
	P_{D}	-	0.8	1.0	W	Note 1
Power Consumption	P_{BL}	-	3.46	4.1	W	Note 4,5
	P_{Total}	-	4.26	5.1	W	Note 1, 4
Power Consumption (EBL)	PEBL	-		-	W	Note 1, 2, 3

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for 3.3V at $25\,^{\circ}$ C.

a) Typ: Window XP pattern

b) Max: Vertical Sub line pattern

c) EBL: Mosaic pattern (32 x 32)

- 2. The power supply voltage and current is measured and specified at the interface connector of LCM including LED Driver.
- 3. Reference value, which is measured with LED Driver for 12V.
- 4. Reference value, which is measured without LED Driver.
- 5. Calculated value for reference (VBL \times ILED \times # of LED Array (27ea)).
- 6. End of Life shall be determined by the time when any of the following is satisfied under continuous lighting at 25°C and ILED = 40mA.
 - Intensity drops to 50% of the Initial Value (Luminance Spec.)
 - Based on LED

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4.0 OPTICAL SPECIFICATION

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm2\,^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{\varnothing=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\varnothing=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\varnothing=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\varnothing=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \varnothing , the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement... VDD shall be 3.3+/- 0.3V at 25°C. Optimum viewing angle direction is 6 o'clock.

PARAMETI	ER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Llowies	Horizontal	Θ_3		-	89	90	Deg.	
Viewing Angle	HOHZOHILAI	Θ_9	CR > 10	1	89	90	Deg.	Nata 4
Range	Vertical	Θ_{12}	CR > 10	-	89	90	Deg.	Note 1
	vertical	Θ_6		-	89	90	Deg.	
Luminance Contrast	ratio	CR	Θ = 0°	700	-	-		Note 2
Luminance of White	Center	Y_{w}		400	-	-	cd/m²	Note 3
White Luminance Uniformity	9 Points	ΔΥ9	Θ = 0°	80	ı	ı		Note 4
White Chromaticity		X_{w}	Θ = 0°	0.284	0.314	0.344		
Write Chromaticity		y_w		0.286	0.316	0.346		
	Red	\mathbf{x}_{R}		0.592	0.622	0.652		
	Reu	\mathbf{y}_{R}		0.332	0.362	0.392		Note 5
Reproduction of	Green	\mathbf{x}_{G}	Θ = 0°	0.277	0.307	0.337		
Color	Green	\mathbf{y}_{G}	0 = 0	0.540	0.570	0.600		
	Blue	\mathbf{x}_{B}		0.115	0.145	0.175		
	blue	y _B		0.070	0.100	0.130		
Degrapes Time		$T_r + T_d$	Ta= 25° C Θ = 0°	-	ı	30	mc	Note 6
Response Time		. 3	Ta= -20° C Θ = 0°	-	-	140	ms	ווטנפ ס
Cross Talk		СТ	Θ = 0°	-	-	2.0	%	Note 7

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- Note: 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface. (see FIGURE 1)
 - 2. Contrast measurements shall be made at viewing angle of Θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster

Luminance when displaying a black raster

- 3. Luminance of white is defined as a luminance value of a point across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed. (See FIGURE 2)

Uniformity
$$\Delta Y = \frac{\text{Minimum Luminance of 9 points}}{\text{Maximum Luminance of 9 points}} X 100 (%)$$

- 5. The color chromaticity coordinates specified in Table 4 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 3 by switching the "data" input signal OFF and ON. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 4).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

User side Connector: 9686S-50B-GF(IRISO)

. Refer to appendix.15 (Figure 7.) for PIN Drawing and shift function

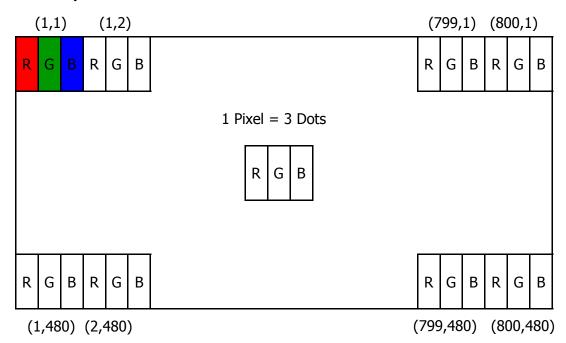
PIN NO. ZIF	PIN NO. FPC	SYMBOL	FUNCTION
1	1	B/L_AN	Back Light(ANODE)
2	1	BL_AN	Back Light(ANODE)
3	-	NC	Not connected
4	2	BL_CA_1	Back Light chain 1(cathode)
5	-	NC	Not connected
6	3	BL_CA_2	Back Light chain 2(cathode)
7	-	NC	Not connected
8	4	BL_CA_3	Back Light chain 3(cathode)
9	-	NC	Not connected
10	5	NTC	Thermal sensor of BL
11	6	NTC	Thermal sensor of BL
12	7	VCC	+3.3V Power Supply
13	8	VCC	+3.3V Power Supply
14	9	GROUND	GROUND
15	10	Blue Data Signal 7	Blue Data Signal 7
16	11	Blue Data Signal 6	Blue Data Signal 6
17	12	Blue Data Signal 5	Blue Data Signal 5
18	13	Blue Data Signal 4	Blue Data Signal 4
19	14	Blue Data Signal 3	Blue Data Signal 3
20	15	Blue Data Signal 2	Blue Data Signal 2
21	16	Blue Data Signal 1	Blue Data Signal 1
22	17	Blue Data Signal 0	Blue Data Signal 0
23	18	GROUND	GROUND
24	19	Green Data Signal 7	Green Data Signal 7
25	20	Green Data Signal 6	Green Data Signal 6
26	21	Green Data Signal 5	Green Data Signal 5
27	22	Green Data Signal 4	Green Data Signal 4
28	23	Green Data Signal 3	Green Data Signal 3
29	24	Green Data Signal 2	Green Data Signal 2
30	25	Green Data Signal 1	Green Data Signal 1
31	26	Green Data Signal 0	Green Data Signal 0
32	27	GROUND	GROUND
33	28	Red Data Signal 7	Red Data Signal 7
34	29	Red Data Signal 6	Red Data Signal 6
35	30	Red Data Signal 5	Red Data Signal 5
36	31	Red Data Signal 4	Red Data Signal 4
37	32	Red Data Signal 3	Red Data Signal 3
38	33	Red Data Signal 2	Red Data Signal 2
39	34	Red Data Signal 1	Red Data Signal 1
40	35	Red Data Signal 0	Red Data Signal 0
41	36	GROUND	GROUND
42	37	VSYNC	Vertical Sync
43	38	HSYNC	Horizontal Sync
44	39	ENAB	Data Enable (DE)
45	40	GROUND	GROUND
46	41	CLK	CLOCK
47	42	GROUND	GROUND
48	43	L/R	Shift Direction Control (left/right) ,NOTE 1
49	44	U/D	Shift Direction Control (up/down), NOTE 1
50	45	BITS	Bit Selection (6bit/8bit)
50	ເປ	רוזט	שונ שכוככנוטוז (טטוני)

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5.2. Data Input Format



< Display position of input data >

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6.0. SIGNAL TIMING SPECIFICATIONS

6.1 TTL Input Timing Specifications

The 8.0" AFFS+ TFT-LCD is operated by the DE (Data enable) mode or SYNC mode.

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	
Clock Frequency		1/T1	27.0	33.3	35.0	MHz	
Clock Pulse Duty		T2	40	50	60	%	
	Total	T3	870	1056	1088	Clocks	
	Active	T4	800	800	800	Clocks	
	Pulse Width	T5	-	128	-	Clocks	
Horizontal Sync	Back Porch	Т6	-	88	-	Clocks	
	Front Porch	T7	-	40	-	Clocks	
	Setup Time	Т8	8	-	-	ns	
	Hold Time	T9	8	-	-	ns	
	Total	T10	486	525	550	Lines	
	Active	T11	480	480	480	Lines	
	Pulse Width	T12	-	2	-	Lines	
Vertical Sync	Back Porch	T13	-	33	-	Lines	
	Front Porch	T14	-	10	-	Lines	
	Setup Time	T15	8	-	-	ns	
	Hold Time	T16	8	-	-	ns	
Data Setup Time	Data Setup Time		8	-	-	ns	
Data Hold Time	a Hold Time		8	-	-	ns	
DE Setup Time		T19	8	-	-	ns	
DE Hold Time		T20	8	-	-	ns	

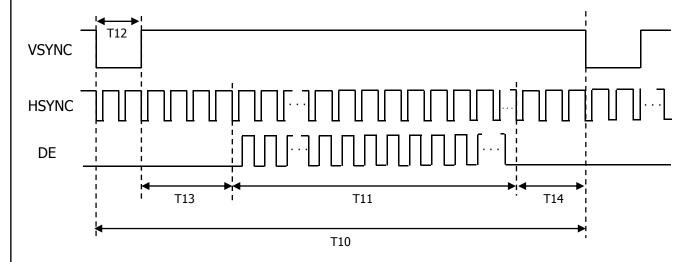
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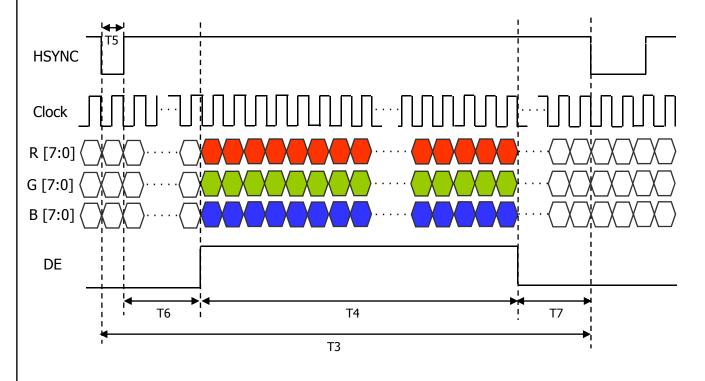
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7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Vertical Input Timing Waveform



7.2 Horizontal Input Timing Waveform

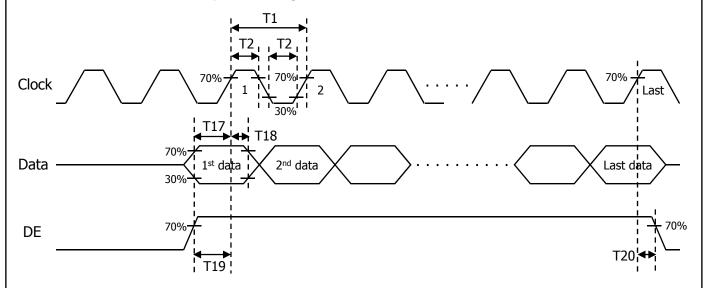


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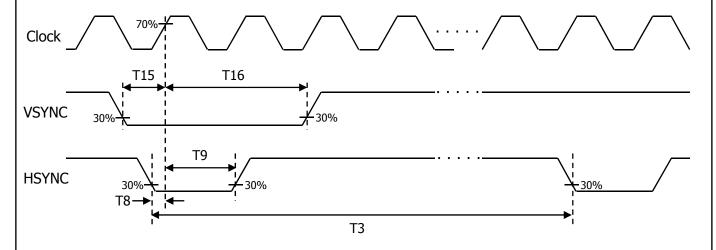


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7.3 Clock and Data Input Timing Waveform



7.4 Clock and SYNC Signal Input Timing Waveform



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8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Each color is displayed in 256 gray scales from a 8 bit data signal input. A total of 16,777,216 colors are derived from the resultant 24 bit data.

Colors & Gray			Red Data								Green Data								Blue Data						
	cale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Darker	0			0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Δ				1	,							,	<u> </u>							,	Į			
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Red	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
i		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Scale Of					1	,				<u> </u>								<u> </u>							
Green	Brighter	0	0	٥	0	_	0	_	0	-1	1	-1	1		-1	0	1								
	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∆	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Gray Scale	Δ		Ů	Ů	ا		Ū	Ŭ	Ů																
Of	∇					,								<u> </u>								<u>. </u>			
Blue	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
Of	Δ				ļ	,								l								<u> </u>			
White	∇				J	,							,	l							,	Į.			
& Black	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
Didek	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

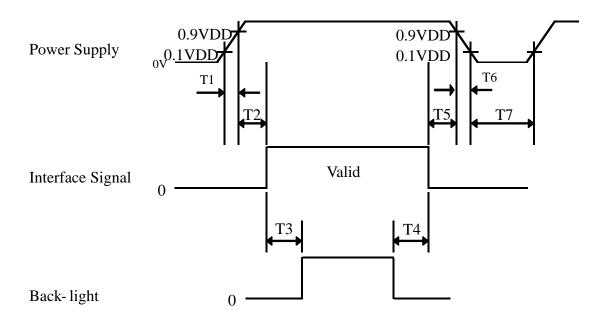
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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- $0 < T1, T6 \le 10 \text{ ms}$
- 0 < T2, T5 \leq 50 ms
- 100 ms \leq T3, T4
- 400 ms < T6

Notes:

- 1. When the power supply VDD is 0V, Keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 5, 6 shown in appendix shows mechanical outlines for the model.

PARAMETER	SPECIFICATION	UNIT
Active area	174.0 (H) × 104.4 (V)	mm
Number of pixels	800 (H) \times 480 (V) (1 pixel = R + G + B dots)	pixels
Pixel pitch	0.2175 (H) × 0.2175 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display colors	16,777,216	colors
Display mode	Normally Black	
Dimensional outline	190.0±0.3 (H) X 120.0±0.3 (V) X 10.0 ±0.3(D)	mm
Weight	300 Typ	g
Back-light	SMD LED Array(27EA)	

10.2 Mounting

See FIGURE 5. (shown in Appendix)

10.3 Anti-Glare Polarizer.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50 cm from the screen with an overhead light level of 150lux. The manufacture shall furnish limit samples of the panel showing the light leakage acceptable.

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11.0 Reliability Test

The Reliability test items and its conditions are shown in below.

No	Test Item	Condition	Duration
1	НТО	85 ℃	750Hrs
2	THO	60℃ 90%	240Hrs
3	LTO	-30℃	240Hrs
4	HTS	90℃	500Hrs
5	LTS	-40℃	500Hrs
	TST	-30°C ↔85°C 10cyc,3Hrs	60Hrs
6	Damp Heat .	25℃ 90~98% ↔55℃ 90~98% 6cyc	144Hrs
7	Humidity Resistance	65℃93%	500Hrs
8	VIB	3 direction X,Y,Z axes 6 repeats Sweep time 1min Peak acceleration 10G Frequency 10 to 150Hz Peak to peak sine wave	
9	SHK	3 direction X,Y,Z axes 6repeats Peak acceleration 100G Pulse duration 6ms sine wave	
10	ESD	$330 \mathrm{pF} \ 330 \Omega$ Air $\pm 25 \mathrm{kV} \ 18 \mathrm{point}$ $330 \mathrm{pF} \ 330 \Omega$ Contact $\pm 8 \mathrm{kV} \ 10 \mathrm{point}$	
11	Imaga stieline	5X5 Checker 1Hr (25℃) Level 2 (Initial), Level 1 (After 3min) @L127	
11	Image sticking	5X5 Checker 1Hr (65℃) Level 1 (After 30m) @L127	

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11.2 Extent of testing

All qualifications and tests of this automotive products have to be done in accordance with automotive general standard

- Functional Test (2parts)
 - All parameters from component specification
- Dimensional Check (2part)
- Appearance (all parts)
 - visual inspection for characteristics, which may impair the function, durability, installation or reliability.
 - visual inspection for characteristics, which may disturb the appearance of the visible area.
 - visual inspection of the powered unit with light source color of the target application and minimum intensity of the target application.

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12.0 HANDLING & CAUTIONS

12.1 Cautions when taking out the module

• Pick the pouch only, when taking out module from a shipping package.

12.2 Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and backlight element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables (FPC) with care.

12.3 Cautions for the operation

- When the module is operating, do not lose MCLK, DE signals. If any one of these signals were lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence were applied, the module would be damaged.

12.4 Cautions for the atmosphere

- Dewdrop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer-packing pouch and under relatively low temperature atmosphere is recommended.

12.5 Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

12.6 Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc, please pack the module not to be broken. We recommend using the original shipping packages.

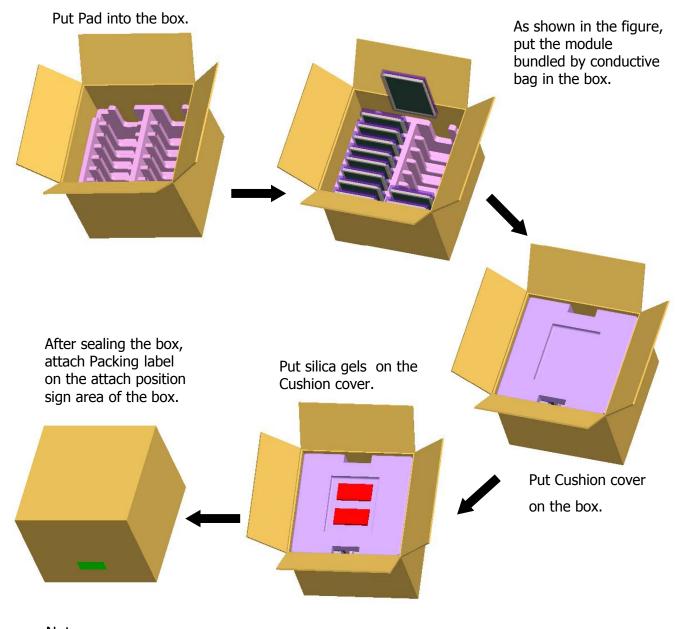
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13.0 PACKING

13.1 Packing Order



Notes

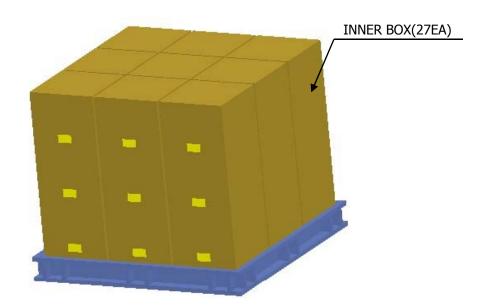
- Box Dimension: 333mm(W) X 333mm(D) X 271mm(H)
- Package Quantity in one Box: 16pcs

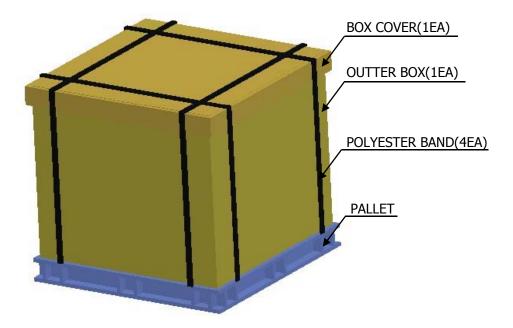
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13.2 Pallet Packing





* Note

● Pallet Dimension:: 1100 mm (L) × 1100 mm (W) × 120 mm (H)

Package Quantity in one Box : 16pcs

Box Quantity in one Pallet : 27ea

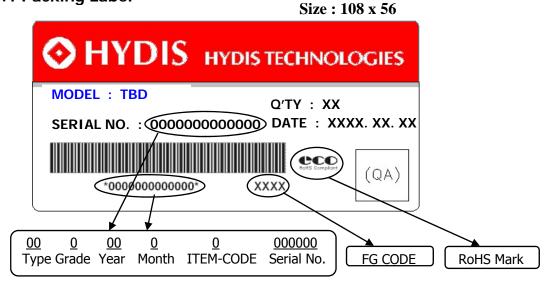
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14.0 Environment & Safety

14.1 Packing Label



14.2 Product Label



HYDIS Barcode

No 1. Control Number

No 5. Month (1, 2, 3, ..., 9, X, Y, Z)

No 2. Rank / Grade

No 6. FG Code

No 3. Line Classification (HYDIS: H)

No 7. Serial Number

No 4. Year (5: 2005, 6: 2006, ...)

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15.0 APPENDIX

Figure 1. Measurement Set Up

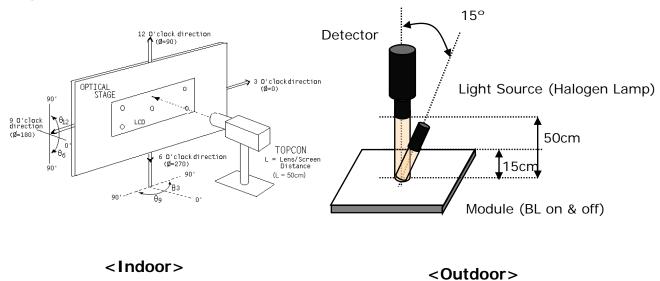
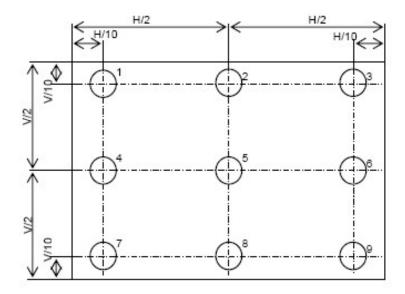


Figure 2. Average Luminance Measurement Locations & Uniformity Measurement Locations



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Figure 3. Response Time Testing

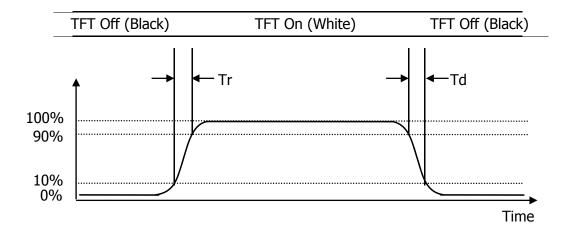
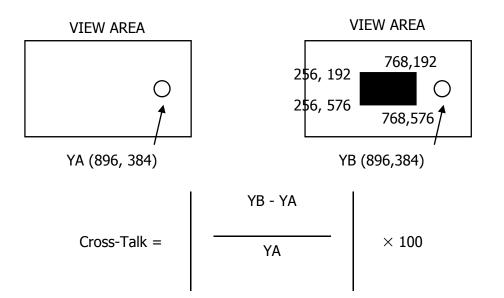


Figure 4. Cross Modulation Test Description



Where:

 Y_A = Initial luminance of measured area (cd/m²)

 Y_B = Subsequent luminance of measured area (cd/m²) The location measured will be exactly the same in both patterns.

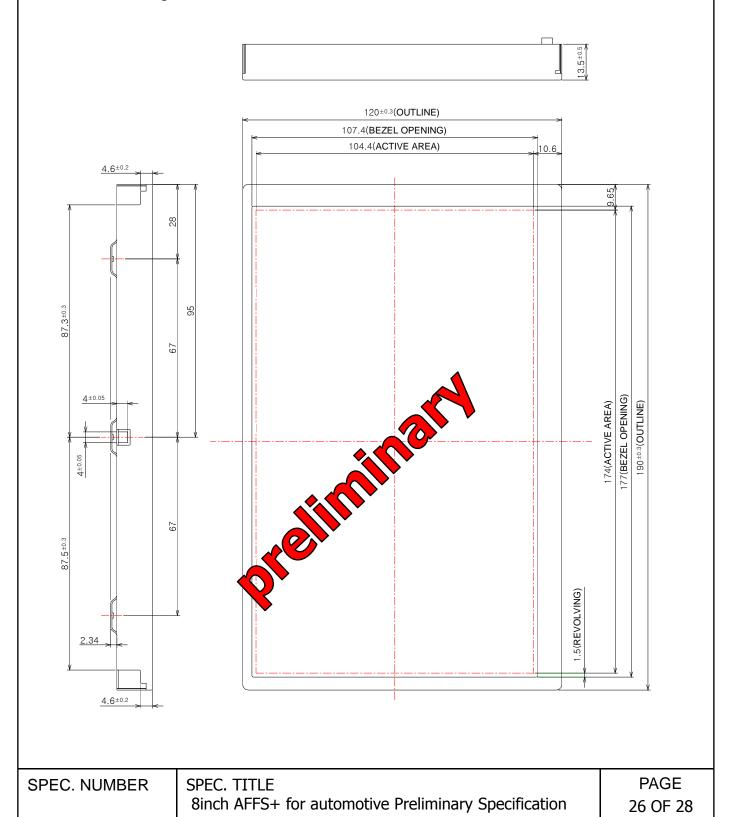
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A4(210 X 297) B2005-C001-B(3/3)



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Figure 5. TFT-LCD Module Outline Dimensions (Front View)



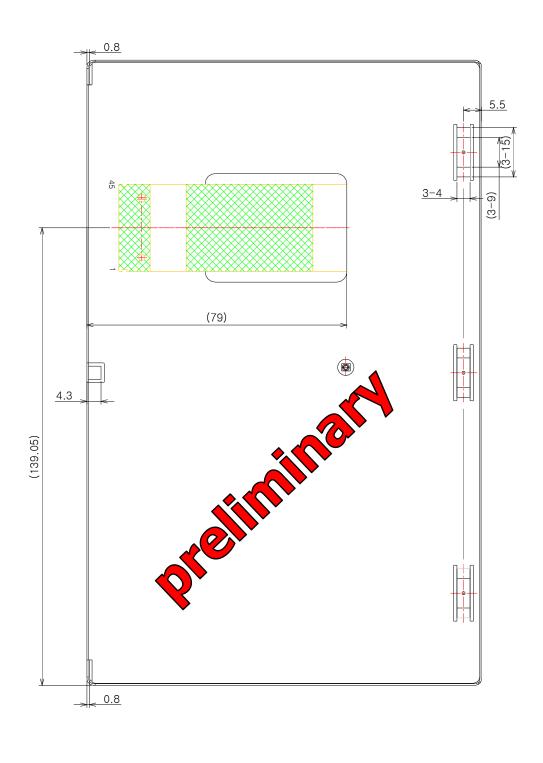
B2005-C001-B(3/3)

A4(210 X 297)



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Figure 6. TFT-LCD Module Outline Dimensions (Rear View)

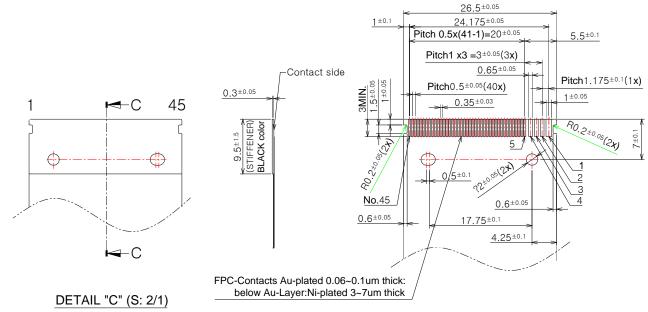


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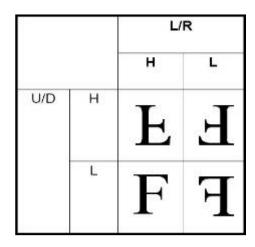
Figure 7. TFT-LCD Module Section for FPC



Connector: IRISO IMSA-9686S-50B-GF (50pin:pitch 0.5)

SECTION C-C (S: 2/1)

*1) L/R(PIN 36) & U/D(PIN 37) Direction setting



*2) BITS(PIN 38)

H: 6Bit In/Output (262K colors) L: 8Bit In/Output (16.7M colors)

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