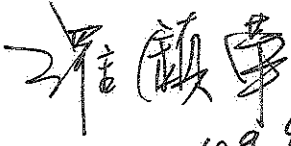

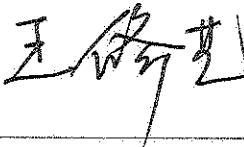
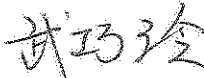


INNOLUX DISPLAY CORPORATION

MT190EN02 V.W LCD MODULE SPECIFICATION

() Preliminary Specification

(●) Final Specification

Approved by	Checked by	Prepared by
 109/9/3	 209/9/2  9/2/09	 9/11-09

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Document Number: MT190EN02 V.W-DR4-26

Contents:	
A. General Specification	
B. Electrical Specifications	
1. Pin assignment	
2. Absolute maximum ratings	
3. Electrical characteristics	
a. Typical operating conditions	
b. Display color vs. input data signals	
c. Input signal timing	
d. Display position	
e. Backlight Unit	
C. Optical specifications	
D. Reliability test items	
E. Safety	
F. Display quality	
G. Handling precaution	
H. Label	
I. Mechanical drawings	
Appendix	

A. General specification

NO.	Item	Specification	Remark
1	Display resolution (pixel)	1280(H) X 1024(V), WXGA+ resolution	
2	Active area (mm)	376.32(H) X 301.06(V)	
3	Screen size (inch)	19 inches diagonal	
4	Pixel pitch (mm)	0.294(H) X 0.294(V)	
5	Color configuration	R, G, B vertical stripe	
6	Overall dimension (mm)	396 (W) X 324 (H) X 9.8 (D) (Typ)	
7	Weight (g)	1500 (max)	
8	Surface treatment	Anti-Glare, Haze=25%, Hard coating (3H)	Note 1
9	Input color signal	8 bit LVDS	
10	Display colors	16.7M (6 bit with Hi-FRC)	
11	Color saturation	70% NTSC	
12	Optimum viewing direction	6 o'clock	
13	Power consumption	17.5(Watt)	Note 2
14	Backlight	W-LED	
15	RoHS&HF	RoHS & HF compliance	
16	TCO'03	TCO'03 compliance	Note 3

Note 1: Glare Option available

Note 2: The specified current is under the Vcc =5V, 25 ° C, fv=60Hz (frame frequency) condition whereas black pattern is displayed.

Note 3: Only Anti-Glare model can meet TCO'03 compliance

B. Electrical specifications

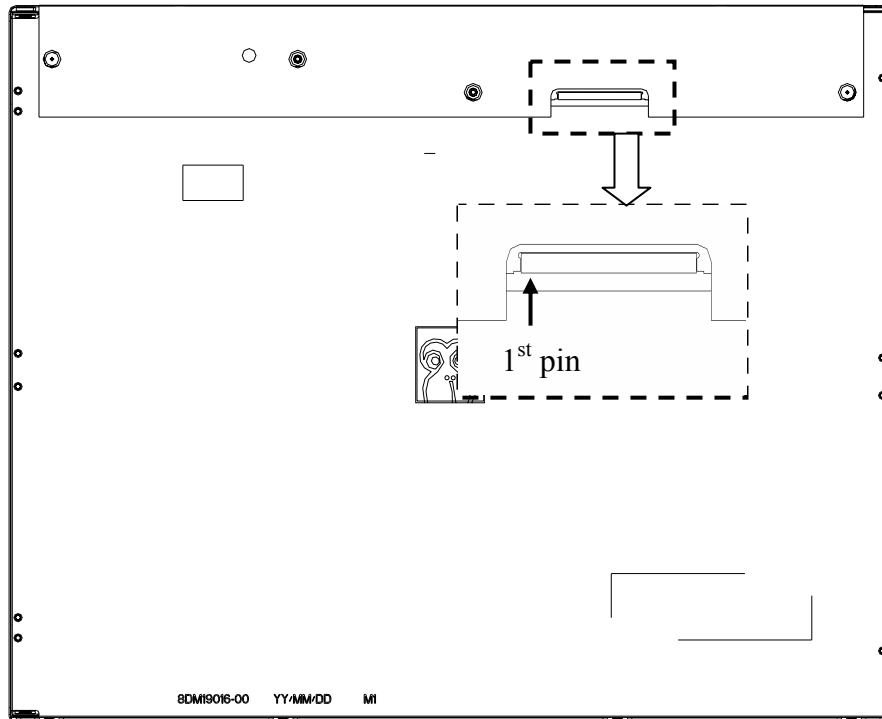
1. Pin assignment

Connector

FOXCONN GS23302-0011R-7F or mechanical interface equivalent connector.

Pin No	Symbol	Description
Frame	VSS	Ground
1	RXinO0-	-LVDS differential data input, Chan 0-Odd
2	RXinO0+	+LVDS differential data input, Chan 0-Odd
3	RXinO1-	-LVDS differential data input, Chan 1-Odd
4	RXinO1+	+LVDS differential data input, Chan 1-Odd
5	RXinO2-	-LVDS differential data input, Chan 2-Odd
6	RXinO2+	+LVDS differential data input, Chan 2-Odd
7	VSS	Ground
8	RXOC-	-LVDS differential Clock input (Odd)
9	RXOC+	+LVDS differential Clock input (Odd)
10	RXinO3-	-LVDS differential data input, Chan 3-Odd
11	RXinO3+	+LVDS differential data input, Chan 3-Odd
12	RXinE0-	-LVDS differential data input, Chan 0-Even
13	RXinE0+	+LVDS differential data input, Chan 0-Even
14	VSS	Ground
15	RXinE1-	-LVDS differential data input, Chan 1-Even
16	RXinE1+	+LVDS differential data input, Chan 1-Even
17	VSS	Ground
18	RXinE2-	-LVDS differential data input, Chan 2-Even
19	RXinE2+	+LVDS differential data input, Chan 2-Even
20	RXEC-	-LVDS differential Clock input (Even)
21	RXEC+	+LVDS differential Clock input (Even)
22	RXinE3-	-LVDS differential data input, Chan 3-Even
23	RXinE3+	+LVDS differential data input, Chan 3-Even
24	VSS	Ground
25	NC	No Connection
26	NC	No Connection
27	NC	No Connection
28	VCC	+5.0V power supply
29	VCC	+5.0V power supply
30	VCC	+5.0V power supply
Frame	VSS	Ground

30 Pin LVDS Connector Pin Position



1.2 Recommend Connector for Backlight Unit

This connector is mounted on the monitor system board for LED light-bar FFC mating.

Connector Name/Designation	Driver Board Connector
Manufacturer	Entery INDUSTRIAL CO.,LTD
Mating type part number	7080-Q10N-00R

1.3 LED Light Bar FFC

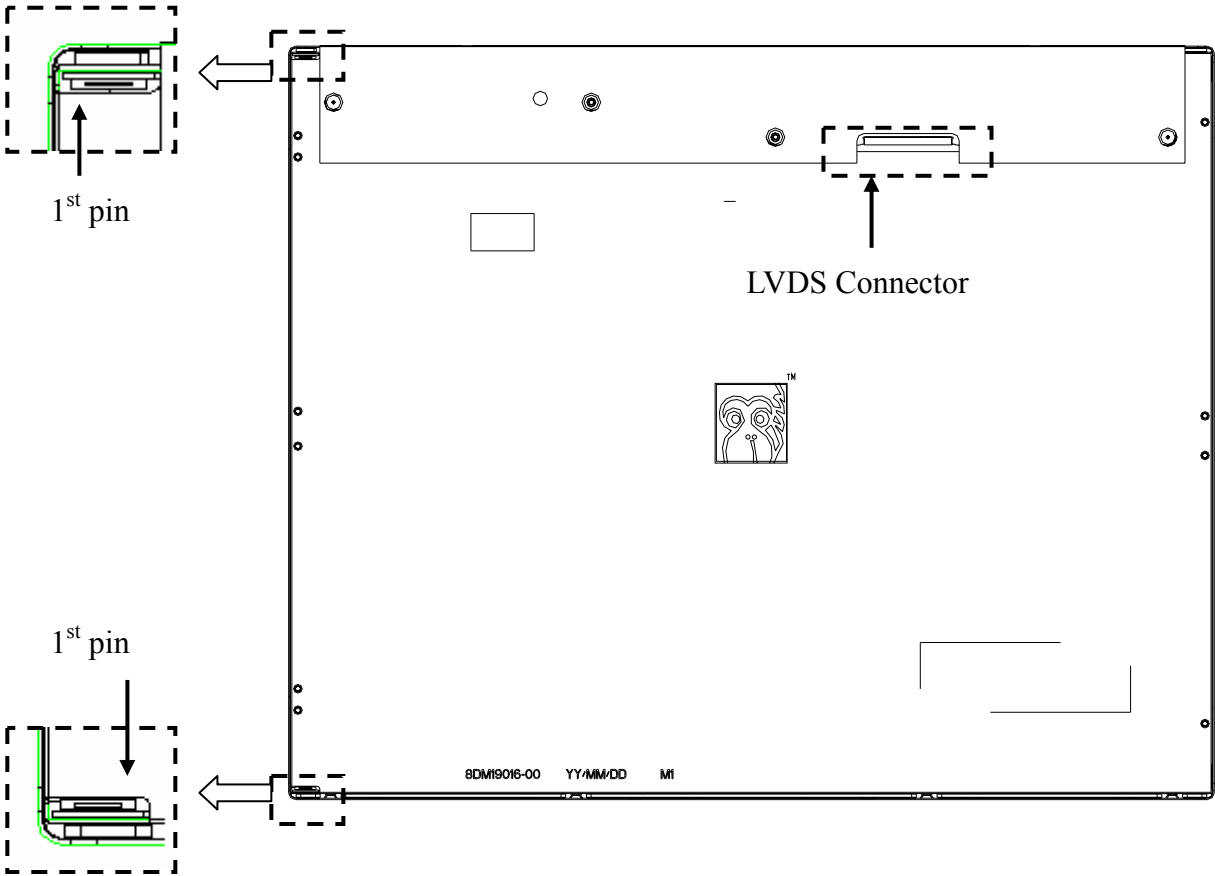
Upper FFC Pin Assignment:

Pin No	Symbol	Description
1	IRLED1	LED current sense for string 1
2	IRLED1	LED current sense for string 1
3	IRLED2	LED current sense for string 2
4	VLED	LED power supply
5	VLED	LED power supply
6	VLED	LED power supply
7	VLED	LED power supply
8	IRLED2	LED current sense for string 2
9	IRLED3	LED current sense for string 3
10	IRLED3	LED current sense for string 3

Lower FFC Pin Assignment:

Pin No	Symbol	Description
1	IRLED1	LED current sense for string 1
2	IRLED1	LED current sense for string 1
3	IRLED2	LED current sense for string 2
4	VLED	LED power supply
5	VLED	LED power supply
6	VLED	LED power supply
7	VLED	LED power supply
8	IRLED2	LED current sense for string 2
9	IRLED3	LED current sense for string 3
10	IRLED3	LED current sense for string 3

FFC pin position



Note: FFC specification:

Temperature=80°C , Voltage=60V , Pin No.=10 , 0.5Pitch, PET , HF

2. Absolute maximum ratings

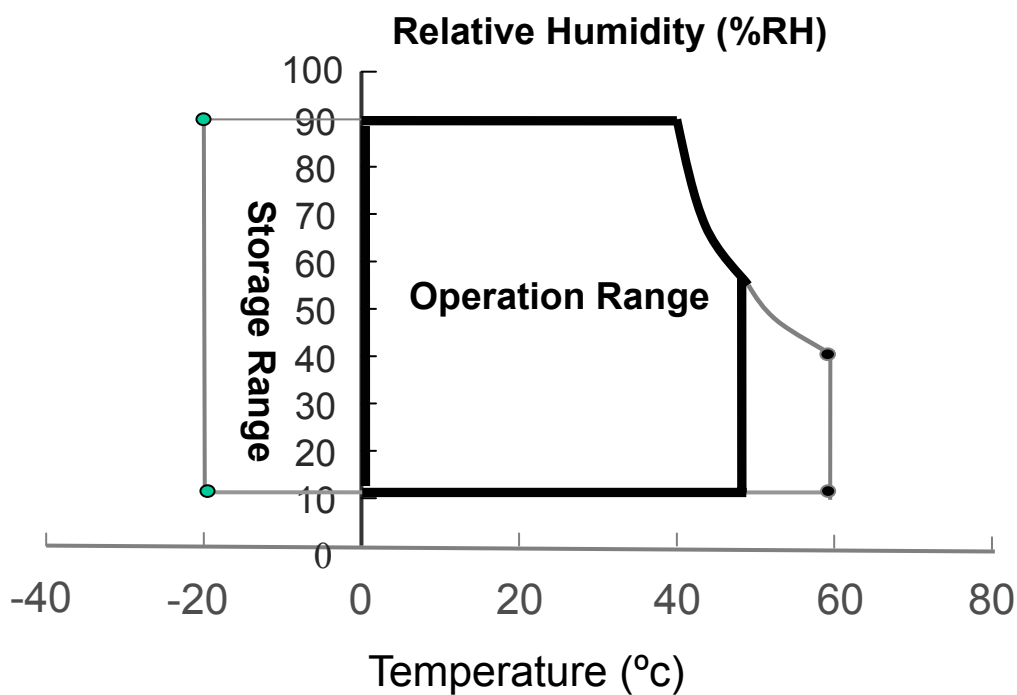
Ta=25±2°C

Parameter	Symbol	Values			Unit	Note
		Min.	Typ.	Max.		
Power voltage	Vcc	-0.3	-	6.0	V	At25°C
Input signal voltage	V _{LH}	-0.3	-	2.8	V	At25°C
Storage temperature	TST	-20	-	+60	°C	(1)
Operating Ambient temperature	Top	0	-	+50	°C	(1)
Operating Humidity	Hop	10	-	90	%RH	(1)
Storage Humidity	HST	10	-	90	%RH	(1)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. (Ta ≤ 40 °C).

(b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).



3. Electrical characteristics

a. Typical operating conditions

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Input Voltage		V_{CC}	4.5	5	5.5	V	
Permissive Power Input Ripple		V_{RF}	-	-	0.15	V	
Input Current	Black	I_{CC}	-	700	1000	mA	Note 1
	White	I_{CC}	-	500	700		Note 2
	Mosaic	I_{CC}	-	700	1000		Note 3
Rush Current		I_{Rush}	-	1.6	3	A	Note 4
Logic Input Voltage LVDS: IN+, IN-	Common Mode Voltage	VCM	-	1.2	-	V	
	Differential Input Voltage	VID	100	-	600	mV	
	Threshold Voltage (High)	VTH	-	-	100	mV	Note 5
	Threshold Voltage (Low)	VTL	-100	-	-	mV	Note 5

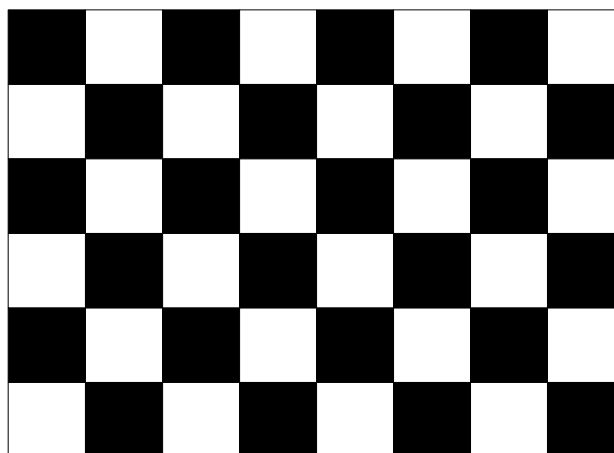
Note 1 : The specified current is under the $V_{CC} = 5V$, $25^{\circ}C$, $f_v = 60Hz$ (frame frequency) condition whereas black pattern is displayed.

Note 2 : The specified current is under the $V_{CC} = 5V$, $25^{\circ}C$, $f_v = 60Hz$ (frame frequency) condition whereas white pattern is displayed.

Note 3 : The specified current is under the $V_{CC} = 5V$, $25^{\circ}C$, $f_v = 60Hz$ (frame frequency) condition whereas mosaic pattern(black & white [8*6]) is displayed.

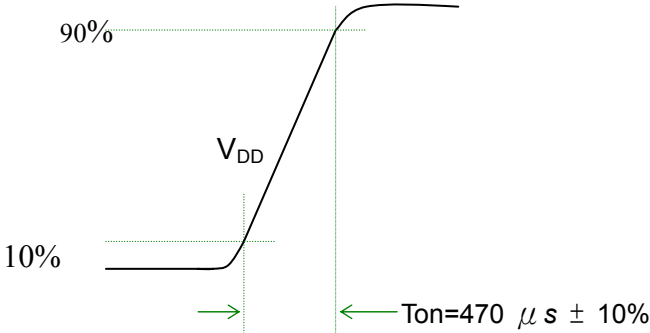
White : 255 Gray

Black : 0 Gray

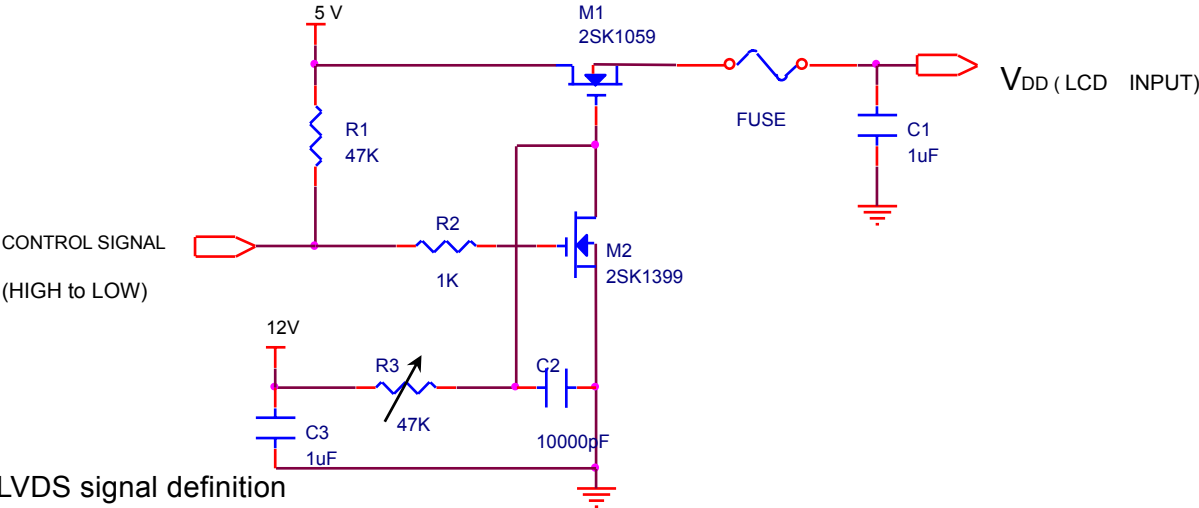


Note 4 : test condition :

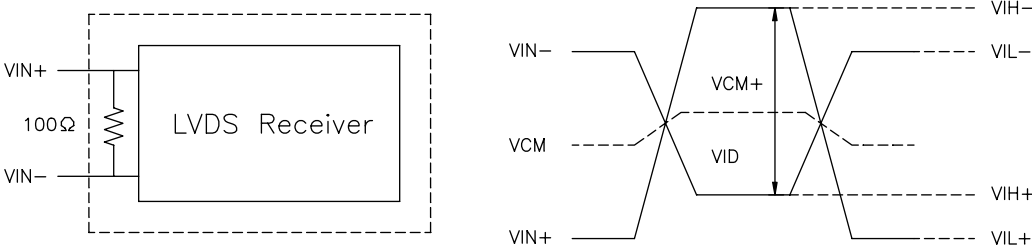
- (1) $V_{DD} = 5\text{ V}$, V_{DD} rising time = $470\ \mu\text{s} \pm 10\%$
- (2) Pattern: Mosaic pattern



(3) Test circuit



Note 5: LVDS signal definition



$VIN+$ = Positive differential DATA & CLK Input
 $VIN-$ = Negative differential DATA & CLK Input

$$VID = VIN+ - VIN- ,$$

$$\Delta VCM = | VCM+ - VCM- | ,$$

$$\Delta VID = | VID+ - VID- | ,$$

$$VID+ = | VIH- - VIH+ | ,$$

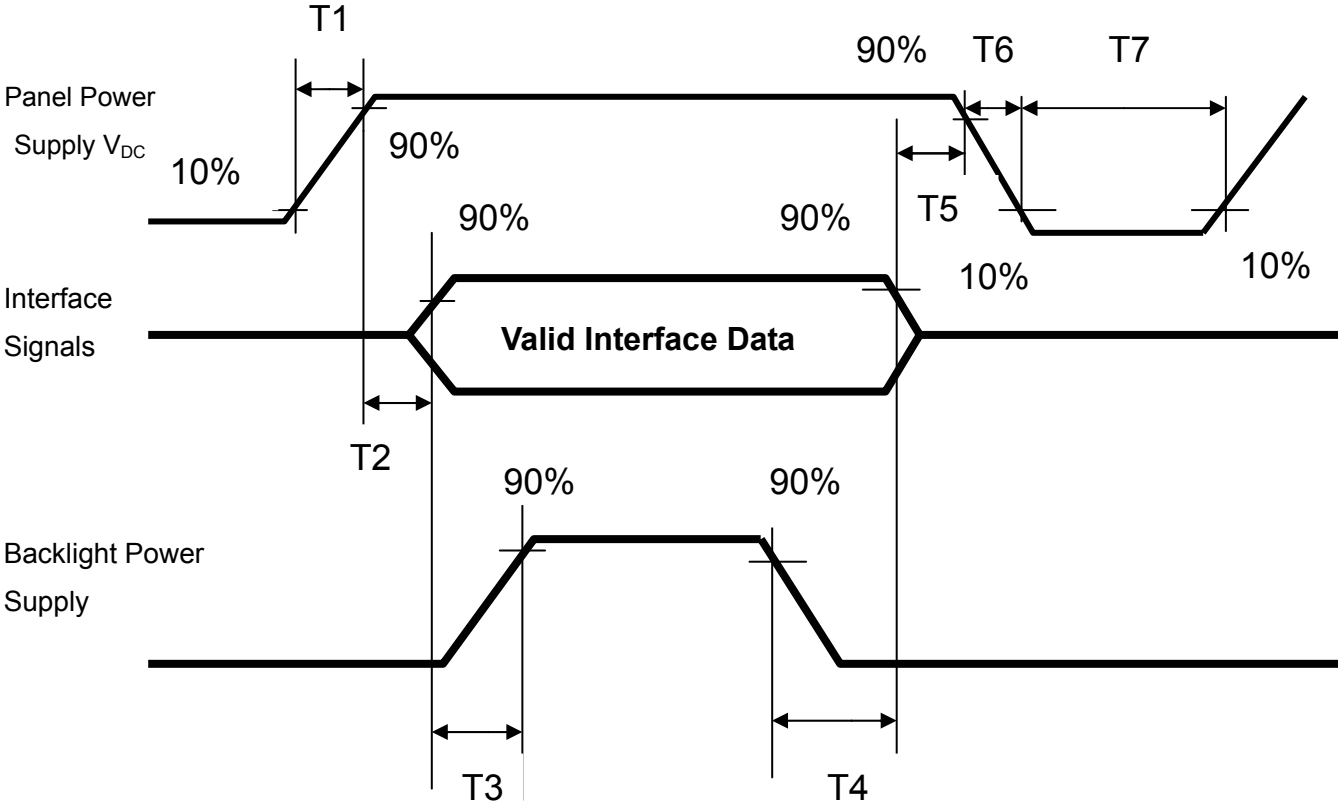
$$VID- = | VIL+ - VIL- | ,$$

$$VCM = (VIN+ + VIN-)/2 ,$$

$$VCM+ = (VIH+ + VIH-)/2 ,$$

$$VCM- = (VIL+ + VIL-)/2 ,$$

Note 6 : Power on sequence for LCD V_{DD}



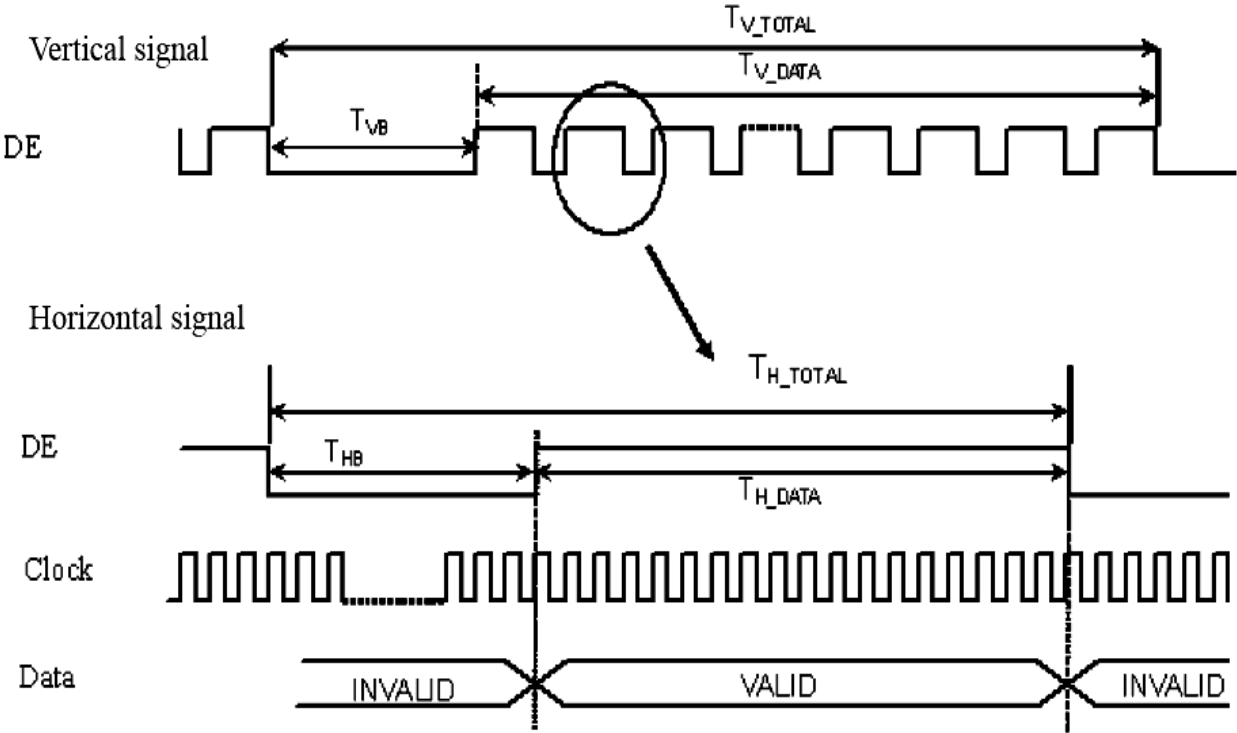
Parameter	Value			Unit
	Min	Typ	Max	
T1	0.1	-	10	ms
T2	0	30	50	ms
T3	200	250	-	ms
T4	100	250	-	ms
T5	0	20	50	ms
T6	0.1	-	50	ms
T7	1000	-	-	ms

c. Input signal timing

Support Input Timing Table

	Item	Description	Min.	Typ.	Max.	Unit
Clock	Dclk	period	14.71	18.52	22.22	nS
		frequency	45	54	68	MHz
Vertical	T_{V_TOTAL}	V total line number	1044	1066	1150	T_{H_TOTAL}
	T_{V_DATA}	Data duration	—	1024	—	T_{H_TOTAL}
	T_{VB}	V-blank	20	42	—	T_{H_TOTAL}
	f_V	frequency	50	60	75	Hz
Horizontal	T_{H_TOTAL}	H total pixel number	790	844	880	DCIk
	T_{H_DATA}	Data duration	—	640	—	DCIk
	T_{HB}	H-blank	150	204	—	DCIk

Note: Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low Logic level or ground. Otherwise, this module would operate abnormally.



d. Display Position

D(1, 1)	D(2, 1)	D(640, 1)	D(1279, 1)	D(1280, 1)
D(1, 2)	D(2, 2)	D(640, 2)	D(1279, 2)	D(1280, 2)
⋮		⋮	⋮	⋮
D(1, 512)	D(2, 512)	D(640, 512)	D(1279, 512)	D(1280, 512)
⋮		⋮	⋮	⋮
D(1, 1023)	D(2, 1023)	D(640, 1023)	D(1279, 1023)	D(1280, 1023)
D(1, 1024)	D(2, 1024)	D(640, 1024)	D(1279, 1024)	D(1280, 1024)

e. Backlight Unit

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Light Bar Input Voltage	V _{LED}	---	33	34	VDC	(Duty 100%)
Light Bar Input Current	I _{LED}	---	---	360	mADC	(1), (2), (3)
Power Consumption	P _{LED}	---	---	12.24	W	(4)
LED Life Time	LBL	---	30000	---	Hrs	(5)

Note (1): There are two Light Bars, and the specified current is input LED chip 100% duty current.

Note (2): The sensing current of each string is 60mA.

Note (3): Each light bar have three current sensing strings, so that each light bar input current is 180mA.

Note (4): $P_{LED} = I_{LED} \times V_{LED}$.

Note (5) : The life time is determined as the time at which luminance of the LED becomes 50% of the initial brightness or not normal lighting at $I_{LED}=360mA$ on condition of continuous operating at $25\pm 2^{\circ}C$.

C. Optical specifications

Item	Symbol	Condition	Specification			Unit	Remark
			Min.	Typ.	Max.		
Response time	Tr	$\theta = 0^\circ$	-	1.5	4	ms	Note 2
	Tf		-	3.5	6		
	Tr+Tf		-	5	10		
Contrast ratio	CR	$\theta = 0^\circ$	700	1000	-		Note 1,3
Viewing angle	Top	$CR \geq 10$	70	80	-	deg.	Note 1,3,5
	Bottom	$CR \geq 10$	70	80	-		
	Left	$CR \geq 10$	75	85	-		
	Right	$CR \geq 10$	75	85	-		
Brightness (Center)	Y_L		200	250	-	Nits	Note 1,4
Color chromaticity(CIE)	Wx	$\theta = 0^\circ$	-0.03	0.313	+0.03		Note 1
	Wy			0.329			
	Rx			0.635			
	Ry			0.350			
	Gx			0.322			
	Gy			0.623			
	Bx			0.153			
	By			0.065			
White uniformity (9)	δ_w		0.70	0.75	-		Note 1,6
Cross talk	Ct		-	-	2%		Note 7

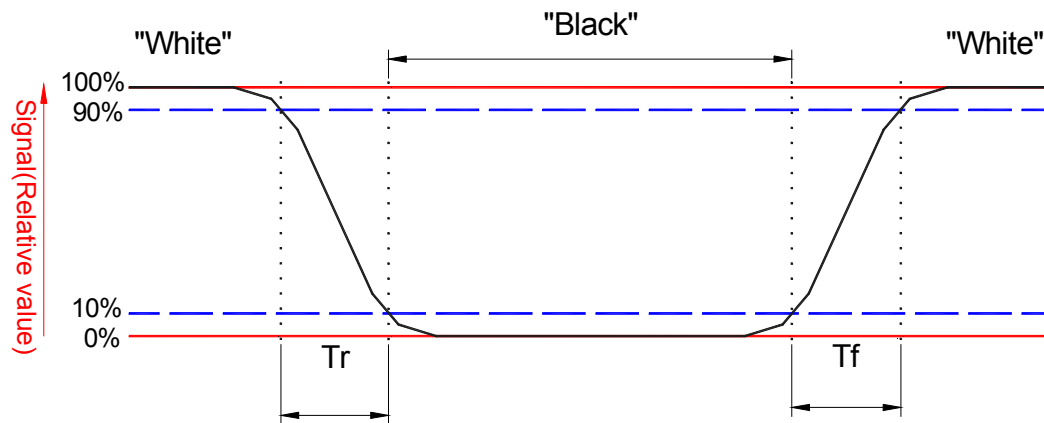
Note: 1. Ambient temperature = 25°C.

2. To be measured in dark room after backlight warm up 10 minutes.

Note 1: To be measured with a viewing cone of 2° by Topcon luminance meter BM-5A.

Note 2: Definition of response time:

The output signals of BM-7 are measured when the input signals are changed from “Black” to “White” (falling time) and from “White” to “Black” (rising time), respectively. The response time interval is between the 10% and 90% of amplitudes. Refer to figure as below.



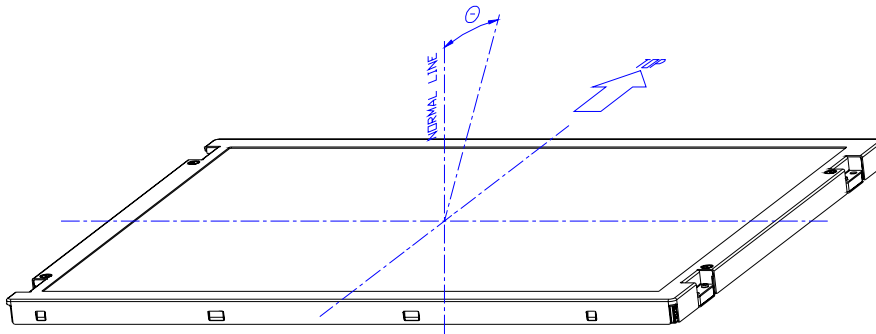
Note 3: Definition of contrast ratio:

Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

Note 4: Driving conditions for LED Light Bar: $I_{\text{LED}}=360\text{mA}$.

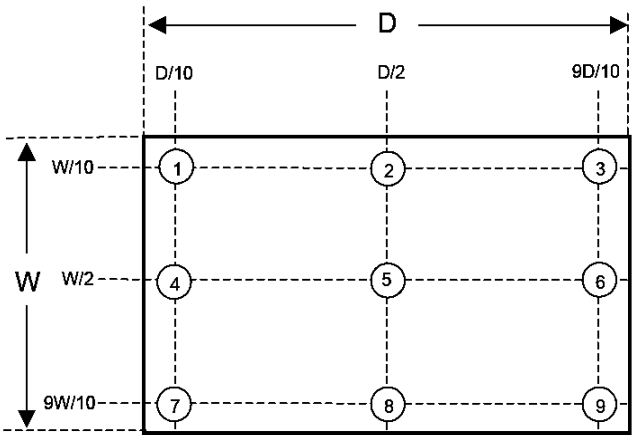
Note 5: Definition of viewing angle



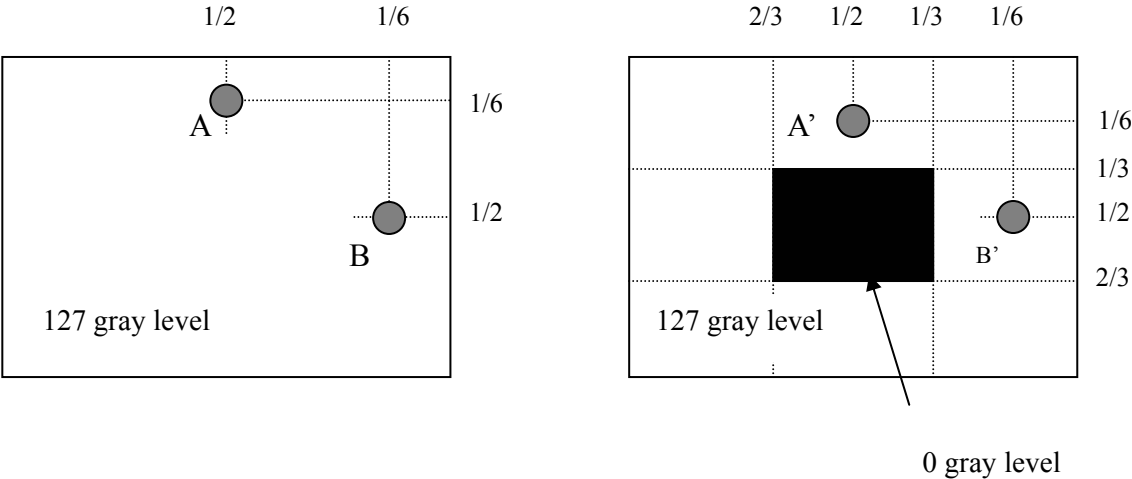
Note 6: Definition white uniformity:

Luminance are measured at the following nine points (P1~P9).

$$\delta_w = \frac{\text{Minimum Brightness of nine points (P1~P9).}}{\text{Maximum Brightness of nine points (P1~P9).}}$$

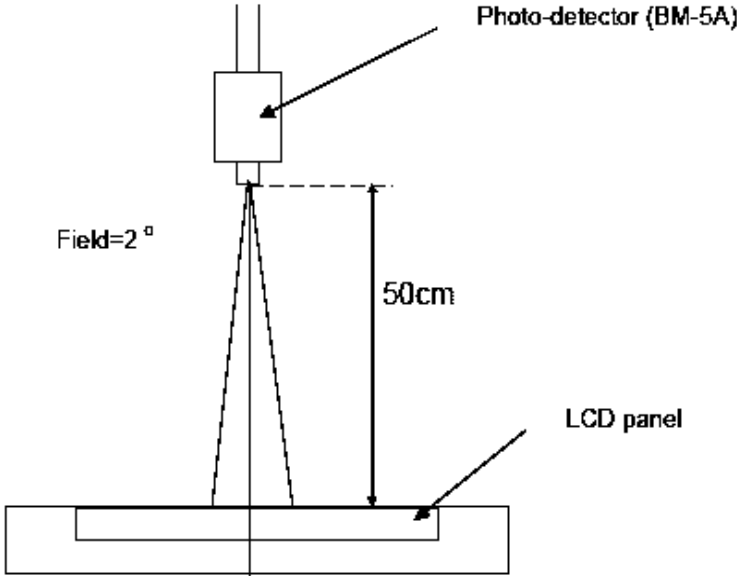


Note 7:



$|L_A - L_{A'}| / L_A \times 100\% = 2\% \text{ max.}$, L_A and $L_{A'}$ are brightness at location A and A'
 $|L_B - L_{B'}| / L_B \times 100\% = 2\% \text{ max.}$, L_B and $L_{B'}$ are brightness at location B and B'

Note 10: Optical characteristic measurement setup.



D. Reliability test items

Test Item	Test Condition	Judgment	Remark
High temperature storage	60°C, 240Hrs	Note 1	Note 2
Low temperature storage	-20°C, 240Hrs	Note 1	Note 2
High temperature & high humidity operation	40°C, 90%RH, 240Hrs (No condensation)	Note 1	Note 2
High temperature operation	50°C, 240Hrs	Note 1	Note 2
Low temperature operation	0°C, 240Hrs	Note 1	Note 2
Thermal Shock (non-operation)	-20°C~60°C 1Hr, 1Hr, 100cycles	Note 1	Note 2
Electrostatic discharge (ESD)	Contact: +/-8kV, 150pF(330ohms), 16points 10 times/1 point, 1 time/1 sec Air discharge: +/-15kV, 150pF(330ohms), 9 points, 10 times/1 point, 1 time/1 sec	Note 1	Note 2
Vibration (non-operation)	Vibration level : 1.5G Bandwidth : 10-300Hz Waveform : sine wave, sweep rate : 10min 30 min for each direction X, Y, Z (1.5 Hrs in total)	Note 1	Note 2
Mechanical Shock (non-operation)	Shock level : 50G, 11ms Waveform : Half sine wave Direction : ±X, ±Y, ±Z One time each direction	Note 1	Note 2
MTBF Demonstration	30,000 hours with confidence level 90%	Note 1	Note 3

Note1: Pass: Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

Note2: Evaluation should be tested after storage at room temperature for two hours.

Note 3: The MTBF calculation is based on the assumption that the failure rate distribution meets the Exponential Model

E. Safety

(1) Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

(2) Materials

a. Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible InnoLux Toxicologist.

b. Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process. The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

c. Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

F. Display quality

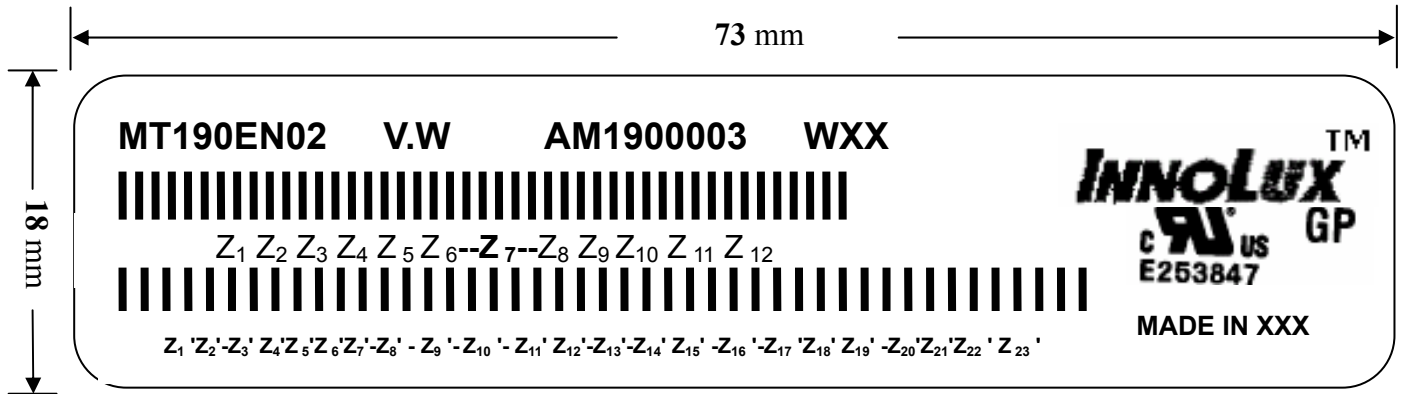
The display quality of the color TFT-LCD module should be in compliance with the Innolux's Incoming inspection standard.

G. Handling precaution

The Handling of the TFT-LCD should be in compliance with the Innolux's handling principle standard.

H. Label

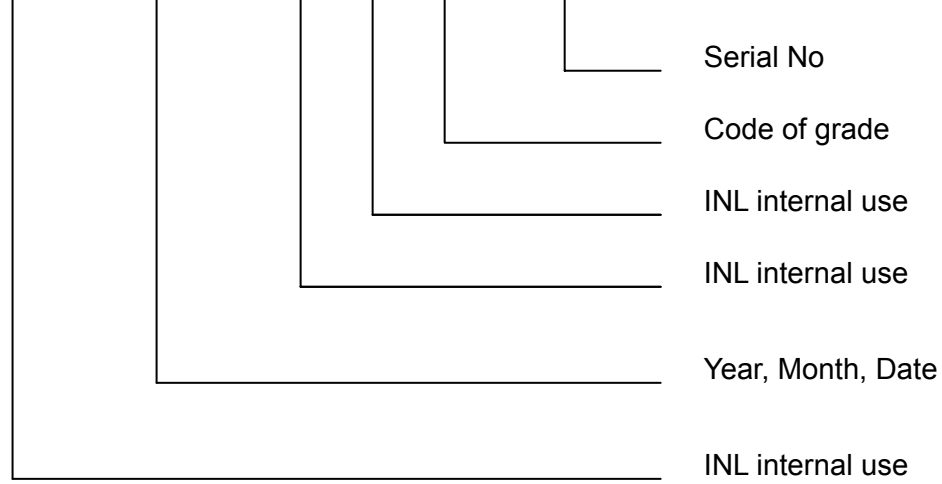
(1) Module Label



(a) Model Number: MT190EN02

(b) Version: V.W

(c) Serial ID I: Z₁ Z₂ Z₃ Z₄ Z₅ Z₆ Z₇ Z₈ Z₉ Z₁₀ Z₁₁ Z₁₂

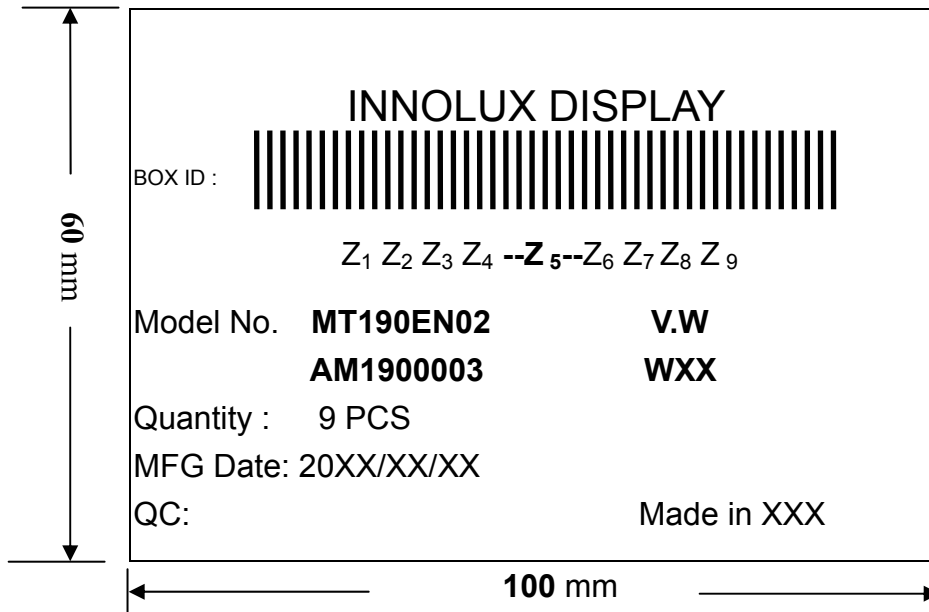


Serial ID includes the information as below:

1. Manufactured Date: Year: 0~9, for 2000~2009
2. Month: 1~9 & A~C for Jan.~Dec.
3. Date: 1~9 & A~Z (exclude I, O, Q, U) for 1st~31th
4. Code of grade: 1, 2, 3, 5, E
5. Serial No: Module manufacture sequence no

(d) Serial ID II (INL internal use)

(2) Carton Label

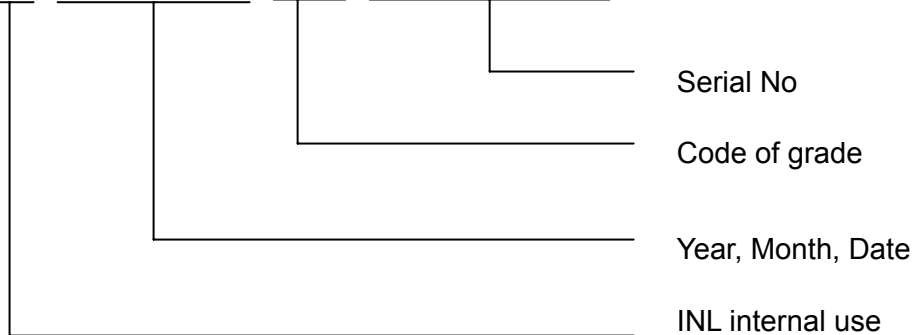


(a) Model Number: MT190EN02

(b) Version: V.W

(c) Packing quantity: 9 pcs

(d) Serial ID: Z₁ Z₂ Z₃ Z₄ Z₅ Z₆ Z₇ Z₈ Z₉



Serial ID includes the information as below:

(a) Manufactured Date: Year: 0~9, for 2000~2009

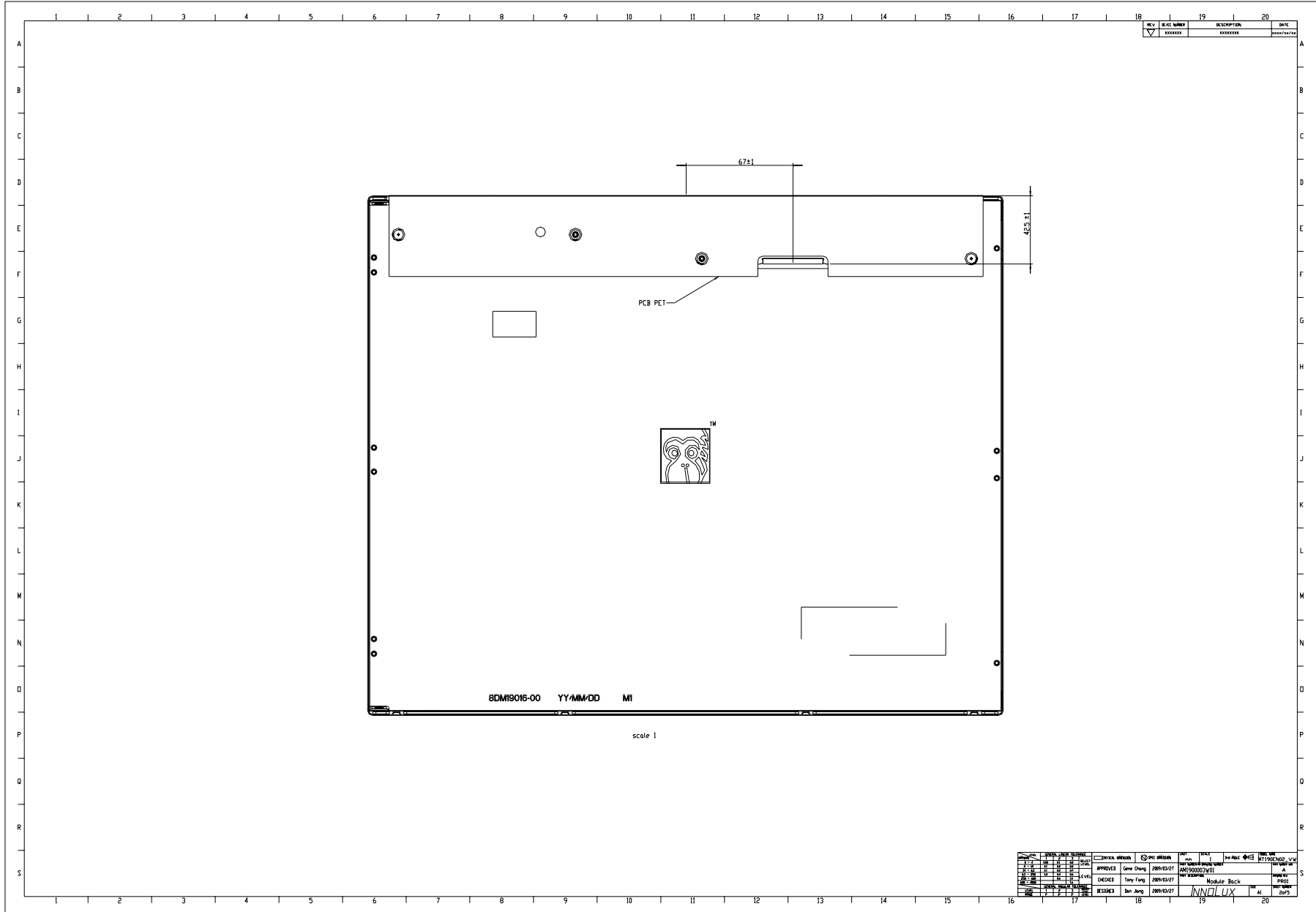
Month: 1~9 & A~C for Jan.~Dec.

Date: 1~9 & A~Z (exclude I, O, Q, U) for 1st~31th

(b) Code of grade: 1, 2, 3, 5, E

(c) Serial No: Module packing sequence no

I. Mechanical drawing



REV	DATE	DESCRIPTION	DATE
1	2019/01/16	PCB LAYOUT	2019/01/16

SDM19016-00 YY/MM/DD MI

scale 1

NO.	REV.	DATE	DESCRIPTION	BY	CHK.	APP.	DATE
1	1	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
2	2	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
3	3	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
4	4	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
5	5	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
6	6	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
7	7	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
8	8	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
9	9	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
10	10	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
11	11	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
12	12	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
13	13	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
14	14	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
15	15	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
16	16	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
17	17	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
18	18	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
19	19	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16
20	20	2019/01/16	PCB LAYOUT	Gene Chang			2019/01/16