

## Hitachi LMG5320XUFC

<b>Model</b>	LMG5320XUFC
<b>Manufacturer</b>	Hitachi
<b>Resolution</b>	640x480
<b>Number of Colors</b>	Mono
<b>Technology</b>	STN-Mono
<b>Interface</b>	Digital

<b>Filename</b>	LC_5320.XXX
<b>Bios code</b>	EJ
<b>Bios/Table version</b>	V1.0/V1.0
<b>Used Controller</b>	548
<b>Date of adaption</b>	1.99
<b>Size</b>	7,2"

### Notes

LCD signal	LCD connector pin	Controller signal
CP	3	CLK
Frame	1	FLM
Load	2	LP
VEE*	7	EVEE*
VDD	4,5	EVDD
VSS	6	GND
LD0	12	P7
LD1	13	P6
LD2	14	P5
LD3	15	P4
UD0	8	P3
UD1	9	P2
UD2	10	P1
UD3	11	P0

Voltage description	Voltage level	Maximum current	Note
VDD	+5V DC	18mA	
VEE	-22V DC	16mA	

\* The voltages for the lcd have to be switched in correct power-up and -down sequence. Use EVEE pin on the board to switch the external generated VEE. On some boards, this output is in TTL level only. An external fet or relais is needed for save switching of VEE. **Never connect VEE direct to the board or LCD!** Ask Digital-Logic LCD-Support for further informations.