

Kaohsiung Opto-Electronics Inc.

FOR MESSRS: DATE: Apr 11 ¹

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX09D30VM1CBA

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ACCEPTED BY:	PROPOSED BY: Lenther
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KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2701-TX09D30VM1CBA-4	PAGE	1-1/1
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RECORD OF REVISION

DATE	SHEET No.			91	JMMARY						
Jan.14,'11	7B63PS 2709 –	9.OUT	LINE DIMENS		O. 1411411/ 11 X I						
	TX09D30VM1CBA-2 Page 9 – 1/1	The connectors on FPC changed.									
	7B64PS 2712 – TX09D30VM1CBA-2	Added	REVISION (RE	V.) CONTROI	_						
	Page 12 – 1/1		REV No.		ITEM		NOTE	1			
			A A		-		- NOTE				
			В	The connec	tors on FPC change	d	-				
May 01,'12	All pages	Company name changed:									
		KAO	HSIUNG HIT	ACHI ELEC	TRONICS CO.,LTI	D.					
		KAO	HSIUNG OP	↓ TO-ELECTR	ONICS INC.						
Apr 11,'13	7B64PS 2704 – TX09D30VM1CBA-4	4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS									
	Page 4 – 2/2		ITE	N /	OPERAT	ING					
			11 =	IVI	Min.		Max.				
			Ambient Te	mperature	-20°C		70℃				
			<u></u>				ا ا				
			ITE	M	OPERAT						
			Ambient Te		Min. -30°C		Max. 80°C				
		<u> </u>									

3.GENERAL DATA

The specifications are applied to the following TFT-LCD (Transmissive with Micro Reflectance) module with Back-light unit.

Note: Driving circuit for LED is not built in this module.

(1) Part Name TX09D30VM1CBA

(2) Module Dimensions 64.0(W)mm x 86.0(H)mm x 3.12(D)mm typ.

(Except FPC Area)

(3) Effective Display Area 53.64(W)mm x 71.52(H)mm (Diagonal:9cm)

(4) Dot Pitch 0.0745mm x 3(R,G,B)(W) x 0.2235(H)mm

(5) Resolution 240 x 3(R,G,B)(W) x 320 (H) dots

(6) Color Pixel Arrangement R,G,B Vertical Stripe

(7) LCD Type Transmissive Color TFT LCD (Normally White)

(8) Display Type Active Matrix

(9) Number of Colors 262^K Colors (R,G,B 6 Bit Digital each)

(10) Backlight Light Emitting Diode (LED) x 6

(11) Weight 36g

(12) Interface 50 pin C-MOS

(13) Viewing Direction 6 O'clock (The direction it's hard to be discolored)

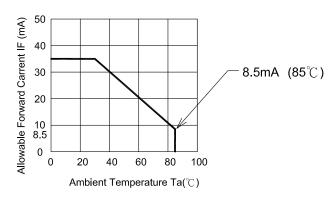
4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF LCD

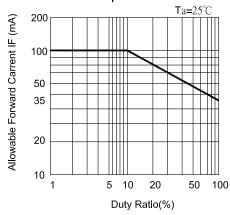
ITEM			SYMBOL	MIN.	MAX.	UNIT	REMARKS
Power	Supply for Logic		VCC	-0.3	3.6	V	
Power Supply Voltage for Source Driver and Vcom		VDH	-0.3	6.0	V		
Input Voltage		Vi	0	VCC	V	Note1	
0-1-	Power Supply for Gate	High	Vgн	-0.3	VGL+20	V	
Gate		Low	Vgl	- 9	0.3	V	
	Forward Current		IF	-	35	mA	Note2
LED	LED Pulse Forward Current		IFP	-	100	mA	Note3
Reverse Voltage			VR	-	5	V	
Sta	tic Electricity		-	-	±2	kV	Note4,5

Note 1: Hsync, Vsync, DCLK, R0~R5, G0~G5, B0~B5

2:



3:IFP Conditions: pulse width \leq 10ms and Duty \leq 1/10



4: Make certains you are grounded when handling LCM.

5 : Testing condition : 200pF - 0Ω , 25° C - 70%RH.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		S	TORAGE	REMARKS	
	Min.	Max.	Min.	Max.	REWARKS	
Ambient Temperature	-30°C	80°C	-30°C	80℃	(Note 2,3,6,7,9,10)	
Humidity	(Note 1)		(Note 1)		Without condensation	
Vibration	_	2.45m/s ²	_	11.76m/s ²	(Note 4,5)	
VIDIALIOIT	-	(0.25G)	-	(1.2G)	(Note 4,5)	
Shock		29.4m/s ²		490m/s ²	(Note 5,8)	
SHOCK	-	(3G)	1	(50G)	(Note 5,8)	
Corrosive Gas	Not Ac	ceptable	Not	Acceptable		

Note 1 : Ta ≤ 40°C : 85%RH max.

 $Ta > 40^{\circ}C$: Absolute humidity must be lower than the humidity of 85%RH at 40°C.

Note 2 : For storage condition Ta at -30° C < 48h, at 80° C < 100h.

For operating condition Ta at -20°C < 100h

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4 : 5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7 : Only operation is guarantied at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

Note 8: Pulse Width: 10ms

Note 9: This is panel surface temperature, not ambient temperature.

Note 10: When LCM be operated at high temperature, the life time of LED will be reduced.

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C, VSS=0V

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for logic	VCC	-	2.5	3.0	3.3	٧
Power Supply Voltage for Source Driver and Vcom	VDH	-	8.3	8.45	8.7	
Input voltage for logic	Vi	"H" level	0.8VCC	ı	VCC	V
(Note 1)	VI	"L" level	VSS	1	0.2VCC	V
	VGH		16.2	16.5	17.0	
Power Supply for LCD	VGL	-	-8.35	-8.05	-8.0	V
	VCOM	VCOM-VSS	-	2.4	-	
	ICC	VCC-VSS=3.0V	-	0.22	-	
Power Supply Current	IDH	VDH-VSS=8.45V	-	3.3	-	mΛ
(Note 2)	IGH	VGH-VSS=16.5V	-	0.23	-	mA
	IGL	VGL-VSS=-8.05V	-	0.17	-	
Vsync Frequency	fV	-	54	60	68	Hz
Hsync Frequency	fH	-	18.57	20.77	22.73	kHz
DCLK Frequency	fCLK	-	5.0	5.6	6.5	MHz

Note 1: DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

Note 2 : fV=(60)Hz, Ta=25°C, Pattern used as display pattern : Black.

Note 3: Need to made sure of flickering and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF BACK LIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
LED Input Voltage	VF	IF=20mA	ı	3.2	3.5	V	LED / Part
LED Forward Current	IF	-	-	20	25	mA	LED / Part
LED Reverse Current	IR	VR=5V	ı	ı	50	μA	LED / Part

6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD (BACK LIGHT ON)

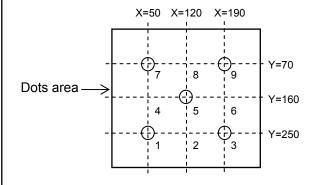
Ta=25°C

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
Brightness	Brightness		φ=0° θ=0°	360	430	-	cd/m ²	Note1
Uniformity		-	φ=0° θ=0°	70	-	-	%	Note2,3,4
		θх	φ=0°,K≧5.0	-	80	-		
Viewing Angle		θх	<i>φ</i> =180°,K≧5.0	-	80	-	doa	Noto F 6
Viewing Angle		θу	φ=90°,K≧5.0	-	80	-	deg	Note5,6
		θу	<i>φ</i> =270°,K≧5.0	-	50	-		
Contrast Ratio		К	φ=0° θ=0°	180	300	-	-	Note4
Response Time (r	Response Time (rise-fall)		φ=0° θ=0°	-	30	-	ms	Note8
Color Tone	Dod	х		0.54	0.59	0.64	-	
(Primary Color)	Red	у		0.29	0.34	0.39	-	
	Croon	х		0.31	0.36	0.41	-	
	Green	у	/ _0° 0_0°	0.51	0.56	0.61	-	Note 4
	Dive	х	$\phi = 0^{\circ} \theta = 0^{\circ}$	0.10	0.15	0.20	-	Note4
	Blue	у		0.08	0.13	0.18	-	
	\\/hita	х		0.28	0.33	0.38	-	
	White	у		0.29	0.34	0.39	-	

Note 1: Active area center

Note 2: Driving Condition Display Pattern : White Raster LED Current: 20mA / Part Measurement of the following

5 places on the display.

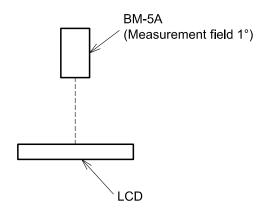


(Measurement condition : KOE standard)

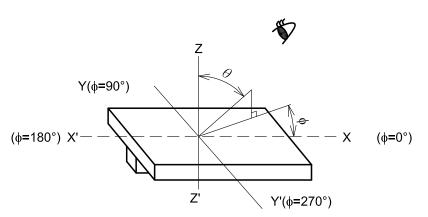
Note 4~7 : See page 6-2/2

Note 3: Definition of the brightness uniformity

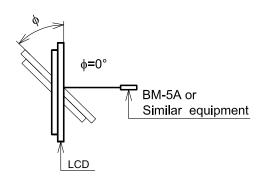
Note 4: Measurement Condition



Note 5 : Definition of θ and ϕ (Normal) Viewing direction

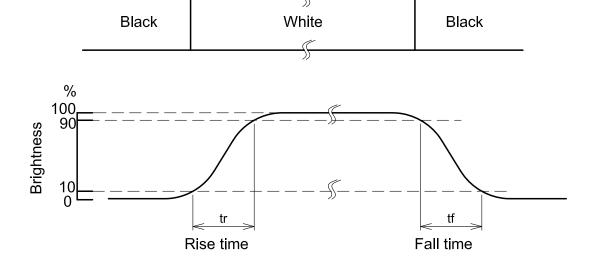


Note 6: Definition of Viewing angle



Note 7: Definition of contrast "K" $K = \frac{White Brightness}{Black Brightness}$

Note 8: Definition optical response time



7. BLOCK DIAGRAM (REFERENCE ONLY) I/F R0[5;0] G0[5;0] B0[5;0] Driver (Source + Gate) FLCK Hsync TFT_LCD LCLK PCLK Vsync **CPLD** VDD DEN DOTCLK CPU VCC(3V) VCC Power VDH(8.45V) IC for VGH(+16.5V) **LCD** VGL(-8.05V) **MOUT VCOM** SHEET NO. KAOHSIUNG OPTO-ELECTRONICS INC. 7B64PS 2707-TX09D30VM1CBA-4 PAGE 7-1/1

8. INTERFACE TIMING

8.1 INTERFACE TIMING

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARKS
DOTCL K avala time	tCYCD	100	-	-	20	1 trans
DOTCLK cycle time	ICTOD	50	1	-	ns	3 trans
DOTCLK low level pulse width	PWDL	20	-	-	ns	-
DOTCLK high level pulse width	PWDH	20	-	-	ns	-
VSYNC setup time	tVSYNCS	0	-	1	clock	-
HSYNC setup time	tHSYNCS	0	1	1	clock	-
(ENABLE setup time)	tENS	20	-	-	ns	*
(ENABLE hold time)	tENH	20	1	-	ns	*
RGB data setup time	TPDS	20	1	-	ns	-
RGB data hold time	TPDH	20	-	-	ns	-
DOTCLK/VSYNC/HSYN C rising edge, falling edge times	trgbr / trgbf	-	-	20	ns	-

^{*}for the reference

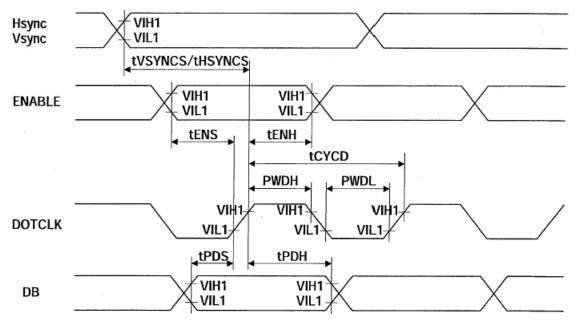


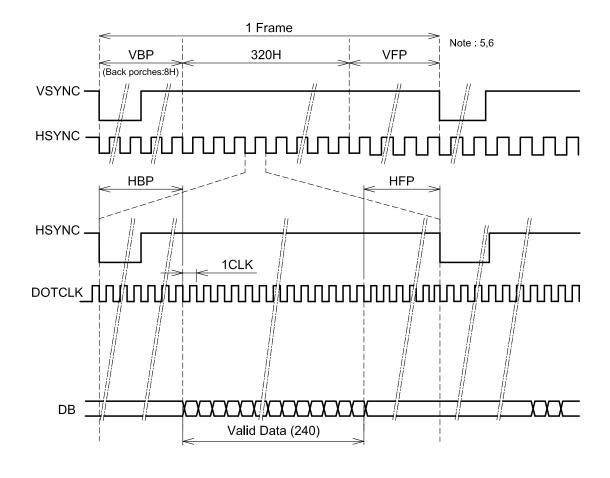
Fig1. RGB Interface timing

8.2 TIMING CHART

	SYMBOL	MIN.	TYP.	MAX.	UNIT
Back porch for Horizontal	HBP	-	12	1	Clock
Front porch for Horizontal	HFP	15	18 Note1	21	Clock
Back porch for Vertical	VBP	-	8 Note3	1	HSYNC
Front porch for Vertical	VFP	17	20 Note2	22	HSYNC

Note 1 : (DOTCLK total) - ((Valid data period for Horizontal) + (HBP))

2: (HSYNC total) - ((Active Area period) + VBP)



3 : Note about VSYNC timing setting
Check the timing chart of VSYNC and HSYNC , If timing setting is not set as Fig1,
it must be set as Fig2 .

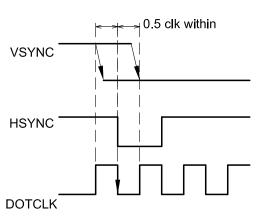


Fig1. VSYNC timing

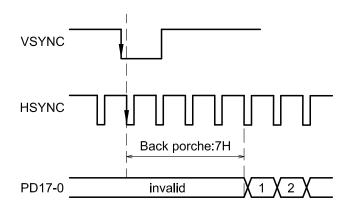
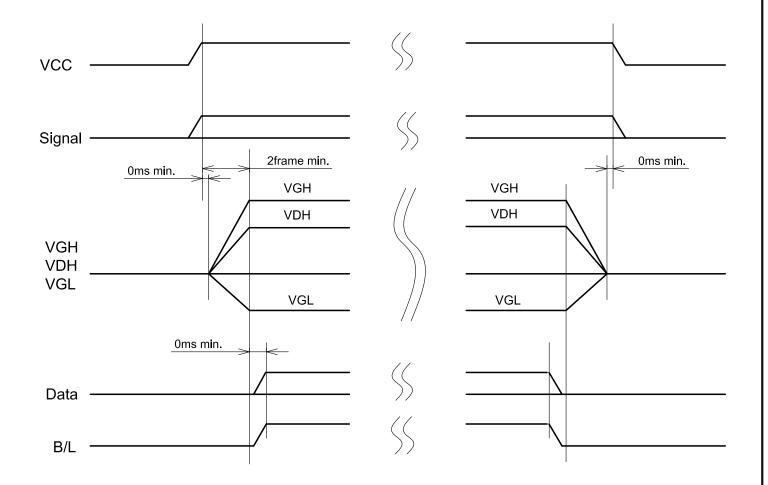


Fig2. Vertical back porch regulation

- 4: The DOTCLK signal must be supplied consecutively.
- 5 : Front and back porches (VBP , VFP) must be set before and after the display operation period.
- 6: The front porch period continues until the next input of VSYNC signal.

8.3 POWER ON/OFF SEQUENCE



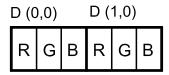
8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

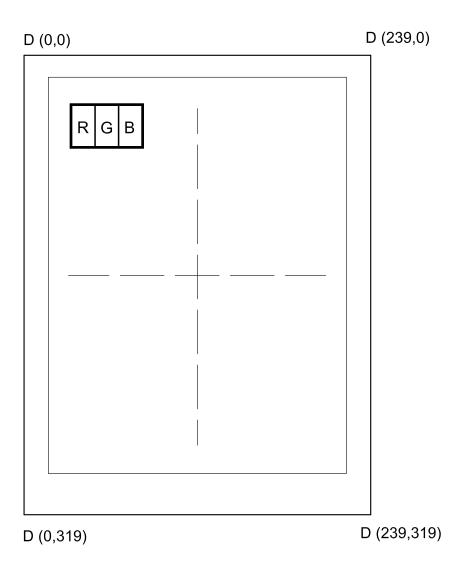
8.4.1 Display Colors

		F	Red	Data	а			G	reen	Da	ıta			Е	Blue	Dat	а		
Input	color	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
Input	COIOI	MSI	3			L	SB	MS	В			L	SB	MS	В			L	SB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	•	:	:	:	:	:	:	•	:	:	:	:	:	:
Neu	:	:	:	:	:	•	:	:	:	:	•	:	•	:	:	•	•	:	:
	Red(2)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	:	:	•	:	•	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(2)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	·	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Diac	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

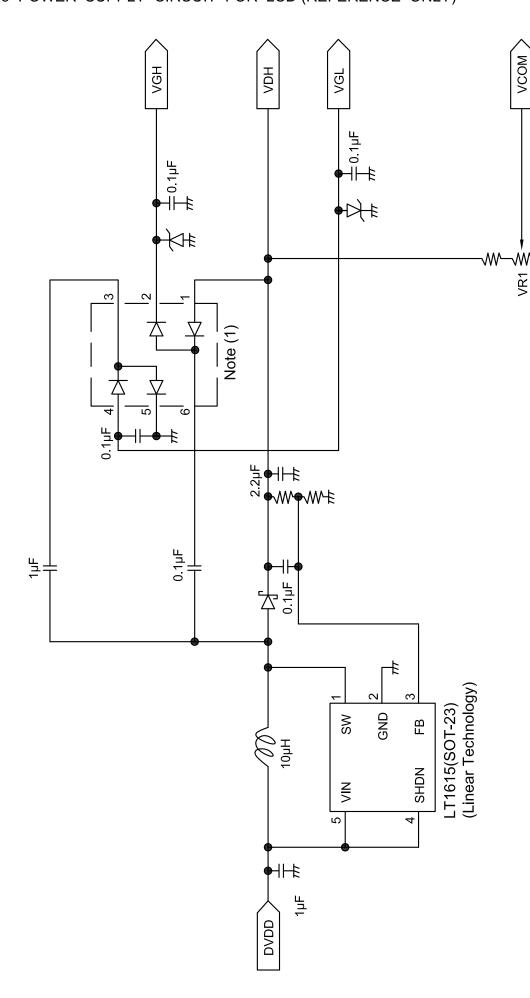
SHEET	
NO.	

8.4.2 Data address



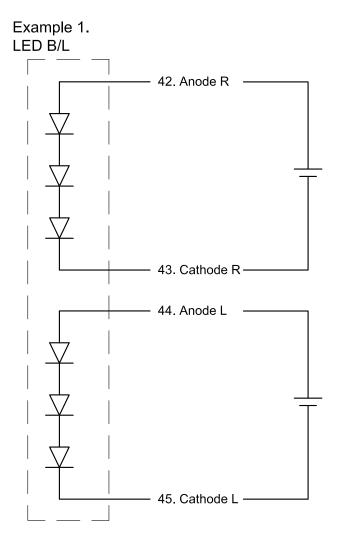


8.5 POWER SUPPLY CIRCUIT FOR LCD (REFERENCE ONLY)

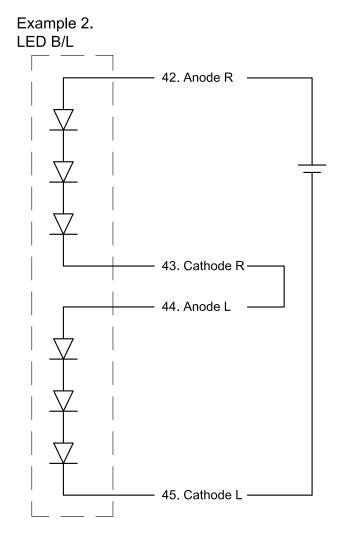


Note 1: Must uses shottky barrier diode, and forward voltage is 0.4V (typ.)

8.6 POWER SUPPLY CIRCUIT FOR LED BL (REFERENCE ONLY)



KAOHSIUNG OPTO-ELECTRONICS INC.

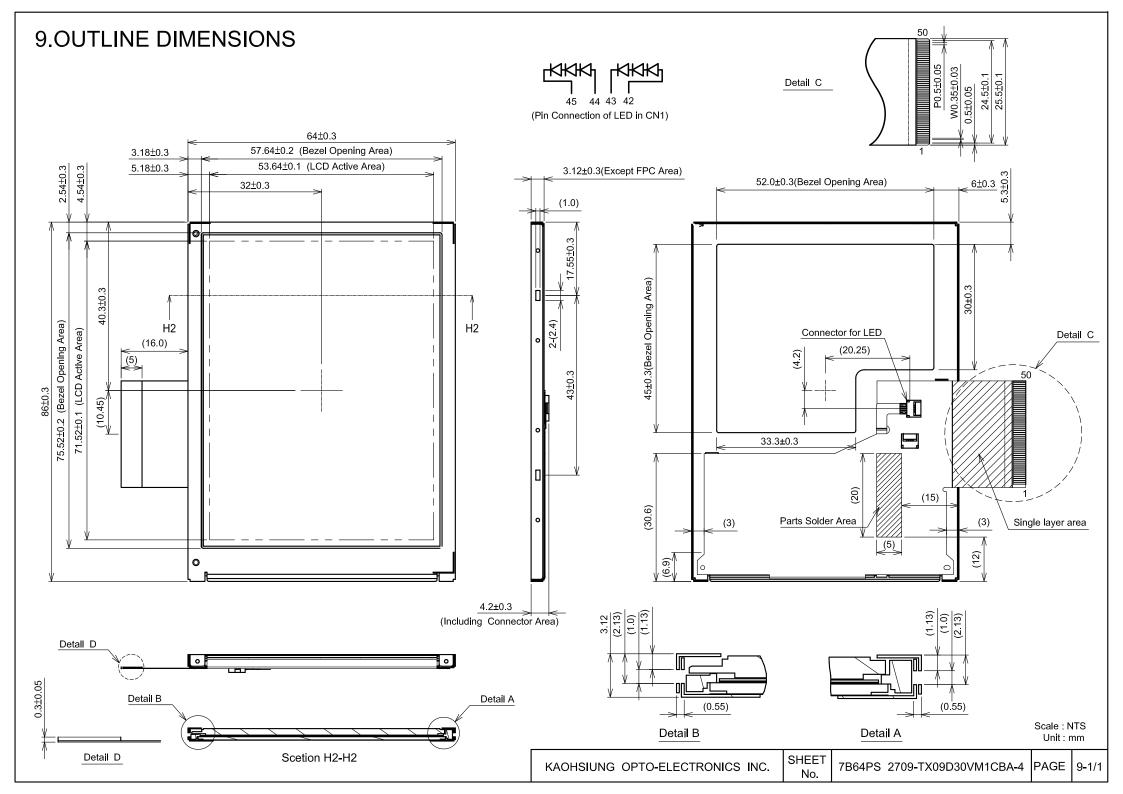


8.7 INTERNAL PIN CONNECTION

Suitable connector: FH12-50S-0.5P

	Oditable	00111100101 . 1 11112 000 0.01	
No	SYMBOL	FUNCTION	No
1	VGH	Power Supply for Gate Driver (High)	26
2	NC	No Connection by FPC Side	27
3	NC	No Connection by FPC Side	28
4	NC	No Connection by FPC Side	29
5	GND	Ground	30
6	VGL	Power Supply for Gate Driver (Low)	31
7	VSS	Ground	32
8	Hsync	Horizontal Sync Signal	33
9	R0	Red Data (LSB)	34
10	R1	Red Data	35
11	R2	Red Data	36
12	R3	Red Data	37
13	R4	Red Data	38
14	R5	Red Data (MSB)	39
15	G0	Green Data (LSB)	40
16	G1	Green Data	41
17	G2	Green Data	42
18	G3	Green Data	43
19	G4	Green Data	44
20	G5	Green Data (MSB)	45
21	В0	Blue Data (LSB)	46
22	B1	Blue Data	47
23	B2	Blue Data	48
24	В3	Blue Data	49
25	B4	Blue Data	50

No	SYMBOL	FUNCTION
26	B5	Blue Data (MSB)
27	NC	No Connection by FPC Side
28	Vsync	Vertical Sync Signal
29	NC	No Connection by FPC Side
30	DOTCLK	Dot Clock Signal
31	NC	No Connection by FPC Side
32	NC	No Connection by FPC Side
33	VDH	Power Supply for Source Driver
34	VDH	Power Supply for Source Driver
35	NC	No Connection by FPC Side
36	NC	No Connection by FPC Side
37	VCC	Power Supply for Logic
38	VCC	Power Supply for Logic
39	VCOM	Common Voltage
40	VCOM	Common Voltage
41	VSS	Ground
42	Anode R	LED Power Supply (+)
43	Cathode R	LED Power Supply (-)
44	Anode L	LED Power Supply (+)
45	Cathode L	LED Power Supply (-)
46	VSS	Ground
47	NC	No Connection by FPC Side
48	NC	No Connection by FPC Side
49	NC	No Connection by FPC Side
50	NC	No Connection by FPC Side

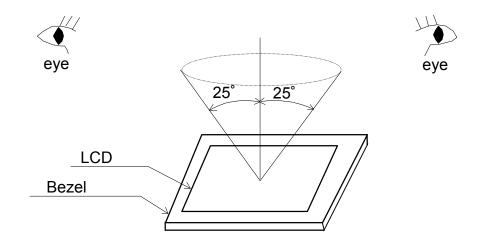


10. APPEARANCE STANDARD

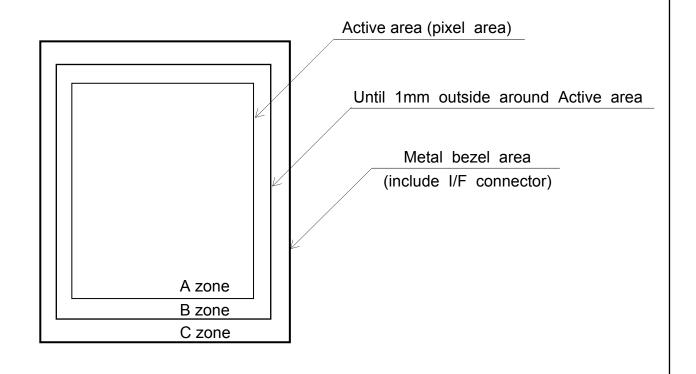
10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the following condition.

- (1) The inspection should be done in a dark room.(More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure. Viewing angle ≤ 25°



10.2 DEFINITION OF ZONE



10.3 APPEARANCE SPECIFICATION

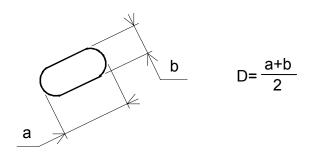
(1)LCD Appearance

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and KOE) will discuss the matter in detail.

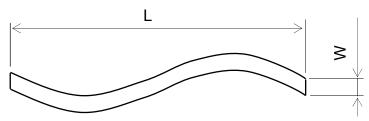
No.	ITEM	CRITERIA						
	Scratches	Length L(mm)	,			Maximum number acceptable	ZONE	
		L≦2.0		W≦0.	03	ignored	A,B	
				3 <w≦0.05< td=""><td>4</td><td></td></w≦0.05<>		4		
				05 <w< td=""><td>none</td><td></td></w<>		none		
	Dent		Seriou	us one is	not allo		Α	
	Wrinkles in Polarizer		Seriou	us one is	not allo	owed	Α	
	Bubbles	Average	diamete	er	N	laximum number		
			nm)			acceptable		
			0.3			2	A	
		0.3	< D			none		
	Stains		Filam	entous	(Line sh	· '		
	Foreign	Length		Width		Maximum number	A,B	
	Materials	L(mm)		W(mm)		acceptable		
	Dark spot	L<2.0		W≦0.05 4		_		
L	Dark Spot	L≦1.0	0.05 < W ≦ 0.1 2 Round(Dot shape)					
		A	-					
С		Average diameter D(mm)			N			
		D			A,B			
D		0.15 <d< td=""><td></td><td colspan="3">6 4</td></d<>		6 4				
		0.2 <d< td=""><td colspan="3">none</td><td></td></d<>	none					
		The total	Filamentous + Round=9			1		
		Those wiped out easily are acceptable						
	Dot Defect					Maximum		
						number		
						acceptable		
		Sparkle mod	е		dot	4		
					dots	2(sets)		
		DI I	1		<u>otal</u>	4	A , B	
		Black mode	;		dot	4		
					dots	2(sets)	-	
		Charles		Total		4	-	
		Sparkle mode & Black mode		2 dots		2(sets)		
				To	otal	6		

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2710-TX09D30VM1CBA-4	PAGE	10-2/3	
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Note 1: Definition of average diameter (D)



Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

(a) Dot Defect: Defect Area > 1/2 dot

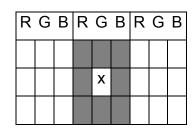
(b) Sparkle mode: Brightness of dot is more than 30% at Black raster.

(c) Black mode: Brightness of dot is less than 70% at R.G.B raster.

(d) 1 dot: Defect dot is isolated, not attached to other defect dot.

(e) N dot: N defect dots are consecutive.

(N means the number of defect dots.)



2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



- (f) Counting definition of adjacent dots(1 sets) : same as 1 dot defect.
- (g) Those wiped out easily are acceptable

11. PRECAUTION IN DESIGN

11.1 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc.

And don't touch I/F pins directly.

11.2 HANDLING PRECAUTIONS

(1) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use:

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

(2) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly.

Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.

- (3) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (4) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.

When you need to take out the LCD module from some place at low temperature for test, etc.

It is required to be warmed them up to temperature higher than room temperature before taking them out.

- (5) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands. (Some cosmetics are detrimental to polarizer's.)
- (6) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (7) Maximum pressure to the surface must be less than 1.96×10⁴ Pa.

 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N.
- (8) Since the metal width is narrow on these locations (see page 9-1/1), please careful with handling.

(9) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.

Hard wiping accumulated dust will leave scars on the surface even using a cloth.

11.3 OPERATION PRECAUTION

(1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.

LCM module's should usually be used under recommended operating conditions shown in chapter 5. Exceeding any of these conditions may adversely affect its reliability.

- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
 - However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.

11.4 STORAGE

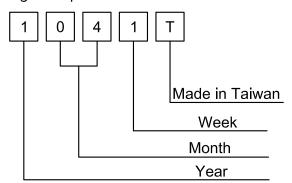
In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

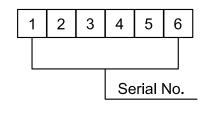
- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between -30°C and 80°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

12.DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 4 dight for production lot 6 digits for production control..





Year	Mark
2013	3
2014	4
2015	5
2016	6
2017	7

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Mark	01	02	03	04	05	06
Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	07	08	09	10	11	12

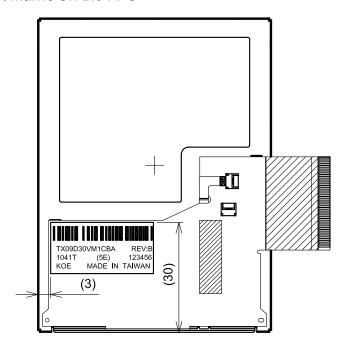
Week (Day In Calendar)	Figure In Lot Mark
01~07	1
08~14	2
15~21	3
22~28	4
29~31	5

12.2 REVISION (REV.) CONTROL

Rev. is the column for manufacturing convenience A-Z except I and O maybe written on this column.

REV.No	ITEM	NOTE
Α	-	-
В	The connectors on FPC Changed	ı

12.3 Location of lot mark: On the FPC



13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity. Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - 1) When a question is arisen in the specifications.
 - 2) When a new problem is arisen which is not specified in this specifications.
 - 3) When an inspection specifications change or operating condition change by customer is reported to KOE, and some problem is arisen in the specification due to the change.
 - 4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with KOE.