# HITACHI

FOR MESSRS: DATE	E: Oct.30,2007
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# CUSTOMER'S ACCEPTANCE SPECIFICATIONS TX31D58VM0CAA

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\*When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.

PROPOSED BY; Elton Liv ACCEPTED BY;

KAOHSIUNG HITACHI	Sh.	7B64PS 2701-TX31D58VM0CAA-1 PA	AGE	1 1/1
ELECTRONICS CO.,LTD.	No.	7B04F3 2701-1X31D30VIVIOCAV4-1	AGE	1-1/1

### RECORD OF REVISION

DATE	SHEET No.	SUMMARY
• • •		
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- -		
AOHSIUNG	HITACHI	Sh. 700 400 5700 TV04 D50 440 04 4 1 D4 05 0 444
	CS CO.,LTD. DATE	Oct.30,'07 Sn. 7B64PS 2702- TX31D58VM0CAA-1 PAGE 2-1/1

### 3. GENERAL DATA

The following specifications are applied to the following IPS TFT module. Note: Inverter for Back-Light unit is not built in this module.

Product N	lame	TX31D58VM0CAA

Effective Display Area (H)261.6 x (V)156.96 [mm]

Number of Pixels (H)800 x (V)480 [pixels]

Pixel Pitch (H)0.327 x (V)0.327 [mm]

Color Pixel Arrangement R.G.B Vertical Stripe

Display Mode Transmissive Mode

Normally Black Mode

Top Polarizer Type Anti-Glare

Number of Colors 16,777,216 [colors]

Viewing Angle Range Super Wide Version

(Horizontal & Vertical : 170°, CR≧10)

Color Gamut 72%(typ.) For NTSC

Input Signal CMOS , R.G.B 8bit digital

Backlight CCFL ,2 pcs Side-light type (L shape)

External Dimensions (H)312.35 x (V)185.8 x (t)13.0 typ. [mm]

Power Supply 3.3V

Weight 790 typ. [g]

### 4. ABSOLUTE MAXIMUM RATINGS

### 4.1 ENVIROMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM Temperature Humidity Vibration Shock Corrosive Gas Illumination at	OPER	ATING	STO	RAGE	UNIT	NOTE
	MIN.	MAX.	MIN.	MIN. MAX.		INOIE
Temperature	(-20)	(70)	-25	70	$^{\circ}\mathbb{C}$	1),5)
Humidity	(Note 2)		(No	te 2)	%RH	1)
Vibration	_	4.9(0.5G)	_	14.7(1.5G)	m/s <sup>2</sup>	3)
Shock	-	29.4(3G)	-	294(30G)	m/s <sup>2</sup>	4)
Corrosive Gas	Not Ac	Not Acceptable		ceptable	-	
Illumination at		50,000		50,000	lv.	
LCD Surface	<b>-</b> .,	30,000		30,000	lx	

Note 1) Temperature and Humidity shall be applied to the glass surface of a IPS TFT Module, not to the system installed with a module.

The temperature at the lower right of rear surface (nearby the terminal of the CCFL) should be less than 80°C on the condition of operating.

Operating temperature means the temperature a TFT-LCD Module can be driven, the screen performance (contrast, brightness, response time etc.) is judged at Ta=25°C. The brightness of a CCFL tends to drop at low temperature.

Besides, the life-time becomes shorter at low temperature.

- 2) Ta  $\leq$  40°C······Relative humidity should be less than 85%RH max. Dew is prohibited. Ta > 40°C······Relative humidity should be lower than the moisture of the 85%RH at 40°C
- 3) Frequency of the vibration is between 20Hz and 50Hz. (Remove the resonance point)
- 4) Pulse width of the shock is 10 ms.
- 5) For storage condition Ta at -25°C <48h,at 70°C <100h. For operating condition Ta at -20°C <48h,at 70°C <100h.

### 4.2 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

### 4.2.1 IPS TFT MODULE

VSS=0V

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
Power Supply Voltage	VDD 0		4.0	٧	
Input Voltage for logic	VI	-0.3	VDD+0.3	V	1)
Electrostatic Durability	VESD0	±100		V	2),3)
	VESD1	<u>+</u>	8	kV	4),5)

Note 1) It shall be applied to pixel data signal and clock signal.

2) Discharge Coefficient : 200pF - 0Ω, Environmental : 25℃ - 70%RH

3) It shall be applied to I/F connector pins.

4) Discharge Coefficient : 200pF - 250 Ω , Environmental : 25°C - 70%RH

5) It shall be applied to the surface of a metallic bezel a LCD panel.

### 4.2.2 BACK-LIGHT

GND=0V

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE				
Input Current	Current IL		7.0	mArms	1)				
Input Voltage	VL		2000	Vrms	2)				

Note 1) It is defined at ground line.

2) It shall be applied at connector pins for a CFL at start-up.

KAOHSIUNG HITACHI	C 4 F F	0 -4 00 207	Sh.	TD04D0 0704 TV04D50V4400AA		
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### 5. OPTICAL CHARCACTERISTICS

The following optical characteristics are measured under stable conditions. It takes about 30 minutes to reach stable conditions. The measuring point is the center of display area unless otherwise noted.

The optical characteristics should be measured in a dark room or equivalent state.

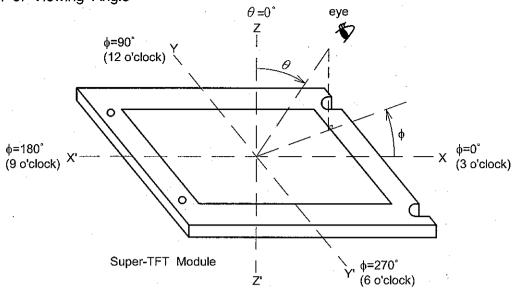
Measuring equipment: Top com BM-7, ritchard 1980A, or equivalent

Temperature = 25°C, VDD=3.3V, fV=60Hz, IL=(6.0)mArms

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Contrast Ratio		CR		250	500	_	_	2)
Response Time	RISE	ton	·	_	13	30	ms	3)
	FALL	toff		_	17	35	ms	3)
Brightness of Wh	ite	Bwh		200	300		cd/m <sup>2</sup>	
Brightness Uniform	nity	Buni	•	65	-	_	%	4)
Color Chromaticity (CIE)	Red	х		0.60	0.65	0.70	_	
		у	$\theta$ =0° (Note 1)	0.28	0.33	0.38		·
	Green	X	((()))	0.25	0.30	0.35		
		у		0.57	0.62	0.67		•
	Blue White	X		0.09	0.14	0.19		
		у		0.05	0.10	0.15		
		X		0.26	0.31	0.36		
	vviile	у		0.28	0.33	0.38		
	x - x'	$\theta \mathbf{x}$	φ=0°	80	85	-		
Viewing Angle	X - X	θx'	φ=180°	80	85	-	dog	1) 2)
(CR≧10)		Øу	φ=90°	80	85	-	deg.	1),2)
	y – y'	∂y'	φ=270°	80	85	- <del></del>		

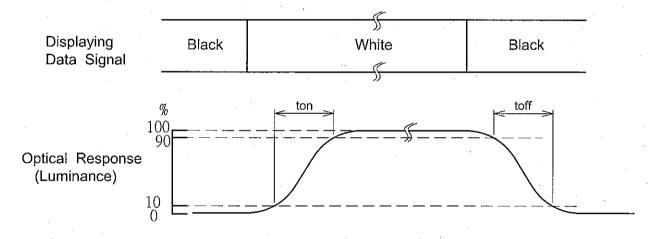
					_
KAOHSIUNG HITACHI			Sh.		
ELECTRONICS CO.,LTD.	DATE	Oct.30,'07	No.	7B64PS 2705- TX31D58VM0CAA-1 PAGE 5-1/2	

Note 1) Definition of Viewing Angle

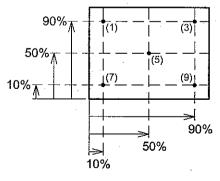


2) Definition of Contrast Ratio(CR):

3) Definition of Response Time



4) Definition of Birghtness Uniformity



·: measuring point

Display pattern is white (63 level). The brightness uniformity is defined as the following equation. Brightness at each point is measured, and average, maximun and minimum brightness is calculated.

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ELECTRONICS CO.,LTD.		001.00, 07	No.	7 DO41 3 2703-173 1D30 VIVIO CAA-1	FAGE	J-2/2	ļ

### 6. ELECTRICAL CHARACTERISTICS

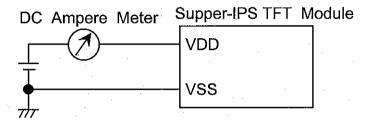
### 6.1 TFT-LCD MODULE

Ta=25°C , VSS=0V

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Power Supply Voltag	е	. VDD	3.15	3.3	3.45	V	·
Input Voltage for	Hi	VIH	2.0	-	-	V	1)
Logic	Lo	VIL			8.0	, v	. ')
Power Supply Currer	it	IDD	· ·	450	600	mA	2),3)
Vsync Frequency		fV	-	60	75	Hz	
Hsync Frequency		fH	-	31.6	39.2	kHz	
DCLK Frequency		fCLK	-	32	35	MHz	

Note 1) It shall be applied to pixel data signal and clock signal.

2) DC current at fv=60.0Hz, fCLK=32MHz, VDD=3.3V and display pattern is white.



3) Current capacity for power supply VDD should be larger than 3A. This TFT-LCD Module has a fuse 1A.

### 6.2 BACK LIGHT

Ta=25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Current	IL	4.0	6.0	6.5	mArms	1),3)
Lamp Voltage	VL	-	1000		Vrms	
Frequency	fL f	50	_	70	kHz	2)
Starting Lamp Voltage	VS	1400	-	-	Vrms	6)
		1600	-	_	VIIIIS	6),7)

Note 1) Higher IL causes short life time of CCFL.

2) Lamp frequency may cause interference with HSYNC frequency, causing beat or flicker on the display.

Therefore lamp frequency shall be as different as possible from HSYNC frequency in order to avoid the interference.

- 3) It is defined at GND line.
- 4) The output voltage of the inverter should be kept 1 sec at least. Otherwise the lamp may not be turned on.
- 5) The power supply characteristics of the inverter are very important to the performance of the back light, for example lighting characteristics or life time.

Please evaluate the power supply matching with the back light by the set which is Near to the actual systems sufficiently, in order to avoid the abnormal lighting (flicker, mislighting etc).

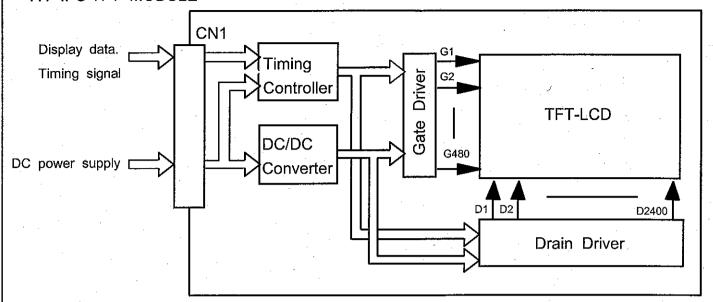
The inverter should be built-in a safety circuit which detects over current, over voltage and distorted wave form of lamp current.

- 6) Starting voltage should be more than Vs (Min.).
- 7) Ta=0°C

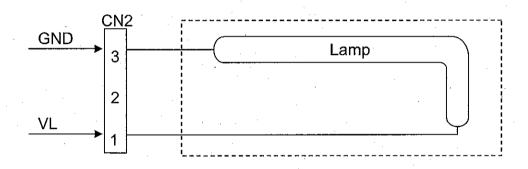
KAOHSIUNG HITACHI		Oct.30,'07	Sh.	7B64PS 2706-	TV24DE0\/M	100 A A A	DACE	6 1/1
ELECTRONICS CO.,LTD.	DATE		No.	/B04P3 2/00-	- 1731028414	UCAA-1	PAGE	0-1/1

### 7. BLOCK DIAGRAM

### 7.1 IPS TFT MODULE



### 7.2 BACK LIGHT UNIT

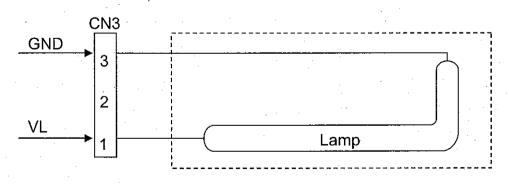


Color of lead wire

3 (GND) : White

2 (NC)

1 (VL) : Pink



Color of lead wire

3 (GND) : White

2 (NC)

1 (VL) Blue

	1 (VL)	. Diue				
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### 8. INTERFACE PIN CONNECTION

### 8.1 TFT-LCD MODULE

CN1 《JAE; FA5B040HP 1R3000》

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	VDD	Power Supply 3.3V	1)
2	· VDD		
3	VDD		
4	VSS	GND	2).
5	VSS		
6	DTMG	Display Timing Data	•
6 7	VSS	GND	2)
8	B7	B Data	
9	B6		
10	B5		
11	B4		
12	VSS	GND (0V)	2)
13	B3	B Data	
14	B2		
15	B1		•
16	B0		<u> </u>
17	VSS	GND	2)
18	G7	G Data	
19	G6	·	
20	G5		•
21	G4		
22	VSS	GND	2)
23	G3	G Data	
24	G2		
25	G1		
26	G0		
27	VSS	GND	2)
28	R7	R Data	
29	R6		
30	R5		·
31	R4		
32	VSS	GND	2)
33	R3	R Data	
34	R2		
35	R1		
36	R0		
37	VSS	GND	2)
38	DCLK	Dot Clock	
39	VSS	GND	2)
40	VSS		

Note 1) All VDD pins shall be connected to (+3.3V)(Typ.).

2) All VSS pins shall be grounded. Metal bezel is internally connected to VSS.

### 8.2 BACK-LIGHT UNIT

CN2: BHR-03VS-1 (JST)

PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	, VL	Power Supply	
2	NC		
3	GND	GND (0V)	

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### 8.3 RELATIONSHIP BETWEEN DISPLAY COLORS AND INPUT SIGNAIS Red Data Green Data Blue Data Input R7 R6 R5 R4 R3 R2 R1 R0 G7 G6 G5 G4 G3 G2 G1 G0 B7 B6 B5 B4 B3 B2 B1 B0 color MSB LSB MSB LSB MSB LSB 0 | Black Red(255) Green(255) 0. Basic Blue(255) Color Cyan Magenta Yellow White Black 0. Ò Red(1) Red(2) Red Red(253) Red(254) Red(255) Ö Black Ó Green(1) Ω Green(2) Green Green(253) Green(254) Green(255) 0. Black 0. Blue(1) Blue(2) Blue

0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 0 0

1 0

### Note 1) Definition of gray scale:

0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0

Blue(253)

Blue(254)

Blue(255)

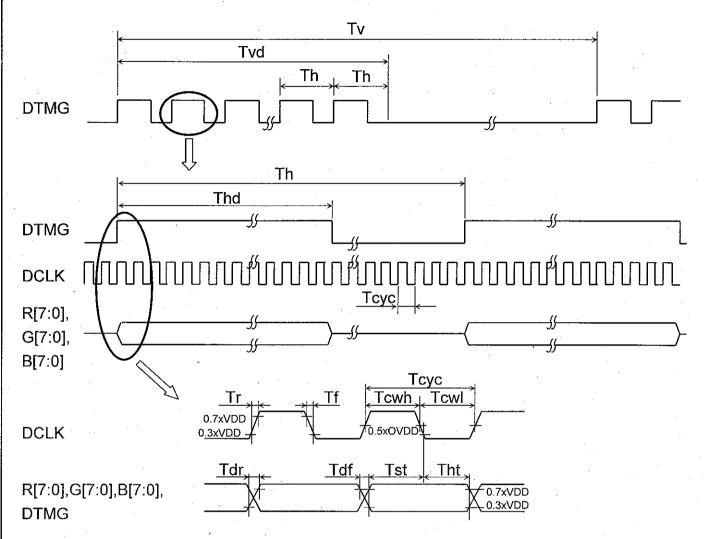
Color(n)···Number in parenthesis indicates gray scale level.

Larger n corresponds to brighter level.

2) Data: 1: High, 0: Low

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# 9. INTERFACE TIMING 9.1 TIMING CHART

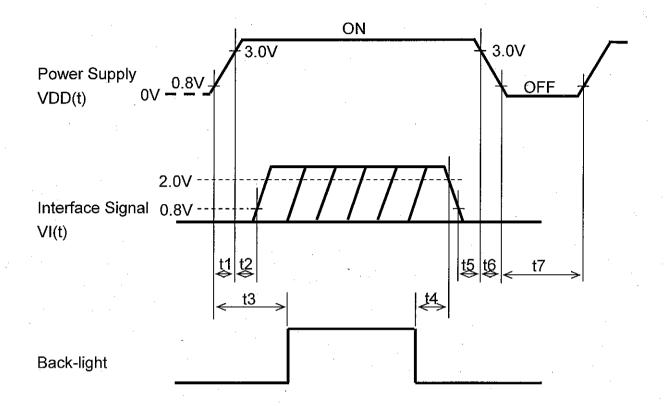


### 9.2 INTERFACE TIMING SPECIFICATIONS

	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
DCLK	Period	1/Tcyc	28.57	31.25	34.48	2 Y	
	Width- High	Twch	8	-	-	-	÷
	Width- Low	Twcl	8	-	-	ns	<b>-</b> .
	Rise time	Tr	-	-	4		
	Fall time	Tf	-	-	4		
	Duty	D	0.45	0.5	0.55	_	D=Twcl/Tcyc
DTMG	Set up time	Tst	5	_	-		
	Hold time	Tht	- 5	-	_	ne	
	Rise time	Tdr	-	-	5	ns	- I
	Fall time	Tdf	-	-	5		
	Hsync cycle time	Th	850	1,048	1,600	Toyro	
	Hsync Width- Active	Thd	800	800	800	Тсус	
	Vsync cycle time	Tv	482	510	610	Th	
	Vsync Width- Active	Tvd	480	480	480	111	<u>-</u>

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### 9.3 TIMING BETWEEN INTERFACE SIGNALS AND POWER SUPPLY



POWER ON t1≦15ms

 $0ms \le t2 \le 45ms$ 

0.1s≦t3

**POWER OFF** 

5ms≦t4

0ms ≤ t5 ≤ 45ms

 $0ms \le t6 \le 20ms$ 

0.4s≦t7

Note 1) Set  $0V \le VI(t) \le VDD(t)$ .

VI(t), VDD(t) mean the transient state of VI, VDD when the power supply is turned ON or OFF.

2) Do not keep interface signal high-impedance, while power is on.

	,						
KAOHSIUNG HITACHI	DATE	0-+ 20 207	Sh.	700400	0700: TV04D50) #40		0.00
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# 10. DIMENSIONAL OUTLINE 10.1 FRONT VIEW 312.35±0.8 300.35 30±0.2 22.5 288.15±0.8 Ф 0 Ф 0 $\overline{\circ}$ 1Pin (CN2) 145.55 (Active Center) 261.6 (Active Area) 265.6±0.3 (Opening Area) Scale: NTS Unit : mm KAOHSIUNG HITACHI DATE | Oct.30,'07 7B63PS 2710-TX31D58VM0CAA-1 PAGE 10-1/2 ELECTRONICS CO.,LTD.

Note 1) Tolerance not specified is ±0.5mm.

2) Dimension in parenthesis are reference value.

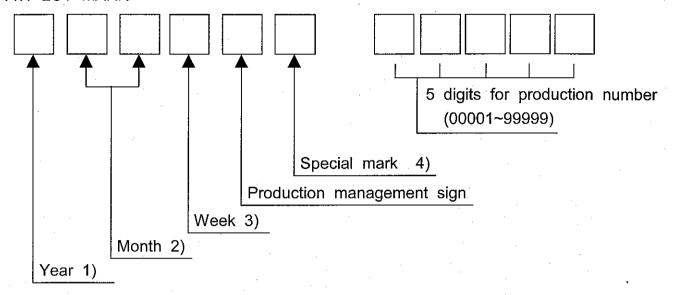
KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.

DATE Oct.30,'07 Sh.

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### 11. DESIGNATION OF LOT MARK

### 11.1 LOT MARK



Note 1)

Year	Mark
2007	7
2008	8
2009	9
2010	0
2011	1

2)

			•
Month	Mark	Month	Mark
1	01	7	07
2	02	8	80
3	03	9	09
4	04	10	10
5	05	11	11
6	06	12	12

3)

Week (Days)	Mark
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

- 4) It is the mark that was opened up by production person to take correspondence with production number.
- 11.2 REVISION(REV.) CONTROL

REV. is the column for manufacturing convenience. A-Z except I and O may be written on this column

### 11.3 LOCATION OF LOT MARK

Lot mark is printed on a label. The label is on the metallic bezel as shown in 7. External Dimensional.

The style of character will be changed without notice.



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### 12. COSMETIC SPECIFICATIONS

### 12.1 CONDITION FOR COSMETIC INSPECTION

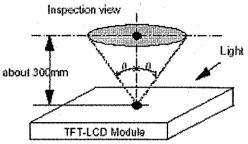
### (1) Viewing zone

 a) The figure shows the correspondence between eyes (of inspector) and TFT-LCD module.

 $\theta \leq \!\!\!\! 45 ^\circ$  ; when non-operating inspection

 $\theta \leq 5^{\circ}$ : when operating inspection

 b) Inspection should be executed only from front side and only A-zone.
 Cosmetic of B-zone and C-zone are ignored. (refer to 12.2 Definition of zone)



### (2) Environmental

a) Temperature : 25°C

b) Ambient light: about 2000 [lx] and non-directive.

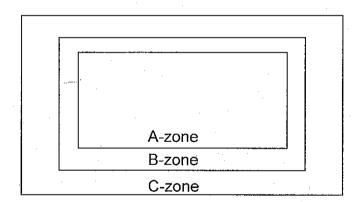
c) Back-light : when non-operating inspection , Back-light should be off.

### 12.2 DEFINITION OF ZONE

A-zone: Display area (pixel area)

B-zone: Area between A-zone and C-zone

C-zone: Metallic bezel area (include I/F connector)



### 12.3 COSMETIC SPECIFICATIONS

When displaying conditions are not stable (ex. at turn on or off), the following specifications are not applied.

Operating inspection

No	IT	-		MAX. ACCEPTABLE		
1 i	· · · · · · · · · · · · · · · · · · ·	EM	•	NUMBER	UNIT	NOT E
	Dot dofoot			A-ZONE		4) 2)
	Dot defect		1-dot	4	pcs	1),2), 4)
			2-dots	1		1),2),
		Sparkle mode	3-dots	) <b>0</b>	Units	5)
			Density	2	Units/ <i>ø</i> 20mm	1),2), 3),6)
		<u>'</u>	Total	5	Units	1),2)
1			1-dot	. 5	pcs	1),3), 4)
1.		Dist	2-dots	2	1.5	1),3),
		Black mode	3-dots	0	Units	5)
		mode	Density	3	Units/ <i>ø2</i> 0mm	1),2), 3),6)
			Total	5	Units	1),3)
		Total		10	Units	• 1)
2	Line defect			Serious one is	·	ĺ
3	Uneven brightness			not allowed		
	Stain inclusion	W≦0.02	L : Ignore	Ignore		
	┌ Line shape ┌	W≦0.03	L≦2.0	10		•
4	W : width (mm)	VV <u>≥</u> 0.05	L>2.0	0		7\
4	L: length (mm)	W<0.06	L≦1.0	10	pcs	7)
		W≦0.06	L>1.0	0	·	
	-	W>0.06	· <b>-</b>	(See dot shape)		
	Stain inclusion	D≦	0.22	Ignore		
5	Dot shape	D≦	0.33	5	pcs	7)
	D : ave. dia (mm)	D>	0.33	0		i i
	Scratch on polarizer	W≦0.01	L : Ignore	Ignore		
	Line shape	14/<0.00	L≦40	10		
6	W : width (mm)	W≦0.02	L>40	0	pcs	8)
	L: length (mm)	10/<0.04	L≦20	10	*4	
		W≦0.04	L>20	0	•	
	Scratch on polarizer	D≦	<u>0.2</u>	Ignore		
1 1	· .					0\
7	☐ Dot shape ☐	D≦0.4 D>0.4		10	pcs	8)

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Non-operating inspection

No	ITEM		Max. acceptable number	Unit	Note
INO	1 1 EE IVI	*	A-zone	Oțiit	Note
	Bubbles, peeling	D≦0.3	Ignore		-
	In polarizer	D≦0.5	10		0)
8	D:ave.dia(mm)	D≦1.0	5	pcs	8)
	D>1.0		, 0		
9	Wrinkles on polarizer		Serious one is not allowed	-	-

Note 1) Dot defect : defect area > 1/2 dot

- 2) Sparkle mode: brightness of dot is more than 30% at black. (visible to eye)
- 3) Black mode: brightness of dot is less than 70% at white (visible to eye)
- 4) 1 dot: defect dot is isolated, not attached to other defect dot.
- 5) N dots: N defect dots are consecutive. (N means the number of defects dots)
- 6) Density: number of defect dots inside 20mm  $\phi$ .
- 7) Those stains which can be wiped out easily are acceptable.
- 8) Polarizer area inside of B-zone is not applied.

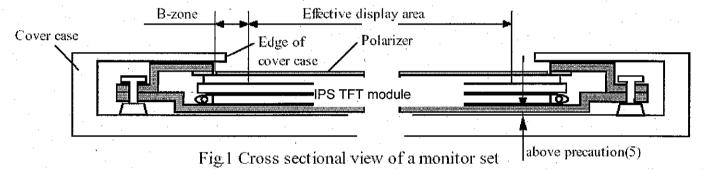
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### 13. PRECAUTION

Please pay attention to the followings when a IPS TFT Module with a Back-light unit is used, handled and mounted.

### 13.1 PRECAUTION TO HANDLING AND MOUNTING

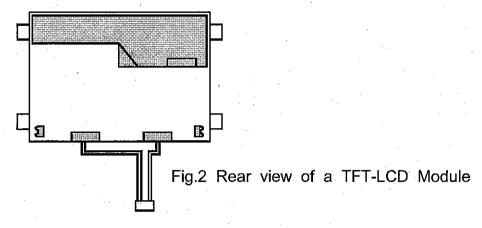
- (1) Applying strong force to a part of the module may cause partial deformation of frame or mold, and cause damage to the display.
- (2) The module should gently and firmly be held by both hands. Never hold by just one hand in order to avoid any internal damage. Never push the exposed polarizer.
- (3) The module should be installed with mounting holes at each corner of a module.
- (4) Uneven force such as twisted stress should not be applied to a module when a module is mounted on the cover case. The cover case must have sufficient strength so that external force can not be transmitted directly to a module.
- (5) It is recommended to leave a space between a module and a holding board of a module so that partial force is not applied to a module.



- (6) Materials included acetic acid and choline should not be used for a cover case as well as other parts and boards near a module. Acetic acid attacks a polarizer. Choline attacks electric circuits due to electro-chemical reaction.
- (7) The polarizer on a TFT cell should carefully be handled due to its softness, and should not be touched, pushed or rubbed with glass, tweezers or anything harder than HB pencil lead. The surface of a polarizer should not be touched and rubbed with bare Hand, greasy clothes or dusty clothes.
- (8) The surface of a polarizer should be gently wiped with absorbent cotton, chamois or other soft materials slightly contained isopropyl alcohol when the surface becomes dirty Isopropyl alcohol as cleaning chemicals is recommended in order to clean adhesives which fix front / rear polarizers on a IPS TFT cell. Other cleaning chemicals such as acetone, toluene and normal-hexane should not be used to clean adhesives because they cause chemical damage to a polarizer.
- (9) Saliva or water drops should be immediately wiped off. Otherwise, the portion of a polarizer may be deformed and its color may be faded.
- (10) The module should not be opened or modified. It may cause not to operate properly.
- (11) Metallic bezel of a module should not be handled with bare hand or dirty gloves. Otherwise, color of a metallic frame may become dirty during its storage. It is recommended use clean soft gloves and clean finger stalls when a module is handled at incoming inspection process and production (assembly) process.
- (12) Lamp(CCFL) cables should not be pulled and held.

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(13) Don't push the hatched area as shown in Fig.2.



### 13.2 PRECAUTION TO OPERATION

- (1) The ambient temperature near the operated module should be satisfied with the absolute maximum ratings. Unless it meets the specifications, sufficient cooling system should be adopted to system.
- (2) The spike noise causes the mis-operation of a module. The level of spike noise should be as follows:  $-500\text{mV} \leq \text{over-}$  and under- shoot of VDD  $\leq +500\text{mV}$ ,  $50\text{nsec} \leq \text{width}$ .
  - (3) Optical response time, luminance and chromaticity depend on the temperature of a IPS TFT module. (At lower temperature, it becomes longer.)
  - (4) Response time of CCFL luminance become longer at lower at lower temperature operation.
  - (5) Sudden temperature change may cause dew on and / or in a module. Dew makes damage to a polarizer and / or electrical contacting portion. Dew causes fading of display quality.
  - (6) Fixed patterns displayed on a module for a long time may cause after-image. It will be recovered soon.
- (7) A module has high frequency circuits. Sufficient suppression to electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be effective to minimize the interference.
- (8) Inserting or pulling I/F connectors causes any trouble when power supply and signal dates are on-state. I/F connectors should be inserted and pulled after power supply and signal dates are turned off.

### 13.3 ELECTROSTATIC DISCHARGE CONTROL

- (1) Since a module consists of a IPS TFT cell and electronic circuits with CMOS-ICs, which are very weak to electrostatic discharge, persons who are handling a module should be grounded through adequate methods such as a list band.

  I/F connector pins should not be touched directly with bare hands.
- (2) Protection film for a polarizer on a module should be slowly peeled off so that the electrostatic charge can be minimized.

### 13.4 PRECAUTION TO STRONG LIGHT EXPOSURE

(1) A module should not be exposed under strong light. Otherwise, characteristics of a polarizer and color filter in a module may be degraded.

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### 13.5 PRECAUTION TO STORAGE

When modules for replacement are stored for a long time, following precautions should be taken care of:

- (1) Modules should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during storage. Modules should be stored at 5℃ to 35℃ at normal humidity (60%RH or less).
- (2) The surface of polarizers should not come in contact with any other object. It is recommended that modules should be stored in the HITACHI's shipping box.

### 13.6 PRECAUTION TO HANDLING PROTECTION FILM

- (1) The protection film for polarizers should be pealed off slowly and carefully by persons who are electrically grounded with adequate methods such as a list band. Besides, ionized air should be blown over during peeling action. Dusts on a polarizer should be blown of by an ionized nitrogen gun and so on.
- (2) The protection film should be peeling off without rubbing it to the polarizer. Because, if the film is rubbed together with the polarizer, since the film is attached to the polarizer with a small amount of adhesive, the adhesive may remain on a polarizer.
- (3) The module with protection film should be stored on the conditions explained in 10.5(1). However, in case that the storage time is too long, adhesive may remain on a polarizer even after a protection film is peeled off. Besides, in case that a module is stored at higher temperature and/or higher humidity, adhesive may remain on a polarizer. The remained adhesive may cause non-uniformity of display image.
- (4) The remained adhesive or its vestige on the polarizer should be wiped off with absorbent cotton or other soft materials slightly contained isopropyl alcohol.

### 13.7 SAFETY

- (1) Since a IPS TFT cell and lamps are made of glass, handling to the broken module should be taken care sufficiently in order not to be injured. Hands touched liquid crystal from a broken cell should be washed sufficiently.
- (2) The CCFL inverter should be designed to include the function of output shut down in case the output overcurrent happen due to any back-light trouble. The shut down function should be assured to work in abnormal condition at the actual systems.
- (3) The module should not be taken apart and should not put conductive thing into the Module, so that back-light drives by high voltage.

### 13.8 ENVIROMENTAL PROTECTION

(1) This IPS TFT module contains cold cathode fluorescent lamps. Please follow local ordinance or regulations for it's disposal.

### 13.9 USE RESTRICTIONS AND LIMITATIONS

(1) This product is not authorized for use in life support devices or systems, military applications or other applications which pose a significant risk of personal injury.

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(2) In no event shall HITACHI, Ltd., be liable for any incidental, indirect or consequential damages in connection with the installation or use of this product, even if informed of the possibility thereof in advance. These limitations apply to all causes of action in the aggregate, including without limitation breach of contact, breach of warranty, negligence, strict liability, misrepresentation and other torts.

### 13.10 OTHERS

(1) Electrical components which may not affect electrical performance are subjective to change without notice because of their availability.

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