SPEC

Spec No.	TQ3C 8EAC0 E1DDV103 00
Date	January 6, 2009

#### TYPE: KCG057QVLEE-G000

< 5.7 inch QVGA transmissive color STN with LED backlight and touch panel>

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Issued
Date: Jan.20,2009
KYDERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

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	Original	Designed by: Engineering dept. Confirmed by: QA dept		Confirmed by: QA dept.			
	Issue Date	Prepared	Checked	Approved	Checked	Approved	
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## Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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#### Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
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### 1. Application

This document defines the specification of KCG057QVLEE-G000. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type STN

Duty ratio : 1/240 duty Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Bias voltage circuit, Randomizing circuit,

DC/DC converter circuit, Temperature compensation circuit

Touch panel : Analog type, Non-Glare treatment

#### 3. Mechanical specifications

#### 3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	154.6 (W)× 114.8 (H) × 10.3 (D)	mm
Active area	115.18 (W) × 86.38 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Effective viewing area	cective viewing area 117.2 (W) × 88.4 (H)	
Dot format	$320 \times (R,G,B) (W) \times 240 (H)$	dot
Dot size	0.10 (W) × 0.34 (H)	mm
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	ase color *1 Normally Black	
Mass	215	

<sup>\*1</sup> Due to the characteristics of the LCD material, the color varies with environmental temperature.

#### 3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation force	0.05 ~ 0.8	N
Transmittance	Typ.79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



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#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	6.0	V
Supply voltage for LCD driving	$ m V_{EE}$	0	30.0	V
Input signal voltage *1	$V_{\rm IN}$	0	$ m V_{DD}$	V
FRM frequency	$f_{\mathrm{FRM}}$	-	150	Hz
Supply voltage (LED)	VF	-	6.0	V
LED forward current	IF	-	450	mA
Reversed voltage	VR	-	5.0	V
Supply voltage for touch panel	$V_{\mathrm{TP}}$	0	6.0	V
Input current of touch panel	$I_{TP}$	0	0.5	mA

<sup>\*1</sup> Input signal: FRM, LOAD, CP, DISP, D0 ~ D7

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	0	60	$^{\circ}\mathrm{C}$
Storage temperature	*2	Tsto	-20	70	°C
Operating humidity	*3	$H_{\mathrm{OP}}$	10	*4	%RH
Storage humidity	*3	Нѕто	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

<sup>\*1</sup> Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for use" for details.)

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

<sup>2</sup> hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



<sup>\*2</sup> Temp. = -20°C < 48h, Temp. = 70°C < 168h

<sup>\*3</sup> Non-condensing

<sup>\*4</sup> Temp. 40°C, 85%RH Max.

<sup>\*6</sup> Acceleration: 490 m/s², Pulse width: 11 ms

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#### 5. Electrical characteristics

 $5-1. V_{DD} = 5.0V$ 

 $V_{DD} = +5.0V \pm 5\%$ , Temp. =  $0 \sim 60$ °C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	-	4.75	5.00	5.25	V
Supply voltage for LCD driving	V V	0~50°C *3	24.4	25.4	26.4	V
*1, *2	$V_{EE} = V_{OP}$	~ 60	1	•	26.1	V
Input signal voltage	<b>17</b>	"High" level	$0.8V_{\mathrm{DD}}$	-	$V_{ m DD}$	V
(FRM,LOAD,CP,DISP,D0 ~ D7)	$ m V_{IN}$	"Low" level	0	•	$0.2 V_{ m DD}$	V
Input current	$ m I_{IN}$	Input signal	-100	-	100	μA
Clock frequency	$\mathbf{f}_{\mathrm{CP}}$	-	1	•	10.00	MHz
Frame frequency *4	${ m f_{FRM}}$	-	70	75	80	$_{ m Hz}$
Current consumption for logic	$I_{\mathrm{DD}}$		-	1.0	1.3	mA
Current consumption for LCD	${ m I}_{ m EE}$	*5		16	21	mA
Power consumption	${ m P}_{ m DISP}$		-	411	561	mW

<sup>\*1</sup> The supply voltage ( $V_{EE} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.

#### \*5 Display pattern:



<sup>\*2</sup> Frame frequency :  $f_{FRM} = 75Hz$ 

<sup>\*3</sup> The LCD module has a temperature compensation circuit.

<sup>\*4</sup> In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

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 $5-2. V_{DD} = 3.3V$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	-	3.0	3.3	3.6	V
Supply voltage for LCD driving	V V	0~50°C *3	24.4	25.4	26.4	V
*1, *2	$V_{\rm EE} = V_{ m OP}$	~ 60	-	-	26.1	V
Input signal voltage	77	"High" level	$0.8 V_{\mathrm{DD}}$	-	$V_{\mathrm{DD}}$	V
(FRM,LOAD,CP,DISP,D0-D7)	$ m V_{IN}$	"Low" level	0	-	$0.2 V_{ m DD}$	V
Input current	${ m I}_{ m IN}$	Input signal	-100	•	100	μA
Clock frequency	$ m f_{CP}$	-	1	-	10.00	MHz
Frame frequency *4	$ m f_{FRM}$	-	70	75	80	Hz
Current consumption for logic	${ m I}_{ m DD}$		•	0.6	0.8	mA
Current consumption for LCD	${ m I}_{ m EE}$	*5		16	21	mA
Power consumption	$P_{ m DISP}$		-	408	557	mW

- \*1 The supply voltage ( $V_{EE} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.
- \*2 Frame frequency :  $f_{FRM} = 75Hz$
- \*3 The LCD module has a temperature compensation circuit.
- \*4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

#### \*5 Display pattern:

#### 5-3. Touch panel

Item	Specification
Supply voltage for touch panel	5.0V
Tamminal masistanas	$xL \sim xR : 200\Omega \sim 1,000\Omega$
Terminal resistance	$yU \sim yL : 200\Omega \sim 800\Omega$
Linearity	less than ±2.0%
Insulation resistance	$100 \mathrm{M}\Omega$ or more at DC25V



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# 6. Optical characteristics

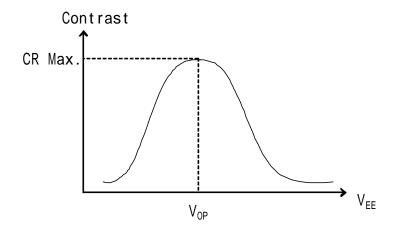
Measuring spot = 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	τr	= =0°	-	320	420	ms
Response time	Down	τd	= =0°	•	170	270	ms
		UPPER		-	20	-	d
V7		LOWER	CR 2	-	35	-	deg.
Viewing angle	range	LEFT	CR 2	-	50	-	1
		$\phi$ right		-	50	-	deg.
Contrast ratio		CR	= =0°	30	55	-	-
Brightness		L	VF=5.0V	110	160	-	cd/m²
	Dad	X	= =0°	0.46	0.51	0.56	
	Red	У	<b>– –</b> 0	0.31	0.36	0.41	
	C	X	= =0°	0.26	0.31	0.36	
Chromaticity	Green	У	0	0.50	0.55	0.60	
coordinates	Dl	X	= =0°	0.10	0.15	0.20	-
Blue	У	<b>– –</b> 0	0.08	0.13	0.18		
	X	= =0°	0.24	0.29	0.34		
	White	У	<b>–</b> –0	0.28	0.33	0.38	

Optimum contrast is obtained by adjusting the LCD driving voltage ( $V_{OP}$ ) while at the viewing angle of  $= = 0^{\circ}$ .

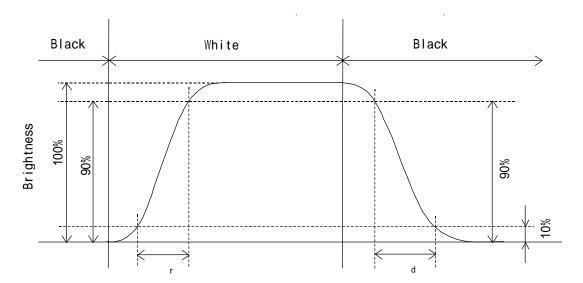
#### 6-1. Definition of contrast ratio

### 6-2. Definition of Vop

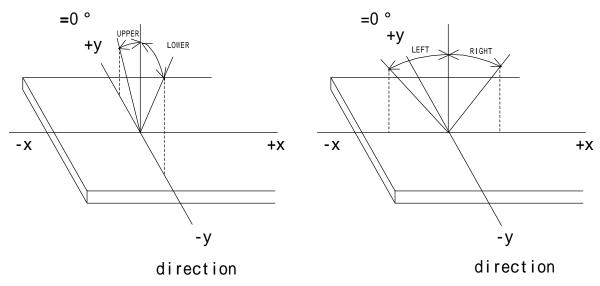




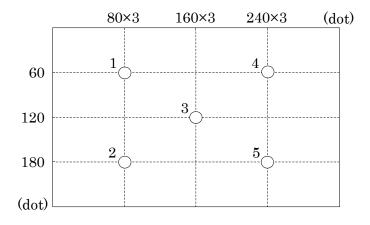
### 6-3. Definition of response time



# 6-4. Definition of viewing angle



#### 6-5. Brightness measuring points

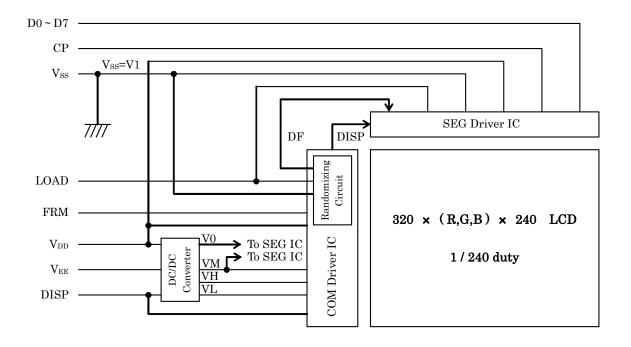


- 1) The brightness is measuring by white brightness at center of display screen(3).
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)
- 3) Backlight: VF=5.0V

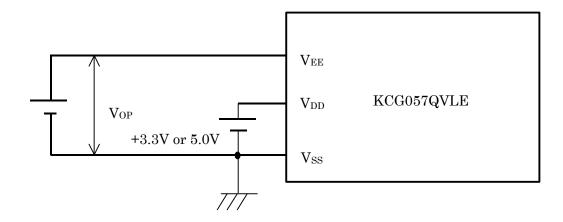


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# 7. Circuit block diagram



# 7-1. Power supply





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# 8. Interface signals

### 8-1. Pin assignment of LCD panel

No.	Symbol	Description	Level
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	$\mathrm{H}  ightarrow \mathrm{L}$
3	CP	Data signal shift clock	$\mathrm{H}  ightarrow \mathrm{L}$
4	DISP	Display control signal	H(ON),L(OFF)
5	$V_{\mathrm{DD}}$	Power supply for logic	-
6	$V_{\mathrm{SS}}$	GND	-
7	$V_{\mathrm{EE}}$	LCD adjust voltage	-
8	D7		
9	D6		
10	D5		
11	D4	Display data	H(OM) I (OEE)
12	D3	Display data	H(ON),L(OFF)
13	D2		
14	D1		
15	D0		

LCD connector : 53261-1571 (MOLEX) Recommended matching FFC or FPC : 51021-1500 (MOLEX)

Note) This assignment is the reverse of what MOLEX defined. Remember that for you designing.

#### 8-2. Pin assignment of LED

No.	Symbol	Description	
1	AN	Anode	
2	CA	Cathode	

#### 8-3. Pin assignment of touch panel

No.	Symbol	Description
1	хL	x-Left terminal
2	уU	y-Upper terminal
3	xR	x-Right terminal
4	уL	y-Lower terminal

Touch panel side connector : 1.25mm pitch

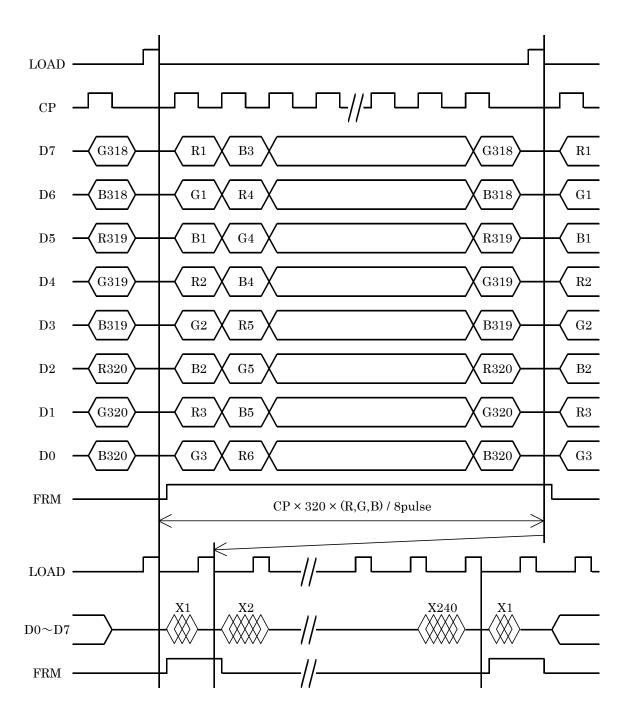
Recommended matching connector : Series FE, FFS (JST)

: KCA-K4R (DMC)



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# 9. Interface timing chart



<sup>\*</sup> The cycle of the LOAD signal should be stable and continuously applied without interruption.



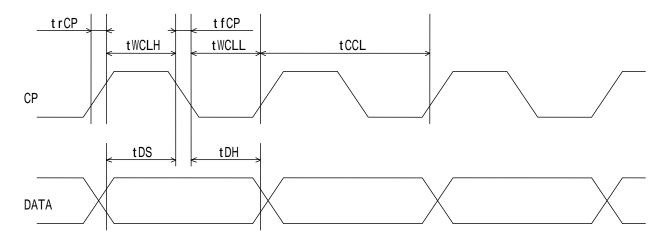
<sup>\*</sup> The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

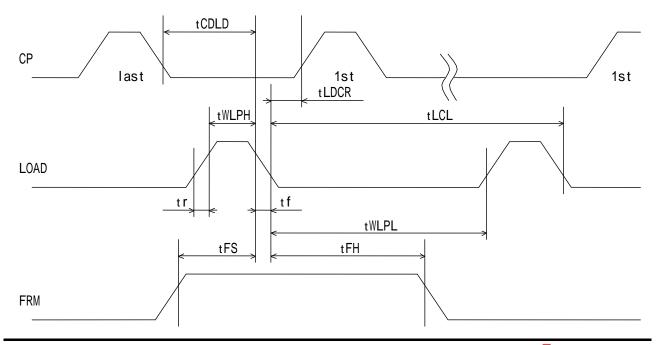
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#### 10. Data and screen

		Y1			Y2			Y3		• • • •		Y320	
	D7 R1	D6 G1	D5 B1	D4 R2	D3 G2	D2 B2	D1	D0	D7	•••		D1 G320	D0
	R1	G1	B1	R2	G2	B2	R3	G3	B3	•••	R320		
ŀ	R1	G1	B1	R2	G2	B2	R3	G3	В3		R320	G320	В3
[ ]													
(240													

# 11. Input timing characteristics







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# 11-1. Switching characteristics ( $V_{DD} = 5.0V$ )

Input characteristics :  $V_{DD} = \pm 5.0 V \pm 5\%$ , Temp. =  $0 \sim 60$ °C

Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	Ns
CP "H" pulse width	tWCLH	30	-	Ns
CP "L" pulse width	tWCLL	30	-	Ns
CP rise up time	trCP	-	15	Ns
CP fall down time	tfCP	-	15	Ns
Data set up time	tDS	25	-	Ns
Data hold time	tDH	25	-	Ns
LOAD "H" pulse width	tWLPH	40	-	Ns
LOAD "L" pulse width	tWLPL	400	-	Ns
LOAD cycle *2	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time	tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time	tLDCR	60	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.

### 11-2. Switching characteristics ( $V_{DD} = 3.3V$ )

Input characteristics :  $V_{DD} = +3.3V \pm 0.3V$ , Temp. =  $0 \sim 60$ °C

Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	40	-	ns
CP "L" pulse width	tWCLL	40	-	ns
CP rise up time	trCP	-	20	ns
CP fall down time	tfCP	-	20	ns
Data set up time	tDS	35	-	ns
Data hold time	tDH	35	-	ns
LOAD "H" pulse width	tWLPH	50	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *2	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time	tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time	tLDCR	80	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.



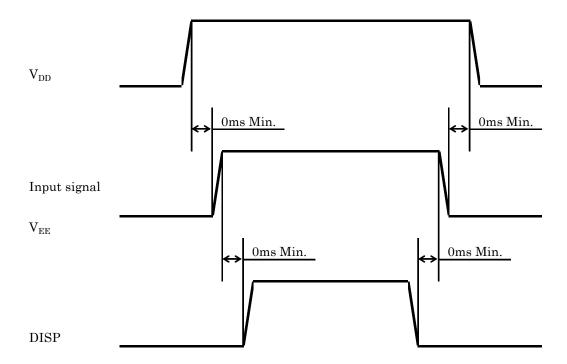
<sup>\*2</sup> LOAD cycle is constant.

<sup>\*2</sup> LOAD cycle is constant.

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### 12. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on  $V_{\text{EE}}$  outside of the time when the  $V_{\text{DD}}$  logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



- \* The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.
- \* Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.



<sup>\*</sup> Input signal: FRM, LOAD, CP, D0  $\sim$  D7

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# 13. LED Backlight characteristics

# LED ratings

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	*1	IF	-	225	285	mA	VF=5.0V, Ta=25°C
Forward voltage	*1	VF	-	5.0	5.2	V	IF=225mA, Ta=25
Operating life time	*2, *3	Т	-	TBD	-	h	VF=5.0V, IF=225mA, Ta=25

<sup>\*1</sup> For AN-CA.



 $<sup>\ ^*2</sup>$  When brightness decrease 50% of initial brightness.

<sup>\*3</sup> Life time is estimated data.

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### 14. Design guidance for analog touch panel

- 14-1. Electrical (In customer's design, please remember the following considerations.)
  - 1) Do not use the current regulated circuit.
  - 2) Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maximum ratings" for details.)
  - 3) Analog Touch panel can not sense two points touching separately.
  - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the Touch panel position data.
  - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

#### 14-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

#### 14-3. Mounting on display and housing bezel

- 1) Do not use an adhesive tape to bond it on the front of Touch panel and hang it to the housing bezel.
- 2) Never expand the Touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the Touch panel will be extremely short.
- 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



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#### 15. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2009	2010	2011	2012	2013	2014
Code	9	0	1	2	3	4

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 16. Warranty

#### 16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 16-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 17. Precautions for use

#### 17-1. Installation of the LCD

- 1) Please ground either of the mounting (screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen. Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

#### 17-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 17-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Adjust the "Supply voltage for LCD driving (VEE)" to obtain optimum viewing angle and contrast ratio.

#### 17-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.



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#### 17-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by a little Ethanol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 7) Do not disassemble LCD module because it will result in damage.
- 8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used
- 10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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### 18. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	70°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-20°C	240h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
Temp. cycle	-20°C 0.5h R.T. 0.5h 70°C 0.5h	10cycles	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
High temp. operation	60°C	500h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul><li>No defect</li><li>No defect</li><li>No defect</li><li>No defect</li></ul>

<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

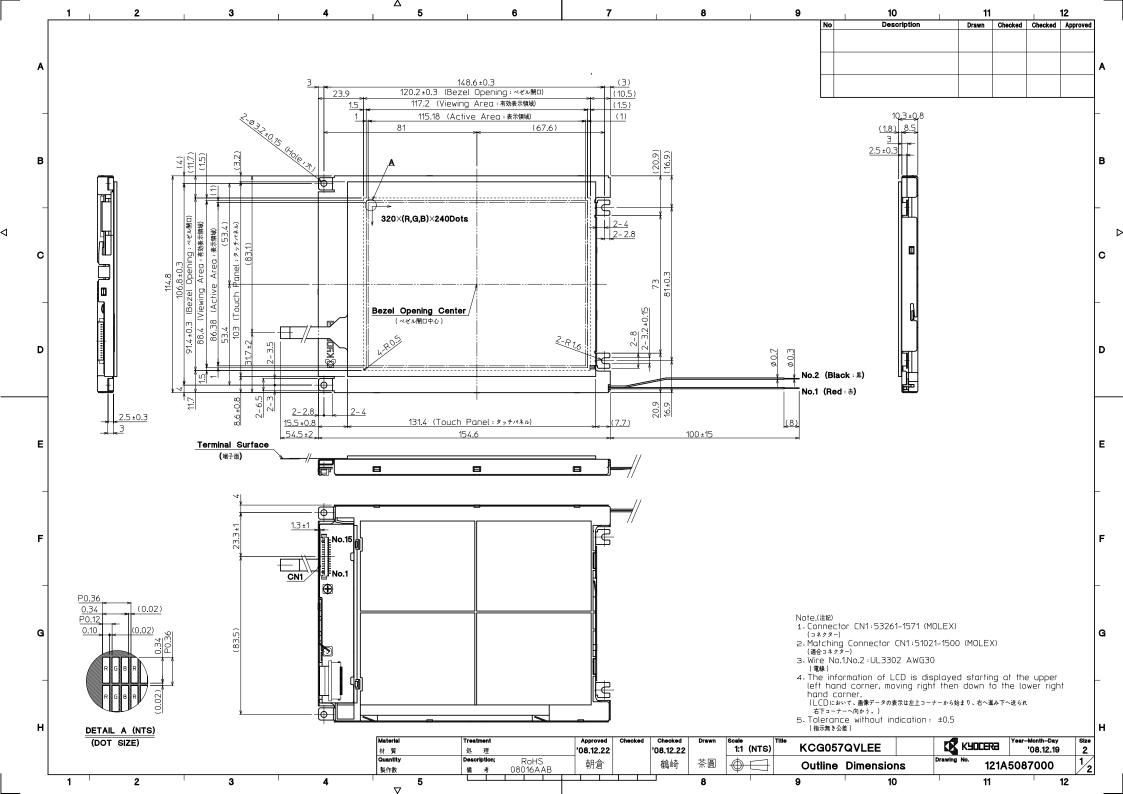


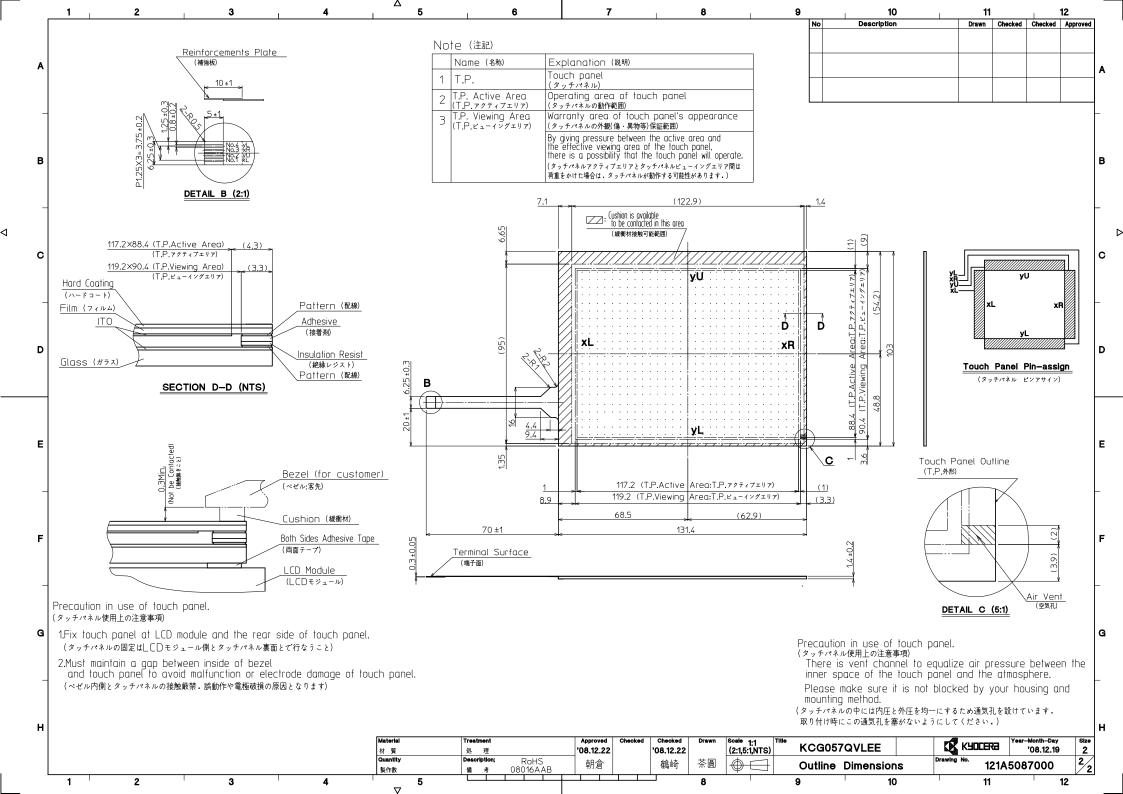
<sup>\*</sup> The LCD is tested in circumstances in which there is no condensation.

<sup>\*</sup> The reliability test is not an out-going inspection.

<sup>\*</sup> The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C 8EAC0 E2DDV95 00
Date	January 6, 2009

# KYOCERA INSPECTION STANDARD

TYPE: KCG057QVLEE-G000

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 6, 2009	S. Kylima	H-Tokumeri	4 Matremoto	J. Sakaguchi	To Sul



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# Revision record

	Data	Designe	Designed by : Engineering dept.		Confirmed by : QA dept.		
	Date	Prepa	red	Checked	Approved	Checked	Approved
-							
Rev.No.	Date	Page			Descripti	ons	



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# Visuals specification 1) Note

Note				
1.When defects specified in this Inspection Standards are inspected, operating voltage (Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-level INSPECTION)				
applied to any defect w	<ul><li>2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area.</li><li>3. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera.</li></ul>			
happen, additional stan				
Inspection distance Temperature	Luminance : $500$ Lux minimum.  Inspection distance : $300$ mm(from the sample)  Temperature : $25\pm5^{\circ}\mathrm{C}$			
Dialeste Deight and	The color of a small area is			
Black spot, Scratch Foreign particle	the color of a small area is different from the remainder.  The phenomenon does not change with voltage.			
Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.			
Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.			
	inspected, operating volumere optimized contrapplied up to effective visual 2. This inspection standar applied to any defect with shall not be applicable to 3. Should any defects which happen, additional standar agreement between custous 4. Inspection conditions  Luminance Inspection distance Temperature Direction  Pinhole, Bright spot Black spot, Scratch Foreign particle  Contrast variation			



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#### 2)Standard

2)Standard					
Inspection item		Judgement standard			
Pinhole, Bright spot, Black spot, Foreign particle	d = (a + b) / 2				
	Category Size(mm) Acceptable number				
	A	d			Neglected
	В	0.2 < d			5
	C	0.3 < d			3
	D	0.5 < d			0
Scratch, Foreign particle	L				
	V	Vidth (mm)	Length	(mm)	Acceptable number
	A	W 0.03	-		Neglected
	В		L	2.0	Neglected
	C 0.03	< W 0.10	2.0 < L	4.0	3
	D		4.0 < L		0
	E 0.10	< W	-		According to 'Circular'
Contrast variation	d = (a + b)/2				
	Category Size (mm)		Acce	eptable number	
	A	d			Neglected
	В	0.5 < d 0.7		3	
	C 0.7 < d 0				



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Inspection item		Judge	ement stand	ard	
Polarizer	(1) Scratch				
(Scratch, Bubble, Dent)		\ 1			
		V			
	*				
	,	Width (mm)	Length (1	mm) Acceptable No	
	A	W 0.1	-	Neglected	
	В	< W 0.3	L	5.0 Neglected	
	C 0.1	< W 0.5	5.0 < L	0	
	D 0.3	s < W	-	0	
			$\mathbf{b}$		
		a			
		((() ← <b>a</b> →	(	d = ( a + b ) / 2	
	Category	Size (	mm)	Acceptable number	
	A	d	mm) 0.2	Acceptable number Neglected	
	A B	0.2 < d	mm) 0.2 0.3	Acceptable number Neglected 5	
	A	d	0.2 0.3 0.5	Acceptable number Neglected	



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<u></u>	<u> </u>					
Inspection item	Judgement standard					
Scratch,	( W = Width, L = Length, D = Diameter = (major axis + minor axis)/ 2)					
Foreign particle	Item				Acceptable number	
(Touch screen portion)		W 0.03 L 20		Neglected		
	Scratch	0.03 < W  0.05	L 10	2pcs	within 20mm	
	Scratch	0.05 < W  0.08	L 6	2pcs	within 20mm	
		0.08 < W 0.1	L 4	1pcs	within φ30mm	
	Foreign	W 0.05	Neglected		Neglected	
	( line like )	0.05 < W = 0.1	L 5	_	within 30mm	
	Foreign	D	0.2		Neglected	
	( circle like )	0.2 < D	0.3	2pcs	within 30mm	
		lied to the visible area.				
		are foreign particle a	C .		· ·	
	electrical perfo	ormance out of the visib	le area, we app	rove of	f this product.	
Glass crack						
(Touch screen	Item	Size (m	ım)		Acceptable	
portion)	Teem	DIZC (III	1111/		number	
			z X	3		
	Conner	Y	3	2 pcs /panel		
			Z	< t		
	Crack in	***	X			
	other area than in		Y	1.5	2 pcs /side	
	corner	2			rside	
		· ·		< t		
			/,			
	Progressive crack				0 pcs (NG even 1pcs)	
	Cruck	7			(IVG even Ipes)	
	Above are applied to the visible area.					
			and damage a	ffected	seriously to the	
	Unless there are foreign particle and damage affected seriously to the electrical performance out of the visible area, we approve of this product.					
Newton's ring	Neglected.				- p	
0	-					
			N	ewton's	sring	

