Spec No.	TQ3C-8EAC0-E1DEC22-00
Date	May 26, 2010

## **TYPE : KCG062HVLAL-G000**

< 6.2inch HVGA transmissive color STN with LED backlight and touch panel>

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### KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: I	Engineering de	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

# Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

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## 1. Application

This document defines the specification of KCG062HVLAL-G000. (RoHS Compliant)

### 2. Construction and outline

: Transmissive color dot matrix type STN
: 1/240 duty
: LED
: Glare treatment
: Bias voltage circuit, Randomizing circuit,
DC/DC converter circuit, Temperature compensation circuit
Analog type, Non-Glare treatment

### 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	$174.2 \text{ (W)} \times 73.4 \text{ (H)} \times 14.0 \text{ (D)}$	mm
Active area	147.84 (W) × 55.44 (H) (15.8cm / 6.2 inch (Diagonal))	mm
Effective viewing area	149.8 (W) $\times$ 57.4 (H)	mm
Dot format	$640 \times (R,G,B)$ (W) $\times 240$ (H)	dot
Dot size	0.057 (W) × 0.211 (H)	mm
Dot pitch	0.077 (W) × 0.231 (H)	mm
Base color 2)	Normally Black	-
Mass	(TBD)	g

3-1. Mechanical specifications of LCD

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

#### 3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation force	0.5±0.3	Ν
Transmittance	Тур. 80	%
Surface hardness	Pencil hardness 2H or more according	-



### 4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V <sub>DD</sub>	0	6.0	V
Supply voltage for LCD driving	VCONT	0	$V_{DD}$	V
Input signal voltage 1)	VIN	0	$V_{DD}$	V
FRM frequency	$\mathbf{f}_{\mathrm{FRM}}$	-	150	Hz
LED forward current 2)	IF	-	(17)	mA
Reversed voltage 2)	VR	-	(5)	V
Supply voltage for touch panel	VTP	0	6.0	V
Input current of touch panel	$\mathrm{I}_{\mathrm{TP}}$	0	0.5	mA

1) Input signal : FRM, LOAD, CP, DISP, D0~D7

2) For each "AN-CA"

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	TOP	0	50	°C
Storage temperature	2)	Тято	-20	60	°C
Operating humidity	3)	Hop	10	4)	%RH
Storage humidity	3)	H <sub>STO</sub>	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for use" for details.)

- 3) Non-condensing
- 4) Temp.≦40°C, 85%RH Max.
- Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	$10\sim 55~{ m Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

```
3 times in each direction: \pm X, \pm Y, \pm Z
```

```
EIAJ ED-2531
```

<sup>2)</sup> Temp. = -20°C<48h, Temp. = 60°C<168h

<sup>6)</sup> Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

### 5. Electrical characteristics

5-1.  $V_{DD} = 5.0 V$ 

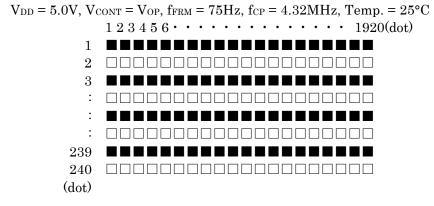
			V <sub>DD</sub> =	$= +5.0V \pm 5\%$	o, Temp. = (	)∼50°C
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	$V_{DD}$	-	4.75	5.00	5.25	V
Supply voltage for LCD driving 1), 2)	$V_{CONT} = V_{OP}$	0∼50°C 3)	1.30	1.80	2.30	V
Input signal voltage	¥7	"High" level	$0.8 V_{DD}$	-	$V_{DD}$	V
(FRM,LOAD,CP,DISP,D0~D7)	$V_{\rm IN}$	"Low" level	0	-	$0.2 V_{DD}$	V
Input current	$I_{IN}$	Input signal	-100	-	100	μA
Rush current for logic	$I_{RUSH}$	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	$\mathbf{f}_{\mathrm{CP}}$	-	-	-	10.00	MHz
Frame frequency 4)	$\mathbf{f}_{\mathrm{FRM}}$	-	70	75	80	Hz
Current consumption for logic	$I_{DD}$	- 5)	-	40	60	mA
Power consumption	$P_{\text{DISP}}$	3)	-	200	300	mW

1) The supply voltage ( $V_{CONT} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.

2) Frame frequency :  $f_{FRM} = 75Hz$ 

- 3) The LCD module has a temperature compensation circuit.
- 4) In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

#### 5) Display pattern:



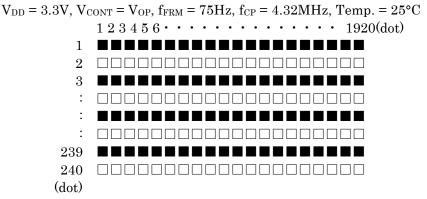
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#### 5-2. $V_{DD} = 3.3 V$

 $V_{DD} = +3.3V \pm 0.3V$ , Temp. =  $0 \sim 50^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic	V <sub>DD</sub>	-	3.0	3.3	3.6	V
Supply voltage for LCD driving 1), 2)	V <sub>CONT</sub> = V <sub>OP</sub>	0∼50°C 3)	1.30	1.80	2.30	V
Input signal voltage	VIN	"High" level	$0.8 V_{DD}$	-	$V_{DD}$	V
(FRM,LOAD,CP,DISP,D0-D7)	V IN	"Low" level	0	-	$0.2 \mathrm{V}_\mathrm{DD}$	V
Input current	$I_{IN}$	Input signal	-100	-	100	μA
Rush current for logic	$I_{RUSH}$	When LCD		3.0A (Peak) × 1ms		
	noon	turn on.				
Clock frequency	${ m f}_{ m CP}$	-	-	-	10.00	MHz
Frame frequency 4)	$\mathbf{f}_{\mathrm{FRM}}$	-	70	75	80	Hz
Current consumption for logic	$I_{DD}$	5)	-	55	83	mA
Power consumption	$P_{DISP}$	3)	-	182	274	mW

- 1) The supply voltage ( $V_{CONT} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.
- 2) Frame frequency :  $f_{FRM} = 75 Hz$
- 3) The LCD module has a temperature compensation circuit.
- 4) In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.
- 5) Display pattern:



5-3. Touch panel

Item	Specification
Supply voltage for touch panel	$5.0\mathrm{V}$
Terminal resistance	$xL\sim xR$ : 200 $\Omega$ $\sim$ 1,000 $\Omega$
	yU~yL:200Ω~1,000Ω
Linearity	less than $\pm 1.5\%$
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$

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Measuring spot = $\phi$ 6.0mm, Temp.						np. – 25 C		
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	
December 1	Rise	$\tau_{\rm r}$	$\theta = \phi = 0^{\circ}$	-	320	420	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	170	270	ms	
		heta upper		-	30	-	1	
<b>1</b> 7 <sup>.</sup> · 1		heta lower	$CR \ge 2$	-	20	-	deg.	
Viewing angle	range	$\phi_{ m  LEFT}$	$CR \leq 2$	-	50	-	1	
		$\phi$ right		-	50	-	deg.	
Contrast ratio		CR	$\mathbf{a} \qquad \theta = \phi = 0^{\circ} \qquad 15 \qquad 30 \qquad \mathbf{a}$		-	-		
Brightness	ness L IF:		IF=15mA/Line	(110)	(160)	-	cd/m <sup>2</sup>	
	D.J	x	$\theta = \phi = 0^{\circ}$	(0.42)	(0.47)	(0.52)		
	Red	У	$0 = \psi = 0$	(0.30)	(0.35)	(0.40)		
	C	x	0 / 00	(0.24)	(0.29)	(0.34)		
Chromaticity	Green	У	$\theta = \phi = 0^{\circ}$	(0.48)	(0.53)	(0.58)		
coordinates	Dlass	x	$\theta = \phi = 0^{\circ}$	(0.10)	(0.15)	(0.20)	-	
	Blue	Blue $\theta = \phi = 0^{\circ}$		(0.07)	(0.12)	(0.17)		
	<b>X</b> 71	x		(0.21)	(0.26)	(0.31)		
	White y		$\theta = \phi = 0^{\circ}$	(0.25)	(0.30)	(0.35)		

## 6. Optical characteristics

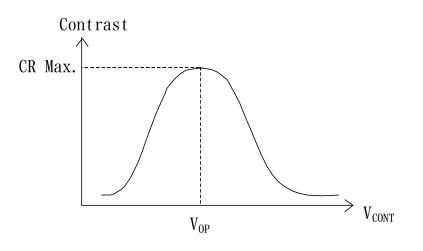
Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Optimum contrast is obtained by adjusting the LCD driving voltage (V<sub>OP</sub>) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ .

6-1. Definition of contrast ratio

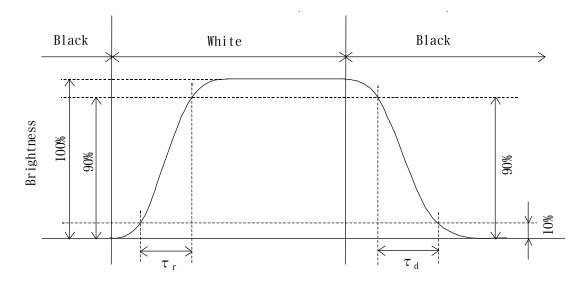
CR(Contrast ratio) = Brightness with all pixels "White" Brightness with all pixels "Black"

6-2. Definition of VOP



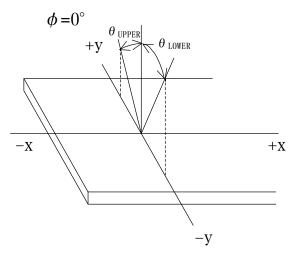


#### 6-3. Definition of response time

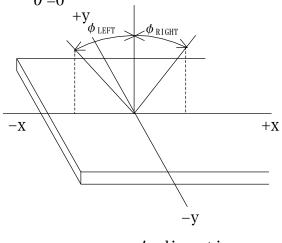


 $\theta = 0^{\circ}$ 

### 6-4. Definition of viewing angle

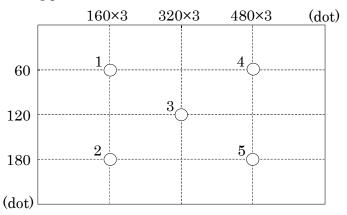


 $\theta$  direction



 $\phi$  direction

6-5. Brightness measuring points

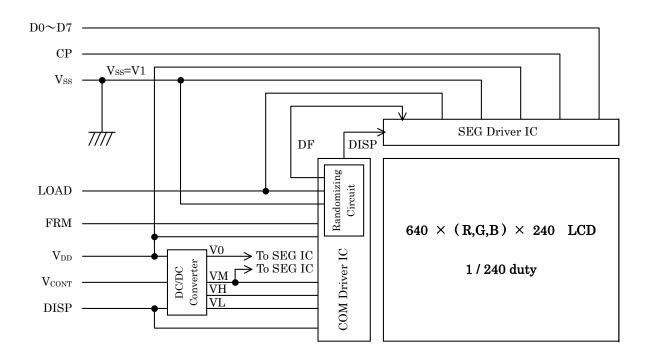


1) Rating is defined on the average in the viewing area.

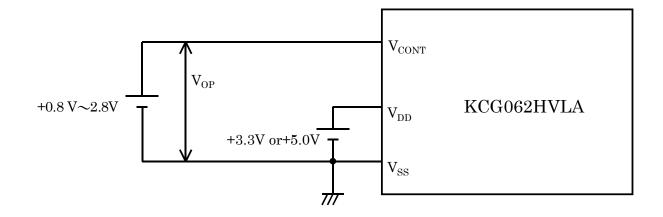
2) Measured 30 minutes after the LED is powered on. (Ambient temp. =  $25^{\circ}$ C)

3) Backlight : IF=15mA / 1 LED line

# 7. Circuit block diagram



7-1. Power supply



## 8. Interface signals

. I III assigi	. Fin assignment of LCD panel								
No.	Symbol	Description	Level						
1	FRM	Synchronous signal for driving scanning line	Н						
2	LOAD	Data signal latch clock	$\mathrm{H} \to \mathrm{L}$						
3	CP	Data signal shift clock	$\mathrm{H} \to \mathrm{L}$						
4	DISP	Display control signal	H(ON),L(OFF)						
5	V <sub>DD</sub>	Power supply for logic	-						
6	$V_{\rm SS}$	GND	-						
7	VCONT	LCD adjust voltage	-						
8	D7								
9	D6								
10	D5								
11	D4	Display data	H(ON),L(OFF)						
12	D3	Display data	$\Pi(ON), L(OFF)$						
13	D2								
14	D1								
15	D0								
16	V <sub>DD</sub>	Demon sumply for laris							
17	V <sub>DD</sub>	Power supply for logic	-						
18	$V_{\rm SS}$								
19	$V_{\rm SS}$	GND	-						
20	$V_{\rm SS}$								

8-1. Pin assignment of LCD panel

LCD connector Recommended matching FFC or FPC : 08-6210-020-340-800+ (ELCO)

: 0.5mm pitch

### 8-2. Pin assignment of LED

No.	Symbol	Description
1	AN1	Anode 1
2	AN2	Anode 2
3	AN3	Anode 3
4	AN4	Anode 4
5	CA4	Cathode 4
6	CA3	Cathode 3
7	CA2	Cathode 2
8	CA1	Cathode 1

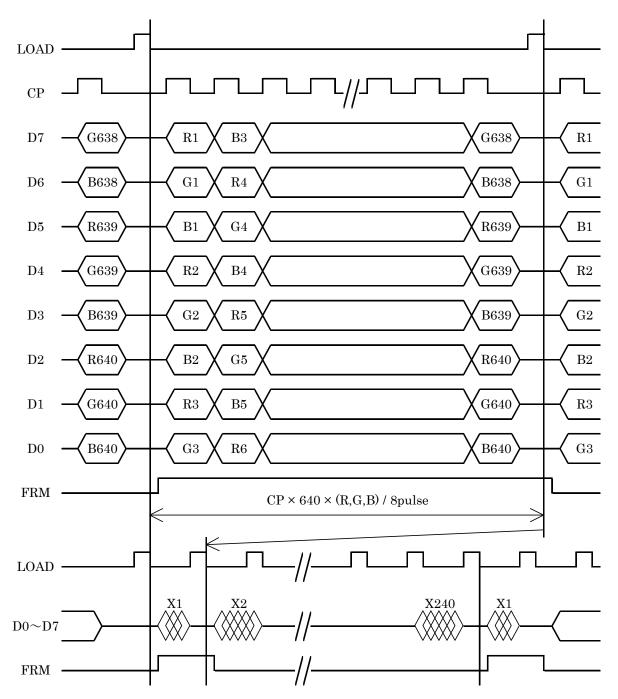
LCD side connector : FPC Recommended matching connector : IMSA-9637S-08A-GF (IRISO)

## 8-3. Pin assignment of touch panel

No.	Symbol	Description
1	уU	y-Upper terminal
2	xL	x-Left terminal
3	yL	y-Lower terminal
4	xR	x-Right terminal

Touch panel side connector	:	1.25mm pitch	
Recommended matching connector	:	Series FE,FFS	(JST)
	:	KCA-K4R	(DMC)

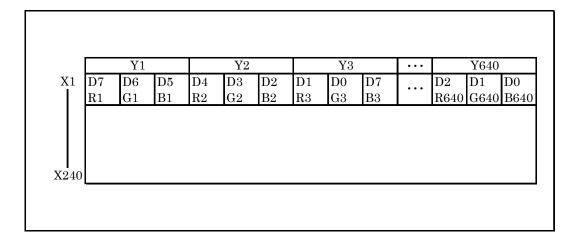
9. Interface timing chart



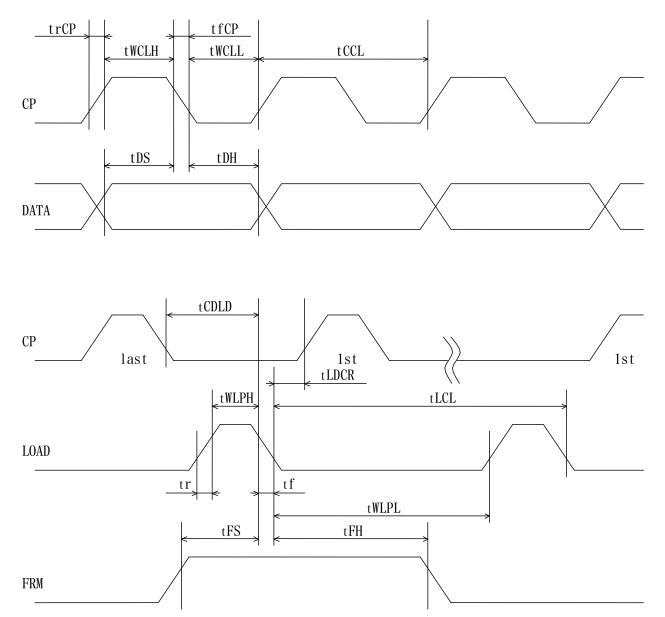
The cycle of the LOAD signal should be stable and continuously applied without interruption.
 The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.



## 10. Data and screen



### 11. Input timing characteristics



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## 11-1. Switching characteristics ( $V_{DD} = 5.0V$ )

Input characteristics : $V_{DD} = +5.0V \pm 5\%$ , Temp. = $0 \sim 50^{\circ}C$						
Item		Symbol	Min.	Max.	Unit	
CP cycle	1)	tCCL	100	-	Ns	
CP "H" pulse width		tWCLH	30	-	Ns	
CP "L" pulse width		tWCLL	30	-	Ns	
CP rise up time		trCP	-	15	Ns	
CP fall down time		tfCP	-	15	Ns	
Data set up time		tDS	25	-	Ns	
Data hold time		tDH	25	-	Ns	
LOAD "H" pulse width		tWLPH	40	-	Ns	
LOAD "L" pulse width		tWLPL	400	-	Ns	
LOAD cycle	2)	tLCL	500	-	ns	
$CP \rightarrow LOAD$ delay time		tCDLD	60	-	ns	
$LOAD \rightarrow CP$ delay time		tLDCR	60	-	ns	
Input signal rise up time		tr	-	20	ns	
Input signal fall down time		tf	-	20	ns	
FRM data set up time		tFS	120	-	ns	
FRM data hold time		$\mathrm{tFH}$	30	-	ns	

1) CP cycle is adjusted so that FRM signal is 75Hz.

2) LOAD cycle is constant.

### 11-2. Switching characteristics ( $V_{DD} = 3.3V$ )

Input characteristics : $V_{DD} = +3.3V \pm 0.3V$ , Temp. = $0 \sim 50^{\circ}C$					
Item		Symbol	Min.	Max.	Unit
CP cycle	1)	tCCL	100	-	ns
CP "H" pulse width		tWCLH	40	-	ns
CP "L" pulse width		tWCLL	40	-	ns
CP rise up time		trCP	-	20	ns
CP fall down time		tfCP	-	20	ns
Data set up time		tDS	35	-	ns
Data hold time		tDH	35	-	ns
LOAD "H" pulse width		tWLPH	50	-	ns
LOAD "L" pulse width		tWLPL	400	-	ns
LOAD cycle	2)	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time		tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time		tLDCR	80	-	ns
Input signal rise up time		tr	-	20	ns
Input signal fall down time		tf	-	20	ns
FRM data set up time		tFS	120	-	ns
FRM data hold time		$\mathrm{tFH}$	30	-	ns

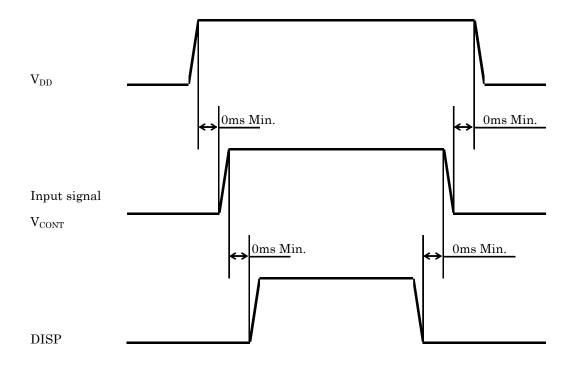
1) CP cycle is adjusted so that FRM signal is 75Hz.

2) LOAD cycle is constant.

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### 12. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on  $V_{\text{CONT}}$  outside of the time when the  $V_{\text{DD}}$  logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



1) Input signal: FRM, LOAD, CP, D0~D7

- 2) The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.
- 3) Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.



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### 13. Backlight characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	1)	IF	-	(15)	-	mA	Ta=0∼50°C
			-	(22.4)	(24.5)	V	IF=15mA, Ta=0°C
Forward voltage	1)	VF	-	(21.7)	(23.8)	V	IF=15mA, Ta=25°C
			-	(21.0)	(23.1)	V	IF=15mA, Ta=50°C
Operating life time	2), 3)	Т	-	(TBD)	-	h	IF=15mA, Ta=25°C

1) For each "AN-CA"

2) When brightness decrease 50% of minimum brightness. The average life of a LED will decrease when the LCD is operating at higher temperatures.

- 3) Life time is estimated data.
   (Condition : IF=15mA, Ta=25℃ in chamber).
- 4) An input current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

### 14. Design guidance for analog touch panel

- 14-1. Electrical (In customer's design, please remember the following considerations.)
  - 1) Do not use the current regulated circuit.
  - 2) Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maximum ratings" for details.)
  - 3) Analog Touch panel can not sense two points touching separately.
  - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the Touch panel position data.
  - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

#### 14-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.
- 14-3. Mounting on display and housing bezel
  - 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
  - 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short.
  - 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.
  - 4) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

### 15. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $\begin{array}{c|cccc} \text{KCG062HVLAL-G000} & - \square & - \square & - \square & \text{MADE IN} & \square \square \square \square \\ & \downarrow \downarrow & \downarrow & \downarrow & & \downarrow \\ & 1 & 2 & 3 & 4 & 5 \end{array}$ 

- No1. No5. above indicate
  - 1. Year code
    - 2. Month code
    - 3. Date
    - 4. Version Number
    - 5. Country of origin (Japan or China)

Year	2010	2011	2012	2013	2014	2015
Code	0	1	2	3	4	5

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

### 16. Warranty

16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

16-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

### 17. Precautions for use

### 17-1. Installation of the LCD

- 1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen. Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

5) Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque : 0.32±0.03N⋅m Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver. Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 4.2±0.5mm Washer/mounting hole (Hole diameter) : \$\$\phi\$ 3.0~\$\$\phi\$ 3.4 Please be careful not to use high torque which may damage LCD module in installation.

### 17-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

17-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Adjust the "Supply voltage for LCD driving (V<sub>CONT</sub>)" to obtain optimum viewing angle and contrast ratio.

17-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.



### 17-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by a little Ethanol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 7) Do not disassemble LCD module because it will result in damage.
- 8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



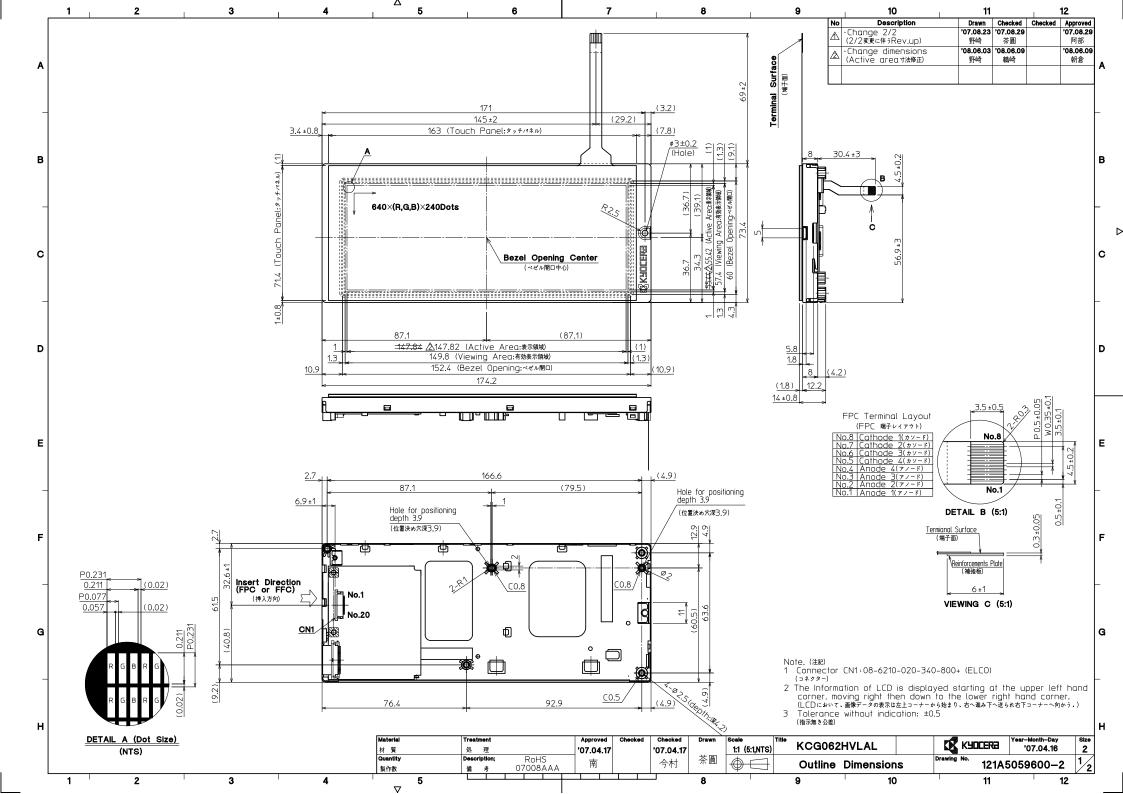
### 18. Reliability test data

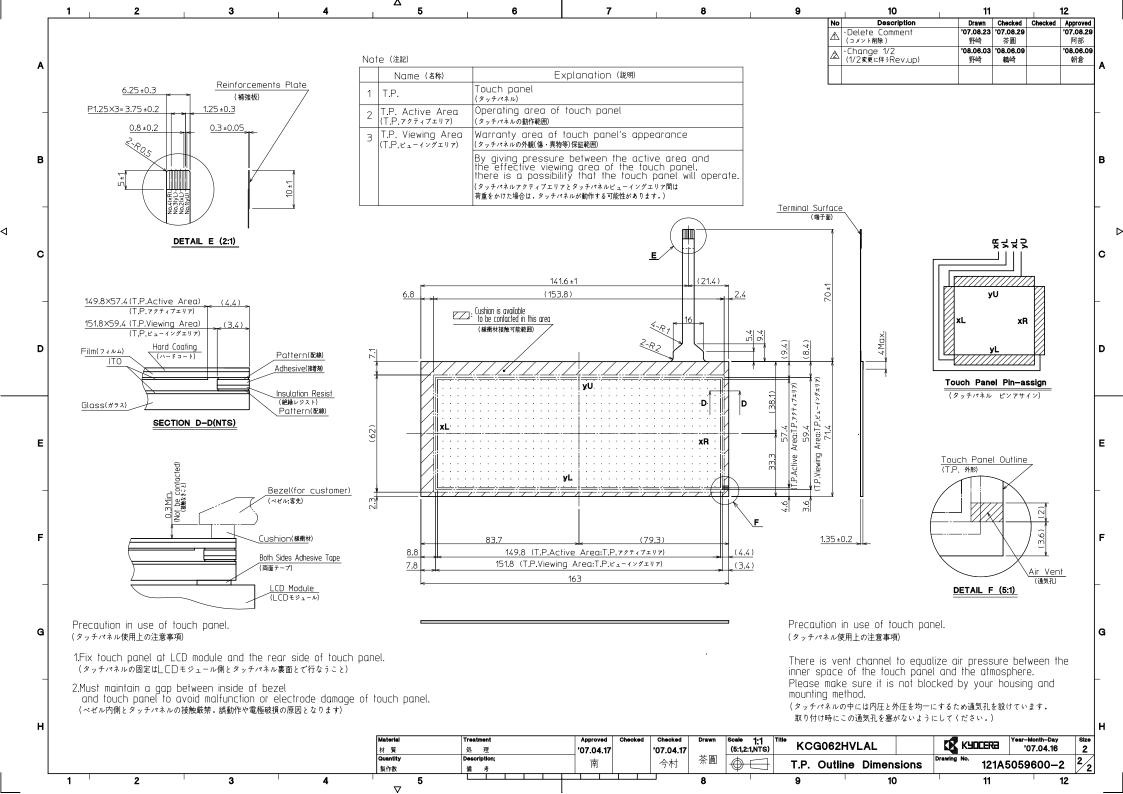
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	70°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-20°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-20°C 0.5h R.T. 0.5h 70°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	60°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Silicon rubber, Tip: R = 4.0 Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul> <li>No defect</li> <li>No defect</li> <li>No defect</li> <li>No defect</li> </ul>

1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

2) The LCD is tested in circumstances in which there is no condensation.

- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAC0-E2DEC22-00
Date	May 26, 2010

# KYOCERA INSPECTION STANDARD

# **TYPE : KCG062HVLAL-G000**

## KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by :	Engineering de	Confirmed by	: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
May 26, 2010	S. Maezuri	Y. Yamazahi	M.FujiTani	.J. Sakaguchi	Zo , Juf



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			$\mathbf{Rev}$	vision r	$\mathbf{ecord}$		
	Data			Engineering d		Confirmed by	: QA dept.
	Date	Prepa		Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	

# Visuals specification

1) Note
---------

Item		Note				
General	inspected, operating volta where optimized contras	in this Inspection Standards are age $(V_{OP})$ shall be set at the level at is available. Display quality is wing area. (Bi-level INSPECTION)				
	<ul> <li>2.This inspection standard about the image quality shall applied to any defect within the effective viewing area a shall not be applicable to outside of the area.</li> <li>3.Should any defects which are not specified in this stand happen, additional standard shall be determined by mut agreement between customer and Kyocera.</li> </ul>					
	Inspection distance : Temperature :	500 Lux minimum. 300 mm(from the sample) 25±5°C right above				
Definition of inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.				
	Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.				
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.				



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### 2)Standard

2)Standard							
Inspection item		Judg	ement star	ndard			
Pinhole, Bright spot, Black spot, Foreign particle	$ \underbrace{ \begin{array}{c} & b \\ \hline \\ \hline \\ \hline \\ \hline \\ \end{array} \end{array} } $						
	d = (a + b) / 2 Category Size(mm) Acceptable number						
	A				Neglected		
	В			5			
	С	$0.3 < d \le 0.5$		3			
	D	0.5< m d		0			
Scratch, Foreign particle	W K L						
	V	Vidth (mm)	Length (mm)		Acceptable number		
	А	$W \leq 0.03$	-		Neglected		
	В			≦2.0	Neglected		
		$\leq W \leq 0.10$	$2.0 \le L$	$\leq 4.0$	3		
	D E 0.10		4.0< L		0 According to		
	E 0.10	< VV			'Circular'		
					enoului		
Contrast variation	$\begin{vmatrix} a \\ \hline a \\ \hline a \\ \hline d = (a + b) / 2$						
	Category Size (mm) Acceptable number			ptable number			
	A	d	$d \leq 0.5$		Neglected		
	В	0.5 < d			3		
	$\begin{array}{c c c c c c c c c c c c c c c c c c c $			0			



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Inspection item		Judgement standard								
Polarizer (Scratch, Bubble, Dent)	(1	(1) Scratch								
	ſ									
			V	/idth (mm)	Length	(mm)	Acceptable No.			
		А		W ≦0.1	-		Neglected			
		В	0.1<	$\leq W \leq 0.3$	$L \leq 5.0$		Neglected			
		С			5.0< L		0			
		D	0.3<	< W	-		0			
		d = (a + b) / 2								
		Cate	gory	Size (	mm)	Acce	eptable number			
		A B C		$\mathrm{d} \leq 0.2$		Neglected				
				$0.2  <  \mathrm{d}$	$\leq 0.3$		5			
				0.3 < d	$\leq 0.5$		3			
				0.5< m d						



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<u>г</u>								
Inspection item	Judgement standard							
Scratch,	(W = Width, L = Length, D = Diameter = (major axis + minor axis)/2)							
Foreign particle	Item			Acc	acceptable number			
(Touch screen		$W \leq 0.03$	$L \leq 20$		Neglected			
portion)		$0.03 < W \le 0.05$	$L \leq 10$	2pcs	s within $\phi 20$ mm			
	Scratch	$0.05 < W \le 0.08$	$0.05 < W \le 0.08$ $L \le 6$		2pcs within $\phi$ 20mm			
		$0.08 < W \le 0.1$	$0.08 < W \le 0.1$ $L \le 4$		1pcs within φ30mm			
	Foreign	$W \leq 0.05$	Neglected		Neglected			
	(line like)	$0.05 < W \le 0.1$	$L \leq 5$	2pcs	s within $\phi$ 30mm			
	Foreign	D≦	0.2		Neglected			
	(circle like)	$0.2 < D \leq$	0.3	2pcs	s within $\phi$ 30mm			
	Above are app	lied to the visible area.						
	Unless there	are foreign particle a	and damage a	affected	d seriously to the			
	electrical perfe	ormance out of the visib	le area, we ap	prove o	of this product.			
Glass crack								
(Touch screen	T.	<b>C:</b> (	``		Acceptable			
portion)	Item	Size (n	nm)		number			
			X	$\leq 3$				
			7					
	Corner	~/./	-		2  pcs			
	crack	\X\X'/	Y	$\leq 3$	/panel			
		$\checkmark$	<u> </u>					
			Z	<t				
				~0				
			X	$\leq 5$				
	Crack in	x / 🔀	×					
	other area				2  pcs			
	than in		Y	$\leq 1.5$	/side			
	corner	$\searrow$						
		3	Z	<t				
				~0				
	Duo muo a sirro	-	//		0 pcs			
	Progressive crack							
	CLACK	$\langle \gamma \rangle$	J		(NG even 1pcs)			
		$\checkmark$						
	Above are applied to the visible area.							
	Unless there are foreign particle and damage affected seriously to the							
	electrical performance out of the visible area, we approve of this product.							

