SPEC

Spec No.	TQ3C-8EAS0-E1DAM26-00
Date	April 19, 2007

#### TYPE: KG057QVLCD-G060

< 5.7 inch QVGA transflective monochrome STN with LED backlight>

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KYDEERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original		Designed by: l	Engineering de <sub>l</sub>	dept. Confirmed		y: QA dept.	
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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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#### Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
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Rev.No.	Date	Page			Descripti	ons	



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# 1. Application

This document defines the specification of KG057QVLCD-G060. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transflective monochrome dot matrix type STN

Duty ratio : 1/240 duty Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Bias voltage circuit, Randomizing circuit,

DC/DC converter circuit, Temperature compensation circuit

#### 3. Mechanical specifications

Item	Item Specification	
Outline dimensions	Outline dimensions $144 \text{ (W)} \times (104.8) \text{ (H)} \times 12.7 \text{ (D)}$	
Active area	115.18 (W) × 86.38 (H) (14.4cm / 5.7 inch (Diagonal))	
Effective viewing area 117.2 (W) × 88.4 (H)		mm
ot format 320 (W) × 240 (H)		dot
Dot size $0.34 \text{ (W)} \times 0.34 \text{ (H)}$		mm
Dot pitch	0.36 (W) × 0.36 (H)	mm
Base color *1	ase color *1 Normally Black	
Mass	175	g

<sup>\*1</sup> Due to the characteristics of the LCD material, the color varies with environmental temperature.



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#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{\mathrm{DD}}$	0	6.0	V
Supply voltage for LCD driving	VCONT	0	$V_{ m DD}$	V
Input signal voltage *1	$V_{\rm IN}$	0	$ m V_{DD}$	V
FRM frequency	${ m f_{FRM}}$	-	150	$_{ m Hz}$
LED forward current *2	IF	-	27	mA
Reversed voltage *2	VR	-	5	V

<sup>\*1</sup> Input signal : FRM, LOAD, CP, DISP, D0 $\sim$ D3

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	65	$^{\circ}\mathrm{C}$
Storage temperature	*2	Tsto	-30	80	°C
Operating humidity	*3	$H_{\mathrm{OP}}$	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

<sup>\*1</sup> Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

\*6 Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



<sup>\*2</sup> For each "AN1-CA1", "AN2-CA2" and "AN3-CA3"

<sup>\*2</sup> Temp. = -30°C< 48h, Temp. = 80°C< 168h

<sup>\*3</sup> Non-condensing

<sup>\*4</sup> Temp. ≤ 40°C, 85%RH Max.

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#### 5. Electrical characteristics

 $5-1. V_{DD} = 5.0V$ 

 $V_{DD} = +5.0V \pm 5\%$ , Temp. = -20 $\sim$ 65°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	-	4.75	5.00	5.25	V
		-20°C∼	0.80	-	-	V
Supply voltage for LCD driving *1, *2	$V_{CONT} = V_{OP}$	0∼50°C *3	1.45	1.95	2. 45	V
		~65°C	-	-	2.80	V
Input signal voltage	7.7	"High" level	$0.8V_{\mathrm{DD}}$	-	$V_{\mathrm{DD}}$	V
(FRM,LOAD,CP,DISP,D0~D3)	$V_{\mathrm{IN}}$	"Low" level	0	-	V	
Input current	$I_{\rm IN}$	Input signal	-100	-	100	μA
Rush current for logic	$I_{ m RUSH}$	When LCD turn on.	3.0A (Peak) × 1ms			
Clock frequency	$f_{\mathrm{CP}}$	-	-	-	10.00	MHz
Frame frequency *4	$f_{\mathrm{FRM}}$	-	70	75	80	Hz
Current consumption for logic	$I_{\mathrm{DD}}$	*5	-	17	22	mA
Power consumption	P <sub>DISP</sub>	9	-	85	110	mW

<sup>\*1</sup> Maximum contrast ratio is obtained by adjusting the LCD supply voltage ( $V_{CONT} = V_{OP}$ ) for driving the LCD.

#### \*5 Display pattern:



<sup>\*2</sup> Frame frequency :  $f_{FRM} = 75Hz$ 

<sup>\*3</sup> The LCD module has a temperature compensation circuit.

<sup>\*4</sup> In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

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 $5-2. V_{DD} = 3.3V$ 

 $V_{DD} = +3.3V \pm 0.3V$ , Temp. = -20 $\sim$ 65°C

					- I		
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	
Supply voltage for logic	$V_{ m DD}$	-	3.0	3.3	3.6	V	
		-20°C∼	0.80	-	-	V	
Supply voltage for LCD driving *1, *2	V <sub>CONT</sub> =V <sub>OP</sub>	0∼50°C *3	1.45	1.95	2. 45	V	
-, <b>-</b>		~65°C	-	-	3.6  - 2.45  2.80  VDD  0.2VDD  100  100  100  100  100  100  100	V	
Input signal voltage	77	"High" level	$0.8 V_{\mathrm{DD}}$	.8V <sub>DD</sub> - V <sub>DD</sub>			
(FRM,LOAD,CP,DISP,D0∼D3)	$ m V_{IN}$	"Low" level	0	-	V		
Input current	$I_{\rm IN}$	Input signal	-100	-	100	μA	
Rush current for logic	Irush	When LCD turn on.		3.0A (Peak) × 1ms			
Clock frequency	$\mathbf{f}_{\mathrm{CP}}$	-	-	-	10.00	MHz	
Frame frequency *4	$\mathbf{f}_{\mathrm{FRM}}$	-	70	75	80	$_{ m Hz}$	
Current consumption for logic	$I_{\mathrm{DD}}$	*5	-	25	33	mA	
Power consumption	$P_{\mathrm{DISP}}$	G	-	83	109	mW	

<sup>\*1</sup> Maximum contrast ratio is obtained by adjusting the LCD supply voltage (V<sub>CONT</sub>=V<sub>OP</sub>) for driving the LCD.

#### \*5 Display pattern:



<sup>\*2</sup> Frame frequency :  $f_{FRM} = 75Hz$ 

<sup>\*3</sup> The LCD module has a temperature compensation circuit.

<sup>\*4</sup> In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

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# 6. Optical characteristics

#### 6-1. Reflective mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	τг	$\theta = \phi = 0^{\circ}$	-	380	480	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	120	220	ms
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	(2.0)	(4.0)	-	-
Reflectance		ρ	Backlight off	(4.0)	(8.5)	-	%

Optimum contrast is obtained by adjusting the LCD driving voltage ( $V_{OP}$ ) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ .

#### 6-2. Trasmissive mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Weasuring spot – $\psi$				$\varphi$ 0.0111111, 10	7111p. 20 C		
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D 4:	Rise	τr	$\theta = \phi = 0^{\circ}$	-	380	480	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	120	220	ms
		heta upper		-	20	-	1
<b>1</b> 77 1		$\theta_{ m LOWER}$	CR≧2	-	35	-	deg.
Viewing angle range  Contrast ratio		$\phi$ left	CR≦2	-	35	-	deg.
		$\phi$ right		-	35	-	
		CR	$\theta = \phi = 0^{\circ}$	(2.0)	(3.0)	-	-
Brightness		L	IF=25mA/Line	(100)	(150)	-	cd/m²
	<b>XX</b> 71- *4 -	X	$\theta = \phi = 0^{\circ}$	TBD	TBD	TBD	
Chromaticity	White	У	$U - \Psi - U$	TBD	TBD	TBD	
coordinates	D11-	X	$\theta = \phi = 0^{\circ}$	TBD	TBD	TBD	-
	Black	У	$\sigma - \psi - 0^{\circ}$	TBD	TBD	TBD	

Optimum contrast is obtained by adjusting the LCD driving voltage ( $V_{OP}$ ) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ .

#### 6-3. Definition of reflectance

$$\rho \, (\text{Reflectance}) \, = \, \frac{\text{Measured reflection brightness}}{\text{Reflection brightness against standard white board}} \, \times 100 [\%]$$

6-4. Definition of contrast ratio (Reflective mode)

$$CR(Contrast \ ratio) \ = \ \frac{Reflectance \ at \ all \ pixel \ "white"}{Reflectance \ at \ all \ pixel \ "black"} \ \times 100[\%]$$

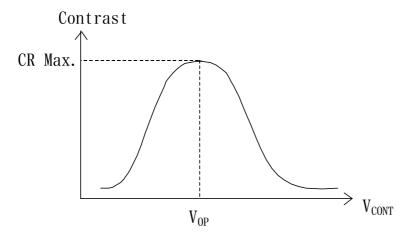


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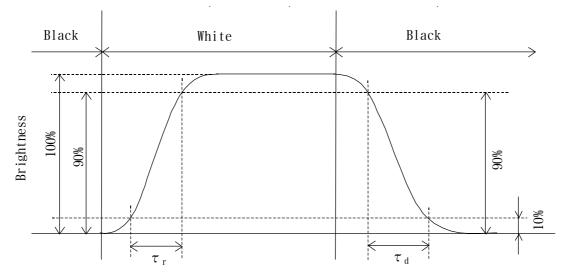
#### 6-5. Definition of contrast ratio (Transmissive mode)

$$CR(Contrast\ ratio)\ =\ \frac{Brightness\ at\ all\ pixel\ "white"}{Brightness\ at\ all\ pixel\ "black"}\ \times 100[\%]$$

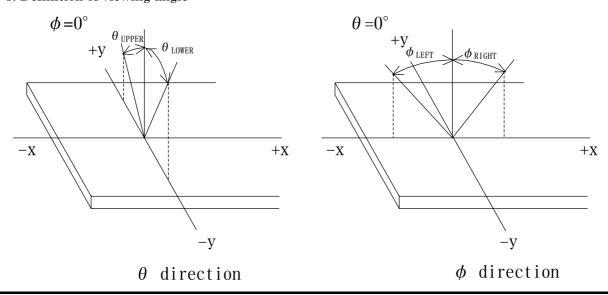
#### 6-6. Definition of VOP



### 6-7. Definition of response time

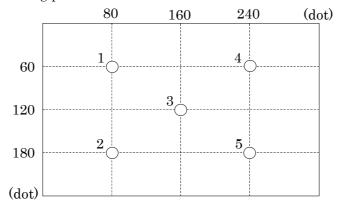


#### 6-8. Definition of viewing angle



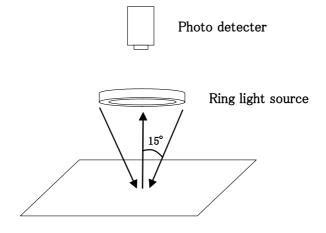
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### 6-9. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)
- 3) Backlight: IF=25mA/1 LED line

#### 6-10. Measurement method of reflectance

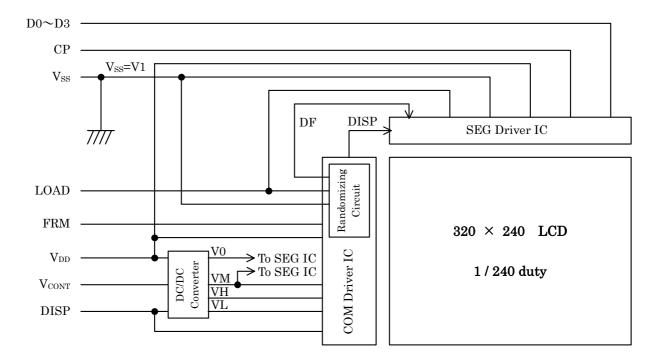


Measurement: 52002 (YOKOGAWA)

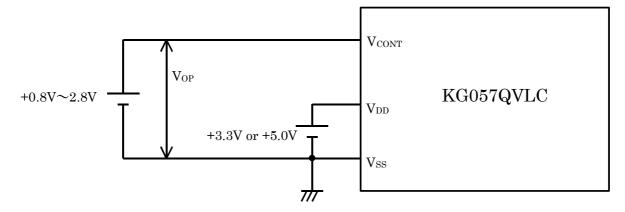


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# 7. Circuit block diagram



#### 7-1. Power supply





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# 8. Interface signals

# 8-1. Pin assignment of LCD panel

No.	Symbol	Description	Level
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	$\mathrm{H}  ightarrow \mathrm{L}$
3	CP	Data signal shift clock	$\mathrm{H}  ightarrow \mathrm{L}$
4	DISP	Display control signal	H(ON),L(OFF)
5	$V_{ m DD}$	Power supply for logic	-
6	$ m V_{SS}$	GND	-
7	$V_{\mathrm{CONT}}$	LCD adjust voltage	-
8	N.C.		
9	N.C.	No connect	_
10	N.C.	No connect	-
11	N.C.		
12	D3		
13	D2	Diaplay data	H(ON),L(OFF)
14	D1	Display data	H(ON),L(OFF)
15	D0		
16	$V_{ m DD}$	Down cumply for logic	
17	$V_{ m DD}$	Power supply for logic	_
18	$V_{\rm SS}$		
19	$V_{\rm SS}$	GND	-
20	$V_{\rm SS}$		

LCD connector : 08-6210-020-340-800+ (ELCO)

Recommended matching FFC or FPC : 0.5mm pitch

#### 8-2. Pin assignment of LED

No.	Symbol	Description
1	AN1	Anode 1
2	AN2	Anode 2
3	AN3	Anode 3
4	CA1	Cathode 1
5	CA2	Cathode 2
6	CA3	Cathode 3

LCD side connector : SHLP-06V-S-B (JST)

Recommended matching connector

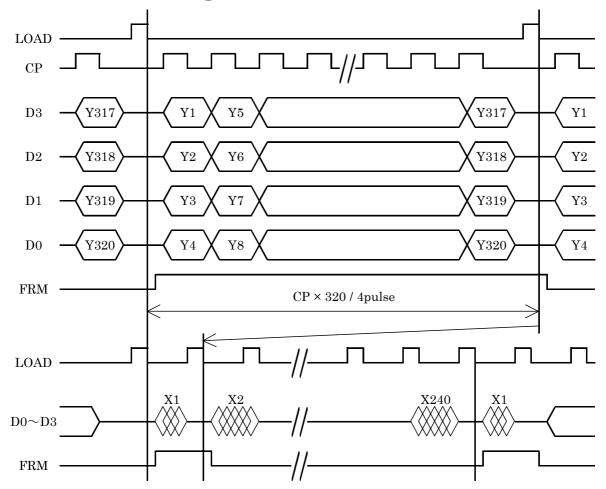
: SM06B-SHLS-TF (JST)

: SM06B-SHLS-TF(LF)(SN) (JST)···(RoHS Compliant)



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# 9. Interface timing chart



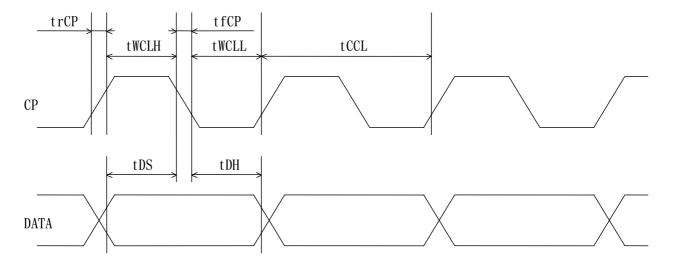
<sup>\*</sup> The cycle of the LOAD signal should be stable and continuously applied without interruption.

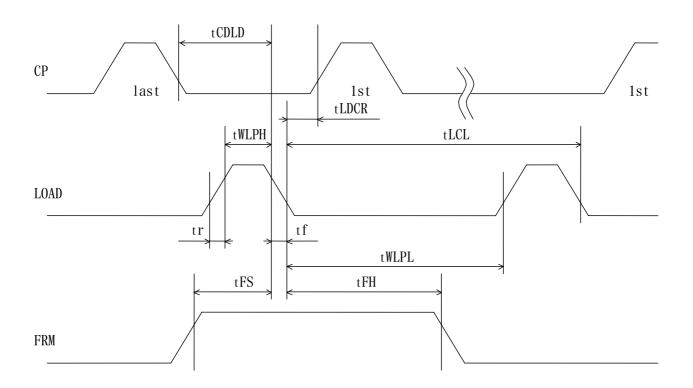


<sup>\*</sup> The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

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# 10. Input timing characteristics







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# $\overline{10\text{-}1}$ . Switching characteristics ( $V_{DD} = 5.0V$ )

Input characteristics :  $V_{DD} = +5.0V \pm 5\%$ , Temp. =  $0 \sim 60^{\circ}$ C

Item	Symbol	Min.	Max.	Unit
			wax.	Cint
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	30	-	ns
CP "L" pulse width	tWCLL	30	-	ns
CP rise up time	trCP	-	15	ns
CP fall down time	tfCP	-	15	ns
Data set up time	tDS	25	-	ns
Data hold time	tDH	25	-	ns
LOAD "H" pulse width	tWLPH	40	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *2	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time	tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time	tLDCR	60	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.

# 10-2. Switching characteristics ( $V_{DD} = 3.3V$ )

Input characteristics :  $V_{DD} = +3.3V \pm 0.3V$ , Temp. =  $0 \sim 60^{\circ}$ C

Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	40	-	ns
CP "L" pulse width	tWCLL	40	-	ns
CP rise up time	trCP	-	20	ns
CP fall down time	tfCP	-	20	ns
Data set up time	tDS	35	-	ns
Data hold time	tDH	35	-	ns
LOAD "H" pulse width	tWLPH	50	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *2	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time	tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time	tLDCR	80	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.



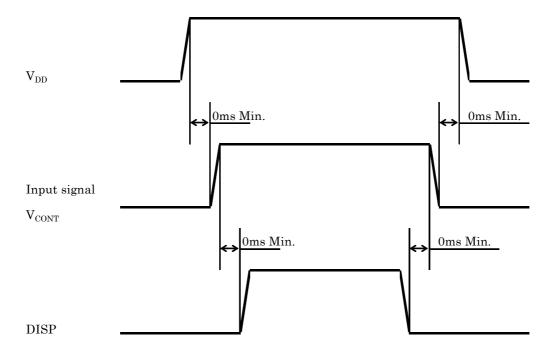
<sup>\*2</sup> LOAD cycle is constant.

<sup>\*2</sup> LOAD cycle is constant.

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# 11. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on  $V_{\text{CONT}}$  outside of the time when the  $V_{DD}$  logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



<sup>\*</sup> Input signal: FRM, LOAD, CP, D0~D3



<sup>\*</sup> The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.

<sup>\*</sup> Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.

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# 12. Backlight characteristics

#### LED ratings

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	*1	IF	•	25	-	mA	Ta=-20∼65°C
			-	(24.5)	(27.3)	V	IF=25mA, Ta=-20°C
Forward voltage	*1	VF	-	(23.1)	(25.9)	V	IF=25mA, Ta=25°C
			-	(22.1)	(24.9)	V	IF=25mA, Ta=65°C
Operating life time	*2, *3	Т	-	(50,000)	-	h	IF=25mA, Ta=25°C

<sup>\*1</sup> For each "AN1-CA1", "AN2-CA2" and "AN3-CA3"



<sup>\*2</sup> When brightness decrease 50% of initial brightness.

<sup>\*3</sup> Life time is estimated data.

<sup>\*</sup> An input current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

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#### 13. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Code         7         8         9         0         1         2	

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 14. Warranty

### 14-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 14-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 15. Precautions for use

#### 15-1. Installation of the LCD

- 1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
- A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.
- 5) The display window size should be the same as the effective viewing area.
- 6) Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque: 0.32±0.03N·m

Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver.

Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5±0.5mm

Please be careful not to use high torque which may damage LCD module in installation.

7) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 15-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 15-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Adjust the "Supply voltage for LCD driving (VCONT)" to obtain optimum viewing angle and contrast ratio.

#### 15-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 15-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 7) Do not disassemble LCD module because it will result in damage.
- 8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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# 16. Reliability test data

Test item	Test condition	Test time	Judgement	
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	65°C	500h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>

<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

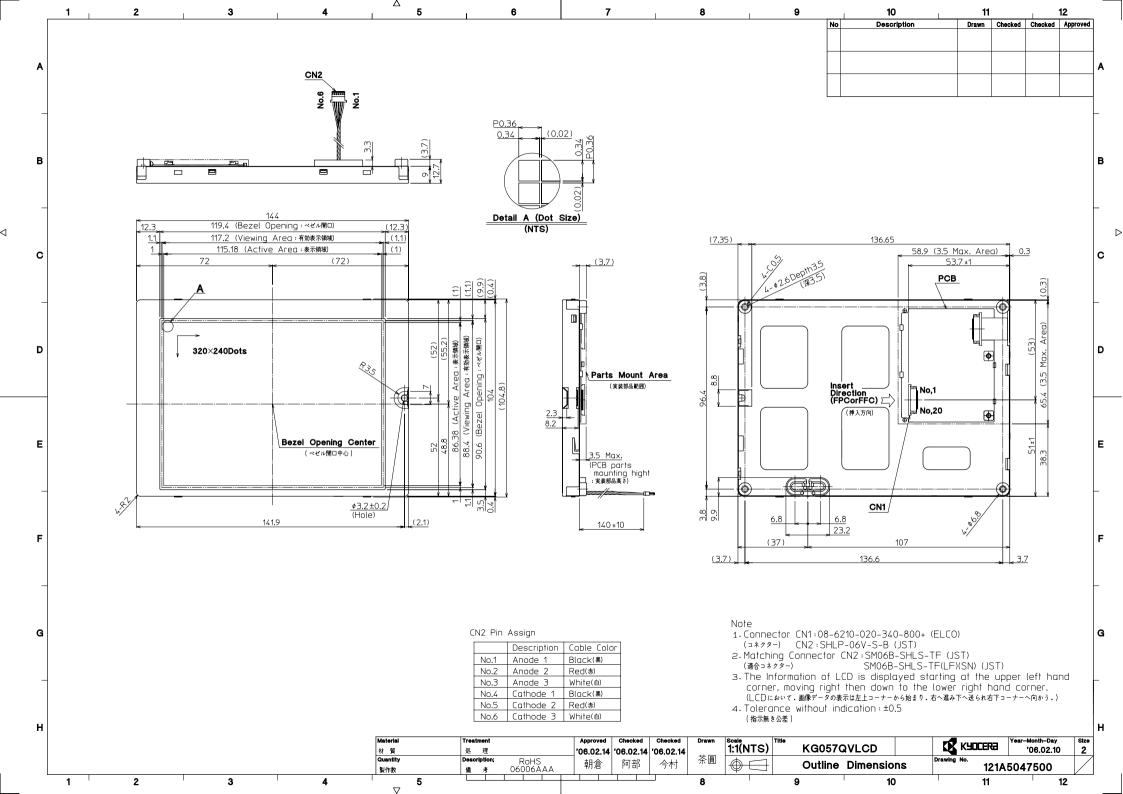


<sup>\*</sup> The LCD is tested in circumstances in which there is no condensation.

<sup>\*</sup> The reliability test is not an out-going inspection.

<sup>\*</sup> The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.



Spec No.	TQ3C-8EAS0-E2DAM25-00
Date	April 19, 2007

# **KYOCERA INSPECTION STANDARD**

 ${\bf TYPE: KG057QVLCD\text{-}G060}$ 

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 19, 2007	O. Ajisaka	S. Oshita	4 Matsumoto	J. Sakaguchi	To Sul



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# Revision record

	Data	Designe	d by:	Engineering of	lept.	Confirmed by : QA dept.	
	Date	Prepared		Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	



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# Visuals specification 1) Note

inspected, operating volve where optimized contral applied up to effective views.  2. This inspection standard applied to any defect with shall not be applicable to a shall not be applicable	h are not specified in this standard dard shall be determined by mutual mer and Kyocera.  500 Lux minimum. 300 mm(from the sample) 25±5°C		
applied to any defect we shall not be applicable to  3. Should any defects which happen, additional standagreement between custo  4. Inspection conditions  Luminance  Inspection distance  Temperature	thin the effective viewing area and outside of the area.  h are not specified in this standard dard shall be determined by mutual mer and Kyocera.  500 Lux minimum. 300 mm(from the sample) 25±5°C		
happen, additional standagreement between custo  4.Inspection conditions  Luminance  Inspection distance  Temperature	lard shall be determined by mutual mer and Kyocera.  500 Lux minimum. 300 mm(from the sample) 25±5°C		
Luminance : Inspection distance : Temperature :	300 mm (from the sample) $25{\pm}5^{\circ}\mathrm{C}$		
Luminance : 500 Lux minimum.  Inspection distance : 300 mm(from the sample)  Temperature : 25±5°C			
Did I Did	m 1 e 11 ·		
Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.		
Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.		
Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.		
	Foreign particle  Contrast variation  Polarizer		



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#### 2)Standard

Inspection item		Judg	ement stand	lard		
Pinhole, Bright spot, Black spot, Foreign particle						
	d = (a + b)/2 Category Size(mm) Acceptable number					
	Category		$\leq 0.2$			
	B	0.2 < d		Neglecte 5	eu	
	С	0.2 < d 0.3 < d		3		
	D	0.5 < d		0		
Scratch, Foreign particle	L					
		Width (mm)	Length (		ptable nber	
	A	W ≤0.03	-	<u> </u>	lected	
	В	· · · = 0.00	I. ≤		lected	
		3< W ≤0.10	2.0< L \( \)		3	
	D	, , ,, = 0.10	4.0< L		0	
		)< W	-	Accor	ding to	
Contrast variation			d = ( a +	b)/2		
	Category Size (mm) Acceptable numb					
	Category	Size (	(mm)	Acceptable n	umber	
	Category			Acceptable n		
	Category A B		$l \le 0.5$	Acceptable n  Neglecte		



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Inspection item	Judgement standard			
Polarizer (Scratch, Bubble, Dent)	(1) Scratch  W			
(Scratch, Bubble, Bellt)				
		Width (mm)	Length (mm)	Acceptable No.
	A	W ≦0.1	-	Neglected
	В	0.1< W ≤0.3	L ≦5.0	Neglected
	C 0.1	0.1 < W ≡0.5	5.0< L	0
	D	0.3< W	-	0
	a			
	d = (a + b) / 2			
	Catego			cceptable number
	A	+	$1 \le 0.2$	Neglected
	В	0.2 < d		5
		1 09 / 1	$\geq 0.5$	n
	D C	0.3 < d 0.5 < d		0

