SPEC

| Spec No. | TQ3C-8EAC0-E1DKJ01-00 |
|----------|-----------------------|
| Date     | June 2, 2008          |

## TYPE: KHG047QVLAB-G000

< 4.7 inch QVGA transflective color STN with LED backlight >

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

| Original     | Designed by: I | Engineering dep | ept. Confirmed by: QA dept. |              | QA dept. |
|--------------|----------------|-----------------|-----------------------------|--------------|----------|
| Issue Date   | Prepared       | Checked         | Approved                    | Checked      | Approved |
| June 2, 2008 | y deeno        | 7 Johnson       | T. Minami                   | J. Sakaguchi | H. Sul   |

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## Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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#### Revision record

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## 1. Application

This document defines the specification of KHG047QVLAB-G000. (RoHS Compliant)

## 2. Construction and outline

LCD : Transflective color dot matrix type STN

Duty ratio : 1/240 duty Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Bias voltage circuit, Randomizing circuit,

DC/DC converter circuit, Temperature compensation circuit

With Constant current circuit for LED backlight

## 3. Mechanical specifications

| Item                                 | Specification  |     |
|--------------------------------------|--|-----|
| Outline dimensions                   | line dimensions  122.4 (W)× 90 (H) × 5.7 (D)  (PCB and bosses are not included)  *Refer outline drawing in detail. |     |
| Active area                          | 95.98 (W) × 71.98 (H)<br>(12.0cm / 4.7 inch (Diagonal))  | mm  |
| Effective viewing area               | 98 (W) × 74 (H)  | mm  |
| Dot format 320×(R,G,B) (W) × 240 (H) |  | dot |
| Dot size                             | $0.08 \text{ (W)} \times 0.28 \text{ (H)}$   | mm  |
| Dot pitch                            | $0.01 \text{ (W)} \times 0.3 \text{ (H)}$  | mm  |
| Base color *1                        | Normally Black   | -   |
| Mass                                 | 90   | g   |

<sup>\*1</sup> Due to the characteristics of the LCD material, the color varies with environmental temperature.



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## 4. Absolute maximum ratings

### 4-1. Electrical absolute maximum ratings

| Item                           | Symbol       | Min. | Max.        | Unit       |
|--------------------------------|--------------|------|-------------|------------|
| Supply voltage for logic       | $V_{ m DD}$  | 0    | 6.0         | V          |
| Supply voltage for LCD driving | VCONT        | 0    | $ m V_{DD}$ | V          |
| Input signal voltage *1        | $V_{\rm IN}$ | 0    | $ m V_{DD}$ | V          |
| FRM frequency                  | $f_{ m FRM}$ | -    | 150         | $_{ m Hz}$ |
| LED forward current            | IF           | -    | 12          | mA         |
| Supply voltage for backlight   | VinB         | 0    | 6.0         | V          |
| Backlight ON-OFF               | BLEN         | 0    | VinB        | V          |
| Brightness adjust voltage      | VBRT         | 0    | VinB        | V          |

<sup>\*1</sup> Input signal: FRM, LOAD, CP, DISP, D0~D7

### 4-2. Environmental absolute maximum ratings

| Item                  |    | Symbol | Min. | Max. | Unit                 |
|-----------------------|----|--------|------|------|----------------------|
| Operating temperature | *1 | Тор    | -20  | 70   | $^{\circ}\mathrm{C}$ |
| Storage temperature   | *2 | Tsto   | -30  | 80   | °C                   |
| Operating humidity    | *3 | Нор    | 10   | *4   | %RH                  |
| Storage humidity      | *3 | Нѕто   | 10   | *4   | %RH                  |
| Vibration             |    | -      | *5   | *5   | -                    |
| Shock                 |    | -      | *6   | *6   | -                    |

<sup>\*1</sup> Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

| Frequency       | 10∼55 Hz | Acceleration value          |
|-----------------|----------|-----------------------------|
| Vibration width | 0.15mm   | $(0.3\sim 9 \text{ m/s}^2)$ |
| Interval        | 10-55-10 | Hz 1 minutes                |

<sup>2</sup> hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

 $EIAJ\ ED\text{-}2531$ 



<sup>\*2</sup> Temp. = -30°C< 48h , Temp. = 80°C< 168h

<sup>\*3</sup> Non-condensing

<sup>\*4</sup> Temp. ≤ 40°C, 85%RH Max.

<sup>\*6</sup> Acceleration: 490 m/s², Pulse width: 11 ms

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### 5. Electrical characteristics

 $5-1. V_{DD} = 5.0V$ 

 $V_{DD} = +5.0V \pm 5\%$ , Temp. = -20 $\sim$ 70°C

| Item                                  | Symbol                            | Condition         | Min.                 | Тур. | Max.              | Unit       |
|---------------------------------------|-----------------------------------|-------------------|----------------------|------|-------------------|------------|
| Supply voltage for logic              | $ m V_{DD}$                       | -                 | 4.75                 | 5.00 | 5.25              | V          |
|                                       |                                   | -20°C∼            | 0.80                 | -    | -                 | V          |
| Supply voltage for LCD driving *1, *2 | $V_{\text{CONT}} = V_{\text{OP}}$ | 0∼50°C *3         | 1.30                 | 1.80 | 2.30              | V          |
|                                       |                                   | ~70°C             | -                    | -    | 2.80              | V          |
| Input signal voltage                  | ***                               | "High" level      | $0.8V_{\mathrm{DD}}$ | -    | $V_{\mathrm{DD}}$ | V          |
| (FRM,LOAD,CP,DISP,D0∼D7)              | $V_{ m IN}$                       | "Low" level       | 0                    | -    | $0.2 V_{ m DD}$   | V          |
| Input current                         | $I_{\rm IN}$                      | Input signal      | -100                 | -    | 100               | μA         |
| Rush current for logic                | $I_{ m RUSH}$                     | When LCD turn on. | 3.0A (Peak) × 1ms    |      |                   |            |
| Clock frequency                       | $\mathbf{f}_{\mathrm{CP}}$        | -                 | -                    | -    | 10.00             | MHz        |
| Frame frequency *4                    | ${ m f_{FRM}}$                    | -                 | 70                   | 75   | 80                | $_{ m Hz}$ |
| Current consumption for logic         | $I_{\mathrm{DD}}$                 | *5                | -                    | 30   | 45                | mA         |
| Power consumption                     | P <sub>DISP</sub>                 |                   | -                    | 150  | 225               | mW         |

<sup>\*1</sup> The supply voltage ( $V_{CONT} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.

### \*5 Display pattern:



<sup>\*2</sup> Frame frequency :  $f_{FRM} = 75Hz$ 

<sup>\*3</sup> The LCD module has a temperature compensation circuit.

<sup>\*4</sup> In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

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 $5-2. V_{DD} = 3.3V$ 

 $V_{DD} = +3.3V \pm 0.3V$ , Temp. = -20 $\sim$ 70°C

| Item                                  | Symbol                             | Condition         | Min.                 | Тур. | Max.                  | Unit |
|---------------------------------------|------------------------------------|-------------------|----------------------|------|-----------------------|------|
| Supply voltage for logic              | $V_{ m DD}$                        | -                 | 3.0                  | 3.3  | 3.6                   | V    |
|                                       |                                    | -20°C             | 0.80                 | -    | -                     | V    |
| Supply voltage for LCD driving *1, *2 | V <sub>CONT</sub> =V <sub>OP</sub> | 0∼50°C *3         | 1.30                 | 1.80 | 2.30                  | V    |
|                                       |                                    | 70°C              | -                    | -    | 2.80                  | V    |
| Input signal voltage                  | 77                                 | "High" level      | $0.8V_{\mathrm{DD}}$ | -    | $V_{ m DD}$           | V    |
| (FRM,LOAD,CP,DISP,D0~D7)              | $ m V_{IN}$                        | "Low" level       | 0                    | -    | $0.2 V_{\mathrm{DD}}$ | V    |
| Input current                         | $I_{\rm IN}$                       | Input signal      | -100                 | -    | 100                   | μA   |
| Rush current for logic                | Irush                              | When LCD turn on. | 3.0A (Peak) × 1ms    |      |                       |      |
| Clock frequency                       | $ m f_{CP}$                        | -                 | -                    | -    | 10.00                 | MHz  |
| Frame frequency *4                    | $f_{\mathrm{FRM}}$                 | -                 | 70                   | 75   | 80                    | Hz   |
| Current consumption for logic         | $I_{ m DD}$                        | *5                | -                    | 45   | 68                    | mA   |
| Power consumption                     | $P_{\mathrm{DISP}}$                |                   | -                    | 150  | 225                   | mW   |

<sup>\*1</sup> The supply voltage ( $V_{CONT} = V_{OP}$ ) to drive the display has individual difference. Please adjust the contrast to be most suitable.

### \*5 Display pattern:



<sup>\*2</sup> Frame frequency :  $f_{FRM} = 75Hz$ 

<sup>\*3</sup> The LCD module has a temperature compensation circuit.

<sup>\*4</sup> In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

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# 6. Optical characteristics

## 6-1. Reflective mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

| Item           |      | Symbol | Condition                   | Min. | Тур. | Max. | Unit |
|----------------|------|--------|-----------------------------|------|------|------|------|
| D              | Rise | τг     | $\theta = \phi = 0^{\circ}$ | -    | 350  | 450  | ms   |
| Response time  | Down | τd     | $\theta = \phi = 0^{\circ}$ | -    | 230  | 330  | ms   |
| Contrast ratio |      | CR     | $\theta = \phi = 0^{\circ}$ | 5.0  | 10.0 | -    | -    |
| Reflectance    |      | ρ      | -                           | 15   | 30   | -    | %    |

Optimum contrast is obtained by adjusting the LCD driving voltage ( $V_{OP}$ ) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ .

## 6-2. Transmissive mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

|                     |          |                     |                             | TiTCa: | suring spot - | φ 0.0111111, 10 | Jinp. 200 |
|---------------------|----------|---------------------|-----------------------------|--------|---------------|-----------------|-----------|
| Item                |          | Symbol              | Condition                   | Min.   | Тур.          | Max.            | Unit      |
| D                   | Rise     | τr                  | $\theta = \phi = 0^{\circ}$ | -      | 350           | 450             | ms        |
| Response time       | Down     | τd                  | $\theta = \phi = 0^{\circ}$ | -      | 230           | 330             | ms        |
|                     |          | heta upper          |                             | -      | 20            | -               | 1         |
| 77 1                |          | $\theta_{ m LOWER}$ | CD > 0                      | -      | 30            | -               | deg.      |
| Viewing angle range |          | $\phi$ left         | CR≧2                        | -      | 50            | -               | 1         |
|                     |          | $\phi$ right        |                             | -      | 50            | -               | deg.      |
| Contrast ratio      |          | CR                  | $\theta = \phi = 0$ °       | 12.5   | 25.0          | -               | -         |
| Brightness          |          | L                   | IF=10mA/Line                | 15     | 25            | -               | cd/m²     |
|                     | D 1      | X                   | $\theta = \phi = 0^{\circ}$ | 0.40   | 0.45          | 0.45            |           |
|                     | Red      | У                   | $\theta = \psi = 0$         | 0.28   | 0.33          | 0.38            |           |
|                     | C        | X                   | 0 - 4 -00                   | 0.27   | 0.32          | 0.37            |           |
| Chromaticity        | Green    | У                   | $\theta = \phi = 0^{\circ}$ | 0.34   | 0.39          | 0.44            |           |
| coordinates         |          | X                   | $\theta = \phi = 0^{\circ}$ | 0.18   | 0.23          | 0.28            | -         |
|                     | Blue     | У                   | $\sigma - \psi - 0^{-1}$    | 0.20   | 0.25          | 0.30            |           |
|                     | XX71 : / | X                   | 0 - 4 -00                   | 0.26   | 0.31          | 0.36            |           |
|                     | White    | У                   | $\theta = \phi = 0^{\circ}$ | 0.28   | 0.33          | 0.38            |           |

Optimum contrast is obtained by adjusting the LCD driving voltage ( $V_{OP}$ ) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ .



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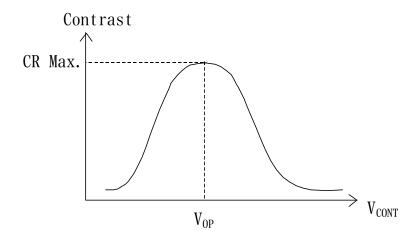
### 6-3. Definition of reflectance

$$\rho \ (\text{Reflectance}) \qquad = \frac{\text{Measured reflection brightness}}{\text{Reflection brightness against standard white board}} \times 100(\%)$$

6-4. Definition of contrast ratio (Reflective mode)

6-5. Definition of contrast ratio (Transmissive mode)

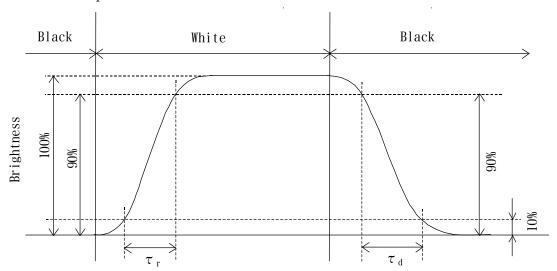
## 6-6. Definition of VOP



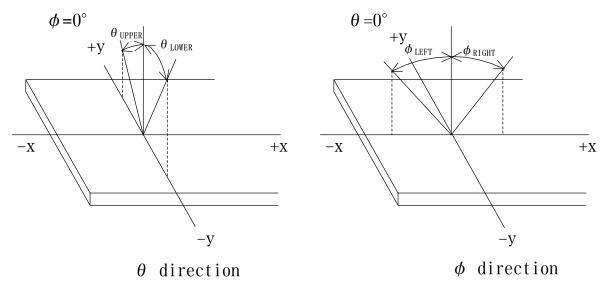


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# 6-7. Definition of response time

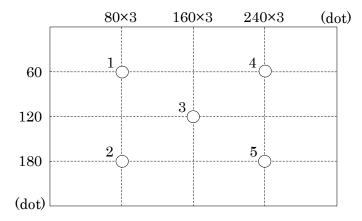


# 6-8. Definition of viewing angle



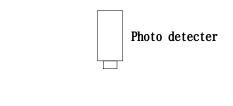
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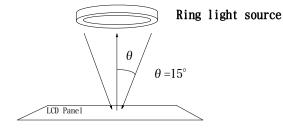
## 6-9. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)
- 3) Backlight: IF=10mA/1 LED line

# 6-10. Measurement method of reflectance (Reflectance)

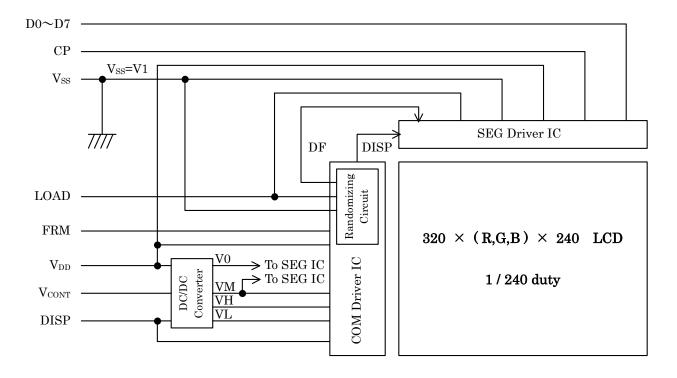




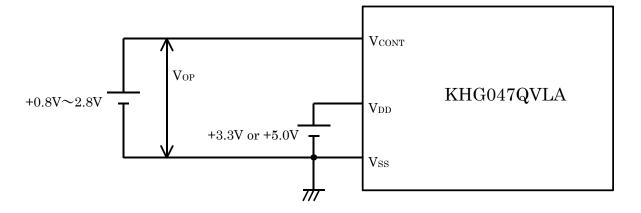


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# 7. Circuit block diagram



## 7-1. Power supply





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# 8. Interface signals

## 8-1. Pin assignment of LCD panel

| No. | Symbol            | Description                                  | Level                              |
|-----|-------------------|--|------------------------------------|
| 1   | FRM               | Synchronous signal for driving scanning line | Н                                  |
| 2   | LOAD              | Data signal latch clock                      | $\mathrm{H}  ightarrow \mathrm{L}$ |
| 3   | CP                | Data signal shift clock                      | $\mathrm{H}  ightarrow \mathrm{L}$ |
| 4   | DISP              | Display control signal                       | H(ON),L(OFF)                       |
| 5   | $V_{ m DD}$       | Power supply for logic                       | -                                  |
| 6   | $V_{\mathrm{SS}}$ | GND  | -                                  |
| 7   | VCONT             | LCD adjust voltage                           | -                                  |
| 8   | D7                |  |                                    |
| 9   | D6                |  |                                    |
| 10  | D5                |  |                                    |
| 11  | D4                | D'adam data                                  | H(ON),L(OFF)                       |
| 12  | D3                | Display data                                 | n(ON),L(OFF)                       |
| 13  | D2                |  |                                    |
| 14  | D1                |  |                                    |
| 15  | D0                |  |                                    |
| 16  | $V_{ m DD}$       | Power cumby for logic                        | _                                  |
| 17  | $V_{ m DD}$       | Power supply for logic                       | -                                  |
| 18  | $V_{\rm SS}$      |  |                                    |
| 19  | $V_{\rm SS}$      | GND  | -                                  |
| 20  | $V_{\rm SS}$      |  |                                    |

LCD connector : 08-6210-020-340-800+ (ELCO)

Recommended matching FFC or FPC  $\qquad : 0.5 mm \ pitch$ 

## 8-2. Pin assignment of LED

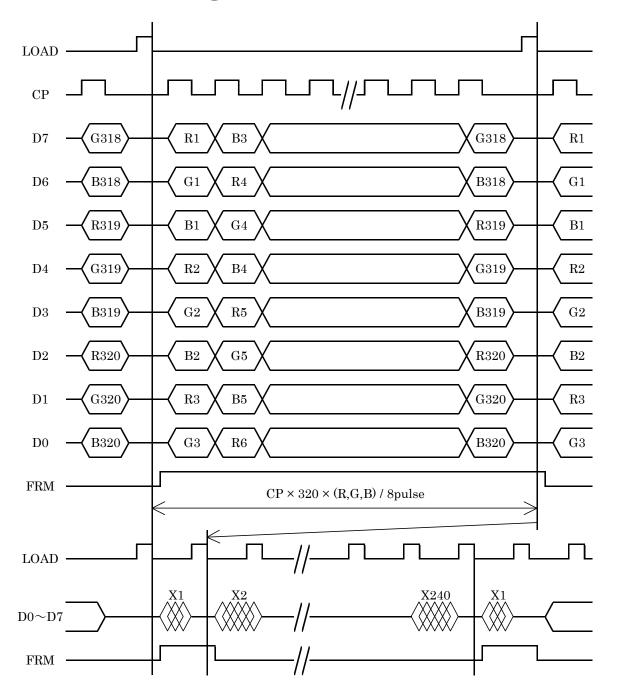
| No. | Symbol | Description                   |  |  |  |
|-----|--------|-------------------------------|--|--|--|
| 1   | VinB   |                               |  |  |  |
| 2   | VinB   | Consulta malta ma             |  |  |  |
| 3   | VinB   | Supply voltage                |  |  |  |
| 4   | VinB   |                               |  |  |  |
| 5   | BLEN   | Backlight ON-OFF (H:ON,L:OFF) |  |  |  |
| 6   | VBRT   | Brightness adjust voltage     |  |  |  |
| 7   | GND    |                               |  |  |  |
| 8   | GND    | CND                           |  |  |  |
| 9   | GND    | GND                           |  |  |  |
| 10  | GND    |                               |  |  |  |

LCD side connector : 08-6210-010-340-800+ (ELCO)



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# 9. Interface timing chart



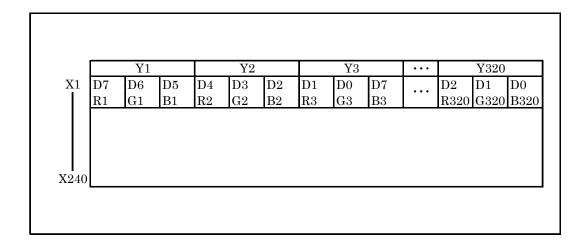
<sup>\*</sup> The cycle of the LOAD signal should be stable and continuously applied without interruption.



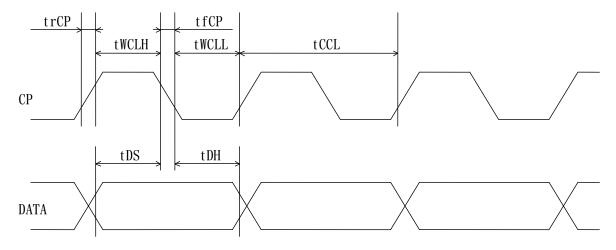
<sup>\*</sup> The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

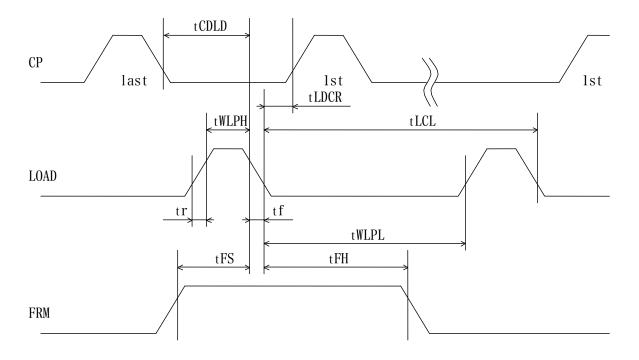
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## 10. Data and screen



## 11. Input timing characteristics







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# 11-1. Switching characteristics ( $V_{DD} = 5.0V$ )

Input characteristics :  $V_{DD} = +5.0V \pm 5\%$ , Temp. = -20 $\sim$ 70°C

| Item                             | Symbol | Min. | Max. | Unit |
|----------------------------------|--------|------|------|------|
| CP cycle *1                      | tCCL   | 100  | -    | ns   |
| CP "H" pulse width               | tWCLH  | 30   | =    | ns   |
| CP "L" pulse width               | tWCLL  | 30   | -    | ns   |
| CP rise up time                  | trCP   | -    | 15   | ns   |
| CP fall down time                | tfCP   | -    | 15   | ns   |
| Data set up time                 | tDS    | 25   | =    | ns   |
| Data hold time                   | tDH    | 25   | -    | ns   |
| LOAD "H" pulse width             | tWLPH  | 40   | -    | ns   |
| LOAD "L" pulse width             | tWLPL  | 400  | -    | ns   |
| LOAD cycle *2                    | tLCL   | 500  | -    | ns   |
| $CP \rightarrow LOAD$ delay time | tCDLD  | 60   | -    | ns   |
| $LOAD \rightarrow CP$ delay time | tLDCR  | 60   | -    | ns   |
| Input signal rise up time        | tr     | -    | 20   | ns   |
| Input signal fall down time      | tf     | -    | 20   | ns   |
| FRM data set up time             | tFS    | 120  | -    | ns   |
| FRM data hold time               | tFH    | 30   | -    | ns   |

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.

## 11-2. Switching characteristics ( $V_{DD} = 3.3V$ )

Input characteristics :  $V_{DD} = +3.3V \pm 0.3V$ , Temp. = -20 $\sim$ 70°C

| Item                             |    | Symbol                | Min. | Max. | Unit |
|----------------------------------|----|-----------------------|------|------|------|
| CP cycle                         | *1 | tCCL                  | 100  | -    | ns   |
| CP "H" pulse width               |    | tWCLH                 | 40   | -    | ns   |
| CP "L" pulse width               |    | tWCLL                 | 40   | -    | ns   |
| CP rise up time                  |    | $\operatorname{trCP}$ | -    | 20   | ns   |
| CP fall down time                |    | tfCP                  | -    | 20   | ns   |
| Data set up time                 |    | tDS                   | 35   | -    | ns   |
| Data hold time                   |    | tDH                   | 35   | -    | ns   |
| LOAD "H" pulse width             |    | tWLPH                 | 50   | -    | ns   |
| LOAD "L" pulse width             |    | tWLPL                 | 400  | -    | ns   |
| LOAD cycle                       | *2 | tLCL                  | 500  | -    | ns   |
| $CP \rightarrow LOAD$ delay time |    | tCDLD                 | 60   | -    | ns   |
| $LOAD \rightarrow CP$ delay time |    | tLDCR                 | 80   | -    | ns   |
| Input signal rise up time        |    | tr                    | -    | 20   | ns   |
| Input signal fall down time      |    | tf                    | -    | 20   | ns   |
| FRM data set up time             |    | tFS                   | 120  | -    | ns   |
| FRM data hold time               |    | ${ m tFH}$            | 30   | -    | ns   |

<sup>\*1</sup> CP cycle is adjusted so that FRM signal is 75Hz.



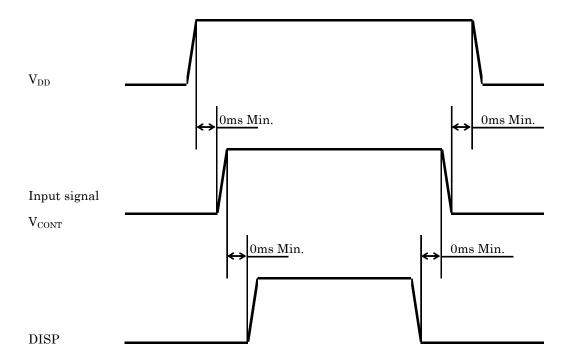
<sup>\*2</sup> LOAD cycle is constant.

<sup>\*2</sup> LOAD cycle is constant.

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## 12. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on  $V_{\text{CONT}}$  outside of the time when the  $V_{DD}$  logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



- \* Input signal: FRM, LOAD, CP, D0~D7
- \* The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.
- \* Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.



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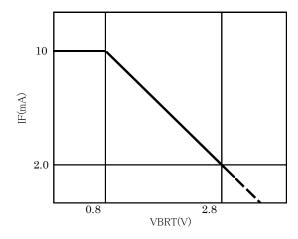
# 13. Backlight characteristics

LED ratings Temp. =  $25^{\circ}$ C

| End raming               |        |         |        |         |      |                       |
|--------------------------|--------|---------|--------|---------|------|-----------------------|
| Item                     | Symbol | Min.    | Typ.   | Max.    | Unit | Note                  |
| Supply voltage           | VinB   | 3.0     | -      | 5.5     | V    | -20∼70°C              |
| ON-OFF(H)                | DIEN   | 0.8VinB | -      | VinB    | V    | -20∼70°C              |
| ON-OFF(L)                | BLEN   | 0.0     | -      | 0.2VinB | v    | -20∼70°C              |
| LED forward current      | IF     | -       | 10.0   | -       | mA   | VBRT=0∼0.8V *1        |
| *1*2*5                   |        | -       | 2.0    | -       |      | VBRT=2.8V *1*2        |
| C 1                      | T. D   | -       | 290    | 350     |      | VinB=3.3V, IF=10mA *1 |
| Supply current           | IinB   | -       | 180    | 220     | mA   | VinB=5.0V, IF=10mA *1 |
| Operation life time *3*4 | Т      | -       | 50,000 | -       | h    | IF=10mA *1            |

<sup>\*1</sup> For each LED.

## \*5 VBRT-IF characteristics



VBRT-IF characteristics (reference) Ta=25℃



<sup>\*2</sup> An input current below 5.0mA may reduce the brightness uniformity of the LED backlight.

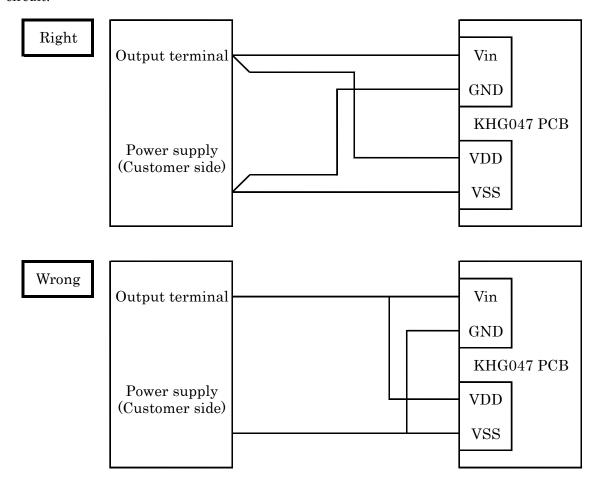
This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

<sup>\*3</sup> When brightness decrease 50% of initial brightness.

<sup>\*4</sup> Life time is estimated data. (Condition: IF=10mA, Ta=25°C in chamber)

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- \* When you start-up, please charge in sequence of VinB->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->VinB.
- \* Please do not connect the other than our backlight to this output connector on the PCB.
- \* In case VDD and VinB are supplied by a single power source, VDD & VinB, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & VinB, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





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### 14. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

| Year | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 |
|------|------|------|------|------|------|------|
| Code | 8    | 9    | 0    | 1    | 2    | 3    |

| Month | Jan. | Feb. | Mar. | Apr. | May | Jun. |
|-------|------|------|------|------|-----|------|
| Code  | 1    | 2    | 3    | 4    | 5   | 6    |

| Month | Jul. | Aug. | Sep. | Oct. | Nov. | Dec. |
|-------|------|------|------|------|------|------|
| Code  | 7    | 8    | 9    | X    | Y    | Z    |

## 15. Warranty

## 15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

### 15-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 16. Precautions for use

### 16-1. Installation of the LCD

- 1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.
- 5) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
- 6) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 16-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

### 16-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Adjust the "Supply voltage for LCD driving (VCONT)" to obtain optimum viewing angle and contrast ratio.

#### 16-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 16-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED FPC and do not bend the root of the wires. Housing should be designed to protect LED FPC from external stress.
- 7) Do not disassemble LCD module because it will result in damage.
- 8) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 10) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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## 17. Reliability test data

| Test item                      | Test condition                       | Test time | Jud  | gement  |
|--------------------------------|--------------------------------------|-----------|--|---|
| High temp.<br>atmosphere       | 80°C                                 | 240h      | Display function Display quality Current consumption | : No defect<br>: No defect<br>: No defect                             |
| Low temp.<br>atmosphere        | -30°C                                | 240h      | Display function Display quality Current consumption | : No defect<br>: No defect<br>: No defect                             |
| High temp. humidity atmosphere | 40°C 90% RH                          | 240h      | Display function Display quality Current consumption | : No defect<br>: No defect<br>: No defect                             |
| Temp. cycle                    | -30°C 0.5h<br>R.T. 0.5h<br>80°C 0.5h | 10cycles  | Display function Display quality Current consumption | <ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul> |
| High temp. operation           | 70°C                                 | 500h      | Display function Display quality Current consumption | <ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>       |

<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

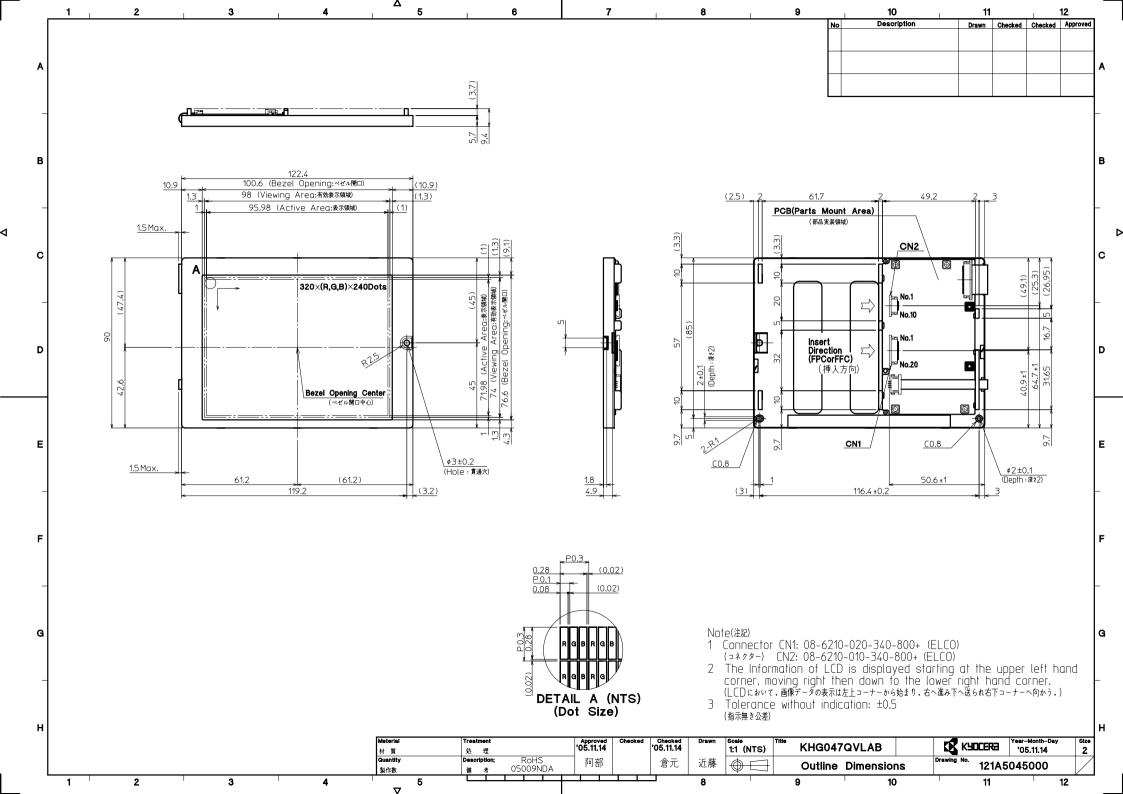


<sup>\*</sup> The LCD is tested in circumstances in which there is no condensation.

<sup>\*</sup> The reliability test is not an out-going inspection.

<sup>\*</sup> The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.



| Spec No. | TQ3C-8EAC0-E2DKJ01-00 |
|----------|-----------------------|
| Date     | June 2, 2008          |

# KYOCERA INSPECTION STANDARD

TYPE: KHG047QVLAB-G000

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

| Original     | Designed by : Engineering dept. |            |           | Confirmed by : QA dept. |          |
|--------------|---------------------------------|------------|-----------|-------------------------|----------|
| Issue Date   | Prepared                        | Checked    | Approved  | Checked                 | Approved |
| June 2, 2008 | y dsano                         | 7d Topusor | T. Miname | J. Sakaguchi            | H. Sul   |



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| TQ3C-8EAC0-E2DKJ01-00 | KHG047QVLAB-G000 | -    |

# Revision record

|         | Data | Designed by : Engineering dept. |     | Confirmed by : QA dept. |           |         |          |
|---------|------|---------------------------------|-----|-------------------------|-----------|---------|----------|
|         | Date | Prepa                           | red | Checked                 | Approved  | Checked | Approved |
|         |      |                                 |     |                         |           |         |          |
| -       |      | ,                               |     |                         |           |         |          |
| Rev.No. | Date | Page                            |     |                         | Descripti | ons     |          |
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# Visuals specification

# 1) Note

| Item                          |   | Note   |  |  |  |
|-------------------------------|---|--|--|--|--|
| General                       | inspected, operating volta  | in this Inspection Standards are age (V <sub>OP</sub> ) shall be set at the level it is available. Display quality is ving area. (Bi-level INSPECTION) |  |  |  |
|                               | •   | about the image quality shall be thin the effective viewing area and outside of the area.  |  |  |  |
|                               |   | are not specified in this standard ard shall be determined by mutual ner and Kyocera.  |  |  |  |
|                               | 4.Inspection conditions  Luminance : 500 Lux minimum.  Inspection distance : 300 mm(from the sample)  Temperature : 25±5°C  Direction : right above |  |  |  |  |
| Definition of inspection item | Pinhole, Bright spot<br>Black spot, Scratch<br>Foreign particle   | The color of a small area is different from the remainder. The phenomenon does not change with voltage.  |  |  |  |
|                               | Contrast variation  | The color of a small area is different from the remainder. The phenomenon change with voltage.   |  |  |  |
|                               | Polarizer<br>(Scratch, Bubble, Dent)  | Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.   |  |  |  |
|                               |   | <u>:</u><br>:  |  |  |  |



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### 2)Standard

| Inspection item                                    | Judgement standard |                                    |              |                   |                           |  |  |
|--|--------------------|------------------------------------|--------------|-------------------|---------------------------|--|--|
| Pinhole, Bright spot, Black spot, Foreign particle | d = (a + b)/2      |                                    |              |                   |                           |  |  |
|  | Category           | Size(mm)                           |              | Acceptable number |                           |  |  |
|  | A                  | $d \leq 0.2$                       |              | Neglected         |                           |  |  |
|  | В                  | $0.2 < d \le 0.3$                  |              | 5                 |                           |  |  |
|  | C                  | 0.2 < d = 0.5<br>$0.3 < d \le 0.5$ |              | 3                 |                           |  |  |
|  | D                  | 0.5 < d                            |              | 0                 |                           |  |  |
| Scratch, Foreign particle                          |                    | L                                  |              |                   |                           |  |  |
|  | V                  | Vidth (mm)                         | Length       | (mm)              | Acceptable number         |  |  |
|  | A                  | W ≤0.03                            | -            |                   | Neglected                 |  |  |
|  | В                  |                                    | L            | $\leq 2.0$        | Neglected                 |  |  |
|  | C 0.03<            | $<$ W $\leq$ 0.10                  | 2.0< L ≤4.0  |                   | 3                         |  |  |
|  | D<br>E 0.10<       | < W                                | 4.0< L       |                   | O According to 'Circular' |  |  |
| Contrast variation                                 | d = (a + b) / 2    |                                    |              |                   |                           |  |  |
|  | Category           | Size (                             | (mm)         | Acce              | eptable number            |  |  |
|  | A                  |                                    | $1 \leq 0.5$ | 11000             | Neglected                 |  |  |
|  | В                  | $0.5 < d \le 0.7$                  |              | 3                 |                           |  |  |
|  | C                  | 0.7 < d                            |              | 0                 |                           |  |  |



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| Inspection item         |   | Judgement standard |             |                   |        |           |                |  |
|-------------------------|---|--------------------|-------------|-------------------|--------|-----------|----------------|--|
| Polarizer               | ( | (1) Scratch        |             |                   |        |           |                |  |
| (Scratch, Bubble, Dent) |   |                    |             |                   |        |           |                |  |
|                         |   | W                  |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   | L                  |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    | V           | Width (mm)        | Length | (mm)      | Acceptable No. |  |
|                         |   | A                  |             | W ≦0.1            | -      |           | Neglected      |  |
|                         |   | В                  | 0.1< W ≦0.3 | L ≦5.0            |        | Neglected |                |  |
|                         |   | С                  |             | 5.0< L            |        | 0         |                |  |
|                         |   | D                  | 0.3         | < W               | -      |           | 0              |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   | (2) Bubble (dent)  |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   |                    |             |                   |        |           |                |  |
|                         |   | d = (a + b) / 2    |             |                   |        |           |                |  |
|                         |   | Category Size      |             |                   | mm)    | Acce      | eptable number |  |
|                         |   | A                  |             | d ≤ 0.2           |        | Neglected |                |  |
|                         |   | В                  |             | $0.2 < d \le 0.3$ |        |           | 5              |  |
|                         |   | С                  |             | $0.3 < d \le 0.5$ |        | 3         |                |  |
|                         |   | D                  |             | 0.5 < d           |        | 0         |                |  |
|                         |   | I                  | )           | 0.5 < α           |        |           | U              |  |

