SPEC

Spec No.	TQ3C-8EAC0-E1DKJ07-00
Date	January 8, 2009

TYPE: KHG047QVLAE-G000

< 4.7 inch QVGA transflective color STN with LED backlight, constant current circuit for LED backlight and touch panel >

CONTENTS

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Circuit block diagram
- 8. Interface signals
- 9. Interface timing chart
- 10. Data and screen
- 11. Input timing characteristics
- 12. Supply voltage sequence condition
- 13. Backlight characteristics
- 14. Design guidance for analog touch panel
- 15. Lot number identification
- 16. Warranty
- 17. Precautions for use
- 18. Reliability test data
- 19. Outline drawing

Issued
Date: Jan.22,2009
KYDERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: I	Engineering de _l	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
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Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	-

Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
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Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	1

1. Application

This document defines the specification of KHG047QVLAE-G000. (RoHS Compliant)

2. Construction and outline

LCD : Transflective color dot matrix type STN

Duty ratio : 1/240 duty Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Bias voltage circuit, Randomizing circuit,

DC/DC converter circuit, Temperature compensation circuit

(with Constant current circuit for LED Backlight)

Touch panel : Analog type (Glass/Glass), Glare Anti-reflection treatment

3. Mechanical specifications

3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	122.4 (W) × 90 (H) × 11.9 (D) Max. **Refer outline drawing in detail	mm
Active area	95.98 (W) × 71.98 (H) (12.0cm / 4.7 inch (Diagonal))	mm
Effective viewing area	98 (W) × 74 (H)	mm
Dot format	320×(R,G,B) (W) × 240 (H)	dot
Dot size	$0.08 \text{ (W)} \times 0.28 \text{ (H)}$	
Dot pitch	0.1 (W) × 0.3 (H)	mm
Base color *1	Normally Black	
Mass	TBD	g

^{*1} Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.1~2.0	N
Transmittance	Typ. 85	%
Surface hardness	Pencil hardness 2H or more according	-



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	2

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$ m V_{DD}$	0	6.0	V
Supply voltage for LCD driving	VCONT	0	$ m V_{DD}$	V
Input signal voltage *1	$V_{\rm IN}$	0	$ m V_{DD}$	V
FRM frequency	f_{FRM}	-	150	Hz
Supply voltage for backlight	VinB	0	6.0	V
Backlight ON-OFF	BLEN	0	VinB	V
Brightness adjust voltage	VBRT	0	VinB	V
Supply voltage for touch panel	V_{TP}	0	6.0	V
Input current of touch panel	I_{TP}	0	0.5	mA

^{*1} Input signal: FRM, LOAD, CP, DISP, D0~D7

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	T_{STO}	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	H_{OP}	10	*4	%RH
Storage humidity	*3	Нѕто	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for use" for details.)

Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

² hours in each direction X, Y, Z (6 hours total)

EIAJ ED-2531

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



^{*2} Temp. = -30° C< 48h, Temp. = 80° C< 168h

^{*3} Non-condensing

^{*4} Temp. ≤ 40°C, 85%RH Max.

^{*6} Acceleration: 490 m/s², Pulse width: 11 ms

Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	3

5. Electrical characteristics

 $5-1. V_{DD} = 5.0V$

 $V_{DD} = +5.0V \pm 5\%$, Temp. = -20 \sim 70°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	$ m V_{DD}$	-	4.75	5.00	5.25	V
		-20°C∼	0.80	-	-	
Supply voltage for LCD driving *1, *2	$V_{CONT} = V_{OP}$	0∼50°C *3	1.30	1.80	2.30	V
		~70°C	-	-	2.80	
Input signal voltage	17	"High" level	$0.8 \mathrm{V}_\mathrm{DD}$	-	$V_{ m DD}$	V
(FRM,LOAD,CP,DISP,D0∼D7)	$ m V_{IN}$	"Low" level	0	-	$0.2 V_{ m DD}$	V
Input current	${ m I}_{ m IN}$	Input signal	-100	-	100	μA
Rush current for logic	I _{RUSH}	When LCD turn on.		3.0A (Peak) × 1ms		
Clock frequency	$ m f_{CP}$	-	-	-	10.00	MHz
Frame frequency *4	f_{FRM}	-	70	75	80	Hz
Current consumption for logic	${ m I}_{ m DD}$	*5	-	30	45	mA
Power consumption	P_{DISP}	9	-	150	225	mW

^{*1} The supply voltage (V_{CONT} = V_{OP}) to drive the display has individual difference. Please adjust the contrast to be most suitable.

*5 Display pattern:



^{*2} Frame frequency : $f_{FRM} = 75Hz$

^{*3} The LCD module has a temperature compensation circuit.

^{*4} In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.

Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	4

 $5-2. V_{DD} = 3.3V$

V_{DD}	=+3.3	$V\pm0.3$	V. '	Γemp.	= -	20~	709	C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	-	3.0	3.3	3.6	V
		-20°C∼	0.80	-	-	
Supply voltage for LCD driving *1, *2	$V_{\text{CONT}} = V_{\text{OP}}$	0∼50°C *3	1.30	1.80	2.30	V
		~70°C	-	-	2.80	
Input signal voltage	77	"High" level	$0.8 \mathrm{V}_\mathrm{DD}$	-	$V_{ m DD}$	V
(FRM,LOAD,CP,DISP,D0-D7)	$ m V_{IN}$	"Low" level	0	-	$0.2 V_{ m DD}$	V
Input current	I_{IN}	Input signal	-100	-	100	μA
Rush current for logic	т	When LCD		$3.0A (Peak) \times 1ms$		
Rush current for logic	I _{RUSH}	turn on.		5.0A (rear	() ^ 1111s	
Clock frequency	\mathbf{f}_{CP}	-	-	-	10.00	MHz
Frame frequency *4	$\mathbf{f}_{\mathrm{FRM}}$	-	70	75	80	Hz
Current consumption for logic	${ m I}_{ m DD}$	*5	-	45	68	mA
Power consumption	$\mathrm{P}_{\mathrm{DISP}}$	o	-	149	224	mW

- *1 The supply voltage (V_{CONT} = V_{OP}) to drive the display has individual difference. Please adjust the contrast to be most suitable.
- *2 Frame frequency : $f_{FRM} = 75Hz$
- *3 The LCD module has a temperature compensation circuit.
- *4 In consideration of display quality, it is recommended that frame frequency be set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency value. Generally, as frame and clock frequencies become higher current consumption increases and display quality will degrade.
- *5 Display pattern:

5-3. Touch panel

Item	Specification		
Supply voltage for touch panel	5.0 V		
Townsia al masistana a	$xL\sim xR:200\Omega\sim 1000\Omega$		
Terminal resistance	yU~yL : 200Ω~1000Ω		
Linearity	less than $\pm 2.5\%$		
Insulation resistance	$50 \mathrm{M}\Omega$ or more at DC25V		



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	5

6. Optical characteristics

6-1. Reflective mode

Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τг	$\theta = \phi = 0^{\circ}$	-	350	450	ms
	Down	τd	$\theta = \phi = 0^{\circ}$	-	230	330	ms
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	5	10	-	-
Reflectance		ρ	-	15	30	-	%

Optimum contrast is obtained by adjusting the LCD driving voltage (V_{OP}) while at the viewing angle of $\theta = \phi = 0^{\circ}$.

6-2. Transmissive mode

Measuring spot = ϕ 6.0mm, Temp. = 25°C

		,	T	- TiTeat	suring spot -	φ 0.0111111, 13	Jinp. 20 C
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	τr	$\theta = \phi = 0^{\circ}$	-	350	450	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	230	330	ms
		heta upper		-	20	-	1
77 1		$\theta_{ m LOWER}$	CD > 0	-	30	-	deg.
Viewing angle	range	ϕ left	CR≧2	-	50	-	1
		ϕ right		-	50	-	deg.
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	12	25	-	-
Brightness		L	IF=10mA/Line	(15)	(25)	-	cd/m²
	D 1	X	0 4 00	(TBD)	(TBD)	(TBD)	
	Red	У	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	
	C	X	0 - 4 -00	(TBD)	(TBD)	(TBD)	
Chromaticity	Green	У	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	
coordinates	DI	X	0 - 4 -00	(TBD)	(TBD)	(TBD)	-
	Blue	У	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	
	XX71 - 14 -	X	0 - h -0°	(TBD)	(TBD)	(TBD)	
	White	У	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	

Optimum contrast is obtained by adjusting the LCD driving voltage (V_{OP}) while at the viewing angle of $\theta = \phi = 0^{\circ}$.



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	6

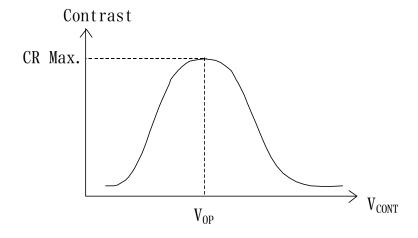
6-3. Definition of reflectance

$$\rho \, (\text{Reflectance}) \qquad = \frac{\text{Measured reflection brightness}}{\text{Reflection brightness against standard white board}} \, \times 100(\%)$$

6-4. Definition of contrast ratio (Reflective mode)

6-5. Definition of contrast ratio (Transmissive mode)

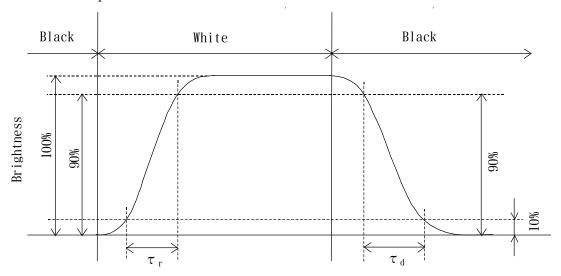
6-6. Definition of VOP



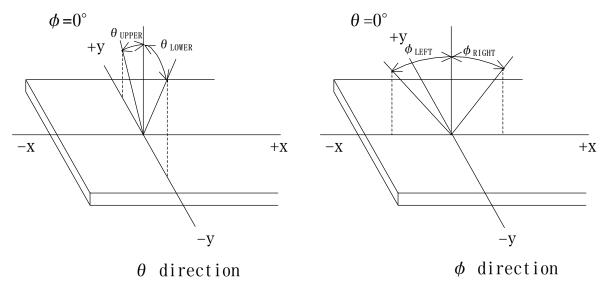


Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	7

6-7. Definition of response time

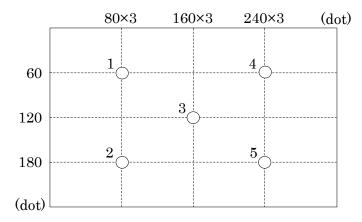


6-8. Definition of viewing angle



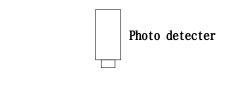
Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	8

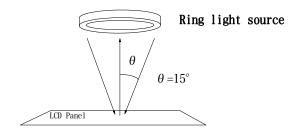
6-9. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)
- 3) Backlight: IF=10mA/1 LED line

6-10. Measurement method of reflectance (Reflectance)

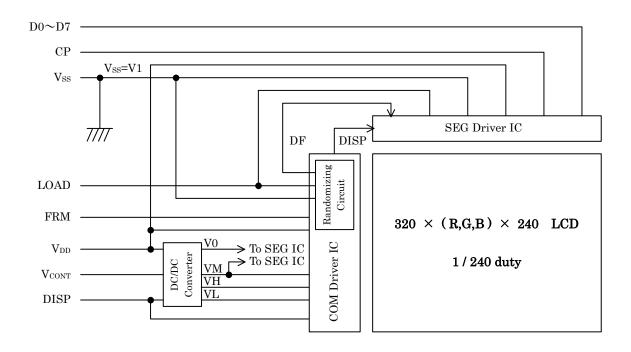




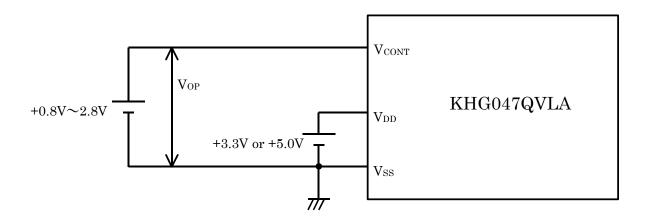


Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	9

7. Circuit block diagram



7-1. Power supply





Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	10

8. Interface signals

8-1. Pin assignment of LCD panel

No.	Symbol	Description	Level
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	$\mathrm{H} \to \mathrm{L}$
3	CP	Data signal shift clock	$H \to L$
4	DISP	Display control signal	H(ON),L(OFF)
5	$V_{ m DD}$	Power supply for logic	-
6	$ m V_{SS}$	GND	-
7	$V_{\rm CONT}$	LCD adjust voltage	-
8	D7		
9	D6		
10	D5		
11	D4	Disulan data	H(ON),L(OFF)
12	D3	Display data	n(ON),L(OFF)
13	D2		
14	D1		
15	D0		
16	$V_{ m DD}$	Power supply for logic	
17	$V_{ m DD}$	rower supply for logic	
18	$V_{\rm SS}$		
19	$V_{\rm SS}$	GND	-
20	$V_{\rm SS}$		

LCD connector : 08-6210-020-340-800+ (ELCO)

Recommended matching FFC or FPC $\qquad : 0.5 mm pitch$

8-2. Pin assignment of LED

No.	Symbol	Description	
1	VinB		
2	VinB	Consulta malta ma	
3	VinB	Supply voltage	
4	VinB		
5	BLEN	Backlight ON-OFF (H:ON,L:OFF)	
6	VBRT	Brightness adjust voltage	
7	GND		
8	GND	CND	
9	GND	GND	
10	GND		

LCD side connector : 08-6210-010-340-800+ (ELCO)



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	11

8-3. Pin assignment of touch panel

No.	Symbol	Description
1	yU	y-Upper terminal
2	хL	x-Left terminal
3	уL	y-Lower terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch

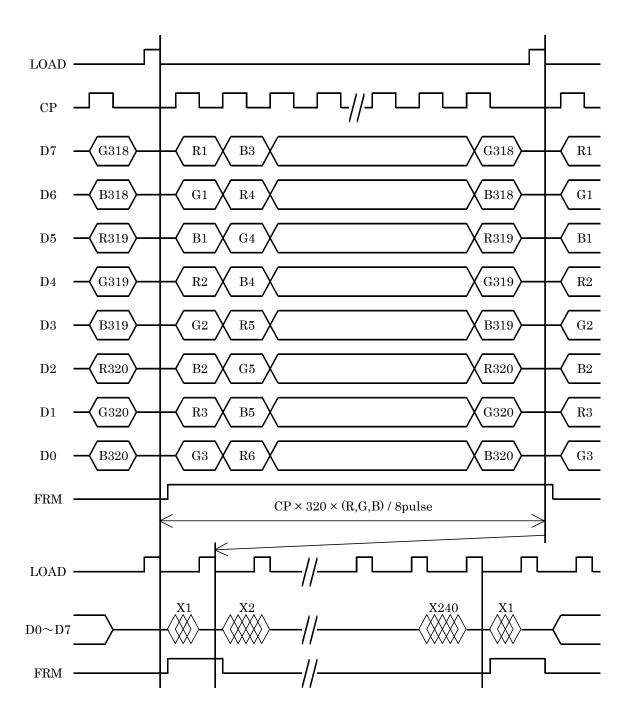
Recommended matching connector : 04FFS-SP-GB-TF(LF)(SN) (JST)

: 00-8370-049-000-888+ (ELCO)



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	12

9. Interface timing chart



^{*} The cycle of the LOAD signal should be stable and continuously applied without interruption.



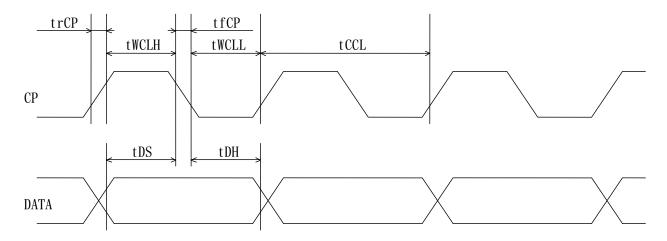
^{*} The above-mentioned timing chart is a reference to set up a LCD module, not an electrical rating.

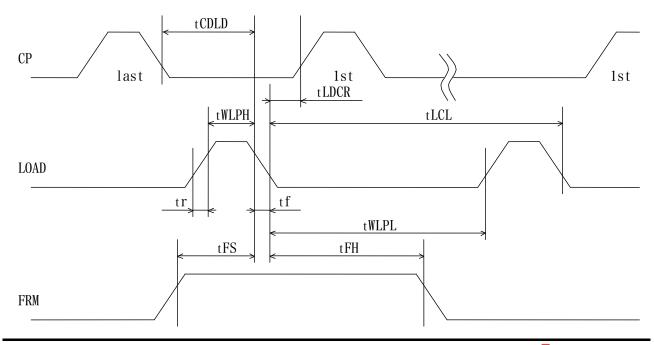
Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	13

10. Data and screen

X1 D7 D6 D5 D4 D3 D2 D1 D0 D7 D2 D1	D1 D0
R1 G1 B1 R2 G2 B2 R3 G3 B3 R320 G320	20 G320 B3

11. Input timing characteristics







Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	14

11-1. Switching characteristics ($V_{DD} = 5.0V$)

Input characteristics : $V_{DD} = +5.0V \pm 5\%$, Temp. = -20 \sim 70°C

Item	Input characters	Symbol	Min.	Max.	Unit
Item		Symbol	Min.	max.	Unit
CP cycle	*1	tCCL	100	-	Ns
CP "H" pulse width		tWCLH	30	-	Ns
CP "L" pulse width		tWCLL	30	-	Ns
CP rise up time		trCP	-	15	Ns
CP fall down time		tfCP	-	15	Ns
Data set up time		tDS	25	-	Ns
Data hold time		tDH	25	-	Ns
LOAD "H" pulse width		tWLPH	40	-	Ns
LOAD "L" pulse width		tWLPL	400	-	Ns
LOAD cycle	*2	tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time		tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time		tLDCR	60	-	ns
Input signal rise up time		tr	-	20	ns
Input signal fall down time		tf	-	20	ns
FRM data set up time		tFS	120	-	ns
FRM data hold time		tFH	30	-	ns

^{*1} CP cycle is adjusted so that FRM signal is 75Hz.

11-2. Switching characteristics ($V_{DD} = 3.3V$)

Input characteristics : $V_{DD} = +3.3V \pm 0.3V$, Temp. = $-20 \sim 70^{\circ}$ C

Item	Symbol	Min.	Max.	Unit
CP cycle *1	tCCL	100	-	ns
CP "H" pulse width	tWCLH	40	-	ns
CP "L" pulse width	tWCLL	40	-	ns
CP rise up time	trCP	-	20	ns
CP fall down time	tfCP	-	20	ns
Data set up time	tDS	35	-	ns
Data hold time	tDH	35	-	ns
LOAD "H" pulse width	tWLPH	50	-	ns
LOAD "L" pulse width	tWLPL	400	-	ns
LOAD cycle *5	2 tLCL	500	-	ns
$CP \rightarrow LOAD$ delay time	tCDLD	60	-	ns
$LOAD \rightarrow CP$ delay time	tLDCR	80	-	ns
Input signal rise up time	tr	-	20	ns
Input signal fall down time	tf	-	20	ns
FRM data set up time	tFS	120	-	ns
FRM data hold time	tFH	30	-	ns

^{*1} CP cycle is adjusted so that FRM signal is 75Hz.



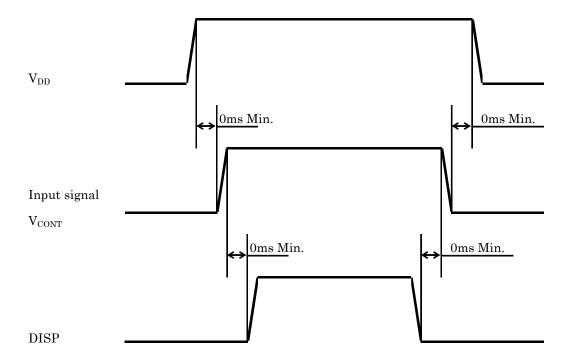
^{*2} LOAD cycle is constant.

^{*2} LOAD cycle is constant.

Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	15

12. Supply voltage sequence condition

In normal operation, logic within the LCD module reverses the polarity of the drive voltage every few lines to prevent DC damage to the liquid crystal material. But when a voltage is present on V_{CONT} outside of the time when the V_{DD} logic voltage is stable, a drive voltage is applied to the liquid crystal material without the polarity reversals. This sometimes result in a deterioration of display quality and a reduction in life time.



- * Input signal: FRM, LOAD, CP, D0~D7
- * The above sequence should be designed as to maintain each normal voltage when the liquid crystal module load is applied to your system.
- * Control the supply voltage sequence to not float any signal line when the LCD panel is being driven.



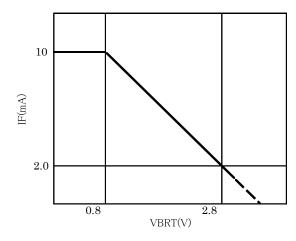
Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	16

13. LED Backlight characteristics

LED ratings Temp. = 25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	-	5.5	V	Ta=-20∼70°C
ON-OFF (ON)	BLEN	$0.8 { m V_{IN} B}$	-	$ m V_{IN}B$	V	Ta=-20∼70°C
ON-OFF (OFF)	DLEN	0.0	-	$0.2~{ m V_{IN}B}$	V	Ta=-20∼70°C
LED forward current	115	9.0	10.0	11.0	4	VBRT=0∼0.8V
*1 * 2 *5	IF	-	2.0	-	mA	VBRT=2.8V
Complex consent	ID	-	(TBD)	(TBD)	A	V _{IN} B =3.3V, IF=10mA
Supply current	$I_{IN}B$	-	(TBD)	(TBD)	mA	V _{IN} B =5.0V, IF=10mA
Operating life *3 *4	Т	-	(TBD)	-	hour	IF=10mA

^{*1} For each LED.



VBRT-IF characteristics (reference) Ta=25℃



^{*2} An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

^{*3} When brightness decrease 50% of initial brightness.

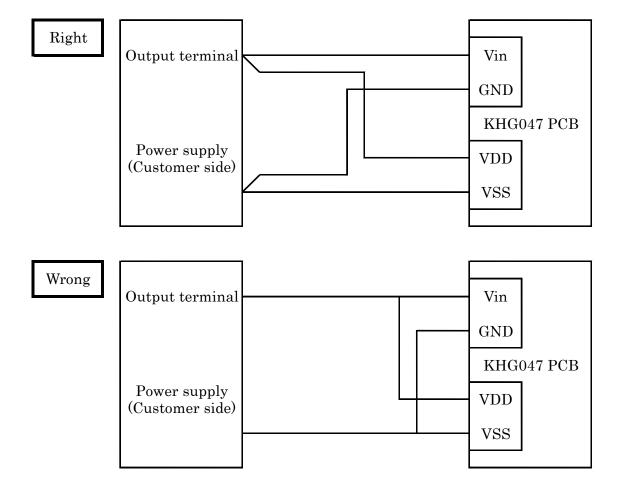
^{*4} Life time is estimated data. (Condition: IF=10mA, Ta=25°C in chamber)

^{*5} VBRT-IF characteristics.

Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	17

^{*} When you start-up, please charge in sequence of $V_{\rm IN}B$ ->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT-> $V_{\rm IN}B$.

- * Please do not connect the other than our backlight to this output connector on the PCB.
- * In case V_{DD} and V_{IN}B are supplied by a single power source, V_{DD} & V_{IN}B, and V_{SS} & GND are connected directly and separately from the output on the power source. If the common wire are used for V_{DD} & V_{IN}B, and for V_{SS} & GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	18

14. Design guidance for analog touch panel (T/P)

- 14-1 Electrical (In customer's design, please remember the following considerations.)
 - 1 Do not use the current regulated circuit.
 - 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
 - 3 Analog T/P can not sense two points touching separately.
 - 4 A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
 - 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

14-2 Software

- 1 Do the "User Calibration".
- 2 "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

14-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 This touch panel has an airtight but not watertight structure. Please not to use it for the applications requiring watertight or under the environments occurred condensation. If it is expected to be exposed to the environments that vapor, moisture or other liquids may seep inside a bezel, please be sure to take some measurements for drip-proof or waterproof by using sealing materials on the bezel.



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	19

15. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2009	2010	2011	2012	2013	2014
Code	9	0	1	2	3	4

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

16. Warranty

16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

16-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	20

17. Precautions for use

17-1. Installation of the LCD

- 1) The LCD module has a grounding hole. Please ground the module to prevent noise and to stabilize its performance as circumstances demand.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
- 5) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

17-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

17-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Adjust the "Supply voltage for LCD driving (VCONT)" to obtain optimum viewing angle and contrast ratio.

17-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

17-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAC0-E1DKJ07-00	KHG047QVLAE-G000	21

18. Reliability test data

Test item	Test condition	Test time	Judg	ement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Polyacetal stylus R4, Hardness 60° Hitting force 2.9N Hitting speed 5 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	: No defect : No defect : No defect : No defect

^{*} Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

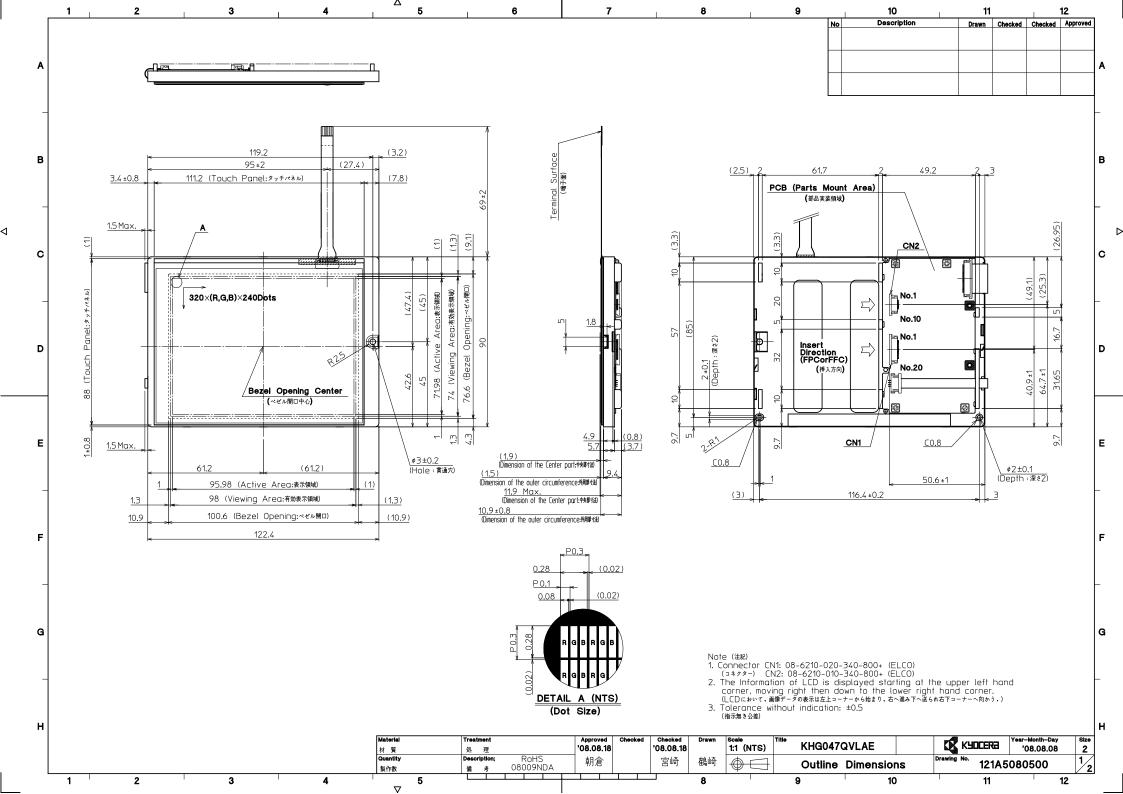


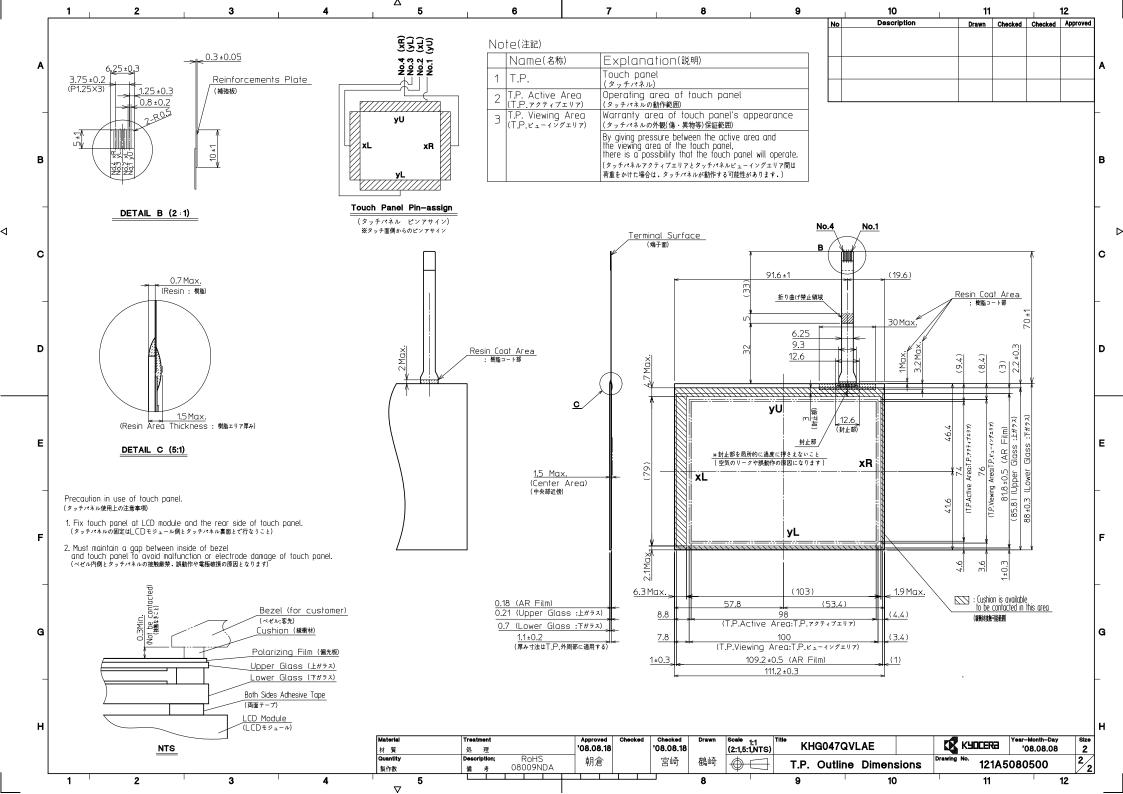
^{*} The LCD is tested in circumstances in which there is no condensation.

^{*} The reliability test is not an out-going inspection.

^{*} The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAC0-E2DKJ07-00
Date	January 8, 2009

KYOCERA INSPECTION STANDARD

TYPE: KHG047QVLAE-G000

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Confirmed by : QA dept.			
Issue Date Prepared Chec		Checked	Approved	Checked	Approved
January 8, 2009	y drano	H.Tokumuri	G Matricmoto	J. Sakaguchi	To , Inf



Spec No.	Part No.	Page
TQ3C-8EAC0-E2DKJ07-00	KHG047QVLAE-G000	-

Revision record

	Data	Designe	d by:	d by : Engineering dept.		Confirmed by : QA dept.		
	Date	Prepa	red	Checked	Approved	Checked	Approved	
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Rev.No.	Date	Page			Descripti	ons		



Spec No.	Part No.	Page
TQ3C-8EAC0-E2DKJ07-00	KHG047QVLAE-G000	1

Visuals specification 1) Note

Item		Note						
General	inspected, operating vol	1.When defects specified in this Inspection Standards are inspected, operating voltage (V _{OP}) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-level INSPECTION)						
	applied to any defect w	2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area.						
	happen, additional stan	3. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera.						
	Inspection distance Temperature	Luminance : 500 Lux minimum. Inspection distance : 300 mm(from the sample) Temperature : $25\pm5^{\circ}\mathrm{C}$						
Definition of inappation	Dinholo Dright and	The color of a small area is						
Definition of inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.						
	Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.						
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.						



Spec No.	Part No.	Page
TQ3C-8EAC0-E2DKJ07-00	KHG047QVLAE-G000	2

2)Standard

2)Standard								
Inspection item		Judgement standard						
Pinhole, Bright spot, Black spot, Foreign particle	d = (a + b)/2							
	Catagory	Category Size(mm) Acceptable number						
	A	Neglected Neglected						
	В	5						
	$\begin{array}{ c c c c c } \hline B & 0.2 < d \le 0.3 \\ \hline C & 0.3 < d \le 0.5 \\ \hline \end{array}$				3			
	D	0.5 < d			0			
					, and the second			
Scratch, Foreign particle	L							
	7	Width (mm)	Length	(mm)	Acceptable number			
	A	W ≤0.03	-		Neglected			
	В		L	≤ 2.0	Neglected			
		< W ≤0.10	2.0< L		3			
	D		4.0< L		0			
	E 0.10	< W	-		According to 'Circular'			
Control					Officular			
Contrast variation	a	b	d = (a +	-b)/2				
	Catamara	Size ((m, m,)	Λ 00 =	ntable number			
	Category				ptable number			
	A	+	$1 \le 0.5$		Neglected			
	С	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			0			
	C 0.7 < u 0							



Spec No.	Part No.	Page
TQ3C-8EAC0-E2DKJ07-00	KHG047QVLAE-G000	3

Inspection item		Judgement standard					
Polarizer Scratch, Bubble, Dent)	(1) Sc	ratch					
			L	V			
			Width (mm)	Length	(mm)	Acceptable No.	
	A		W ≦0.1	-		Neglected	
	В	0.1	< W ≦0.3	L	≦ 5.0	Neglected	
	С	0.1	· w ≡0.0	5.0< L		0	
	D	0.3	< W	-		0	
			_a	b			
					d = (a	+ b)/2	
	Са	tegory	Size (eptable number	
	Ca	A	d	≤ 0.2		eptable number Neglected	
	Ca	A B	0.2 < d	≤ 0.2 ≤ 0.3		eptable number Neglected 5	
	Ca	A	d	≤ 0.2 ≤ 0.3 ≤ 0.5		eptable number Neglected	



 Spec No.
 Part No.
 Page

 TQ3C-8EAC0-E2DKJ07-00
 KHG047QVLAE-G000
 4

					-	
Inspection item		Judgemer	nt standard			
Scratch,	(W = Width,	L = Length, D = Diame	eter = (major ax	kis+m	ninor axis)/2)	
Foreign particle	Item	Item Width(mm) Length(mm) Acc				
(Touch screen		$W \le 0.03$	$L \le 20$		Neglected	
portion)	Scratch	$0.03 < W \le 0.05$	$L \le 10$	2pce	s within $\phi 20$ mm	
	Scratch	$0.05 < W \le 0.08$	$L \le 6$	2pce	s within $\phi 20$ mm	
		$0.08 < W \le 0.1$	$L \le 4$	1pce	es within φ30mm	
	Foreign	$W \le 0.05$	Neglected		Neglected	
	(line like)	$0.05 < W \le 0.1$	$L \le 5$	2pce	s within ϕ 30mm	
	Foreign	D ≦	0.2		Neglected	
	(circle like)	$0.2 < D \le$	0.3	2pce	s within ϕ 30mm	
	Above are appl	lied to the visible area.				
		are foreign particle a	_		-	
	electrical perfo	ormance out of the visib	le area, we app	prove o	of this product.	
Glass crack						
(Touch screen	Item	Size (m	,m)		Acceptable	
portion)	Item	Size (II	1111/		number	
		/	z X	≦ 3		
	Conner	\sim \times \times \sim \sim	Y	≦ 3	2 pcs	
	crack				/panel	
				\leq t		
	Crack in	x > **		≤ 5		
	other area		77	/1 =	2 pcs	
	than in		Y	≦ 1.5	/side	
	corner	2	\mathbf{Z}	<t< td=""><td></td></t<>		
		<u> </u>				
	Progressive				0 pcs	
	crack		3/		(NG even 1pcs)	
	A1	11.14.41				
		lied to the visible area.	and dam	ffaat -	d gamianal 4- 41-	
		are foreign particle a	_		-	
Newton's ring		ngs in the center of the			_	
(Touch screen		the screen are permitte		reject	eu.	
portion)	Dorder around	one serven are permitte	ou.			
por mon/			뷠		164	
			ন		Fe.	
		NG	(OK		

