SPEC

Spec No.	TQ3C-8EAF0-E1DEG05-00
Date	May 22, 2008

TYPE: TCG057QVLBA-G20

< 5.7 inch QVGA transmissive color TFT with LED backlight, Constant current circuit for LED backlight >

CONTENTS

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Backlight characteristics
- 10. Lot number identification
- 11. Warranty
- 12. Precautions for use
- 13. Reliability test data
- 14. Outline drawing



KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
May 22, 2008	O. Ajisaka	7 Johnson	G. Matsumoto	J. Sakaguchi	Ho , Suf

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	-

Revision record

Date		Designe	ed by:	Engineering of	lept.	Confirmed by	: QA dept.
		Prepa	ıred	Checked	Approved	Checked	Approved
		- T			T		
Rev.No.	Date	Page			Descripti	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	${\tt TCG057QVLBA\text{-}G20}$	1

1. Application

This document defines the specification of TCG057QVLBA-G20. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	
Outline dimensions	127.2 (W)× 100.4 (H) × 5.7 (D)	
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	
Dot format	$320 \times (B,G,R) (W) \times 240 (H)$	dot
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	Normally White	-
Mass	110	g

^{*1} Due to the characteristics of the LCD material, the color varies with environmental temperature.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	2

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{IN}B$	V
Brightness adjust voltage	VBRT	0	$V_{\rm IN} B$	V

^{*1} Input signal: CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	T_{OP}	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	Tsto	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	H_{STO}	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

*6 Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



^{*2} Temp. = -30°C< 48h , Temp. = 80°C< 168h

^{*3} Non-condensing

^{*4} Temp. ≤ 40°C, 85%RH Max.

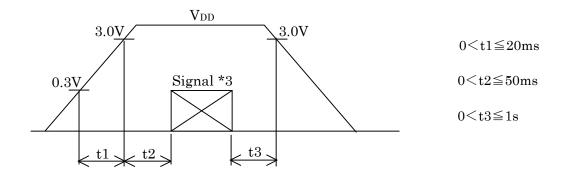
Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	${\tt TCG057QVLBA\text{-}G20}$	3

5. Electrical characteristics

Temp. = $-20 \sim 70^{\circ}$ C

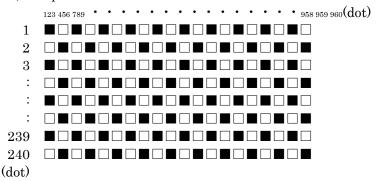
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic *1	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	*2	-	60	80	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
I	$V_{ m IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage *3	V_{IH}	"High" level	$0.7 V_{\mathrm{DD}}$	-	V_{DD}	V

*1 $V_{\rm DD}$ -turn-on conditions



*2 Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



^{*3} Input signal : CK, R0~R5, G0~G5, B0~B5, H_SYNC, V_SYNC, ENAB, R/L, U/D



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	4

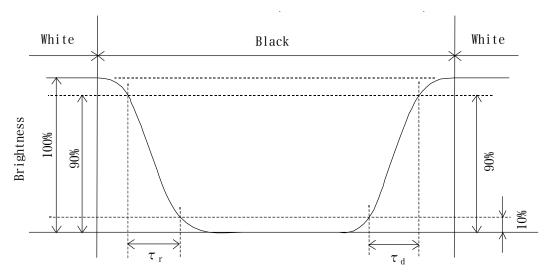
6. Optical characteristics

Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	τr	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	25	-	ms	
				-	80	-	,	
Viewing angle View direction	_	$ heta_{ m LOWER}$	CR≧5	-	80	-	deg.	
: 12 o'clo		ϕ left	Cn≦5	-	80	-	1	
(Gray inversion)		φ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	300	500	-	-	
Brightness		L	IF=15mA/Line	210	300	-	cd/m²	
D 1		X	$\theta = \phi = 0$ °	0.57	0.62	0.67		
	Red	У	$\theta - \psi = 0$	0.32	0.37	0.42		
	C	X	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
Chromaticity	Green	У	$\theta - \psi = 0$	0.54	0.59	0.64	_	
coordinates		X	$\theta = \phi = 0^{\circ}$	0.09	0.14	0.19	-	
	Blue	У	$v - \psi - 0$	0.04	0.09	0.14		
	White	X	$\theta = \phi = 0^{\circ}$	0.27	0.32	0.37		
	wnite	У	υ – ψ –υ	0.29	0.34	0.39		

6-1. Definition of contrast ratio

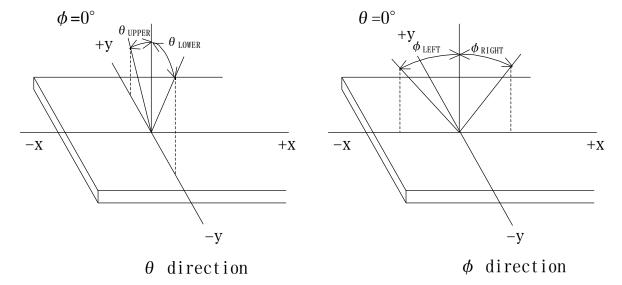
6-2. Definition of response time



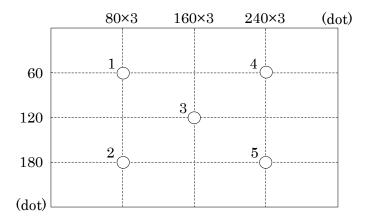


\$	Spec No.	Part No.	Page
,	TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	5

6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	6

7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	H_{SYNC}	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	Ι	
25	В5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal , H: Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H: Normal , L: Up / Down reverse mode	I	
32	NC	No connect	-	
33	$V_{\rm IN}B$	Power supply for LED backlight	-	
34	$V_{\rm IN}B$	Power supply for LED backlight	-	
35	$V_{\rm IN} B$	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H:ON, L:OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight	-	
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	7

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

*2



$${\rm R/L} = {\rm L}$$

$${\rm U/D} = {\rm H}$$



$$R/L = H$$

 $U/D = H$



$$R/L = L$$
$$U/D = L$$

$$R/L = H$$
$$U/D = L$$

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	8

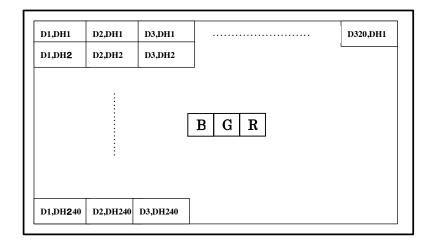
8. Input timing characteristics

8-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	1/Tc	_	6.3	7.0	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12	_	_	ns	
Data	Hold time	Tdh	12	_	_	ns	
	Cycle	TH	50.0	63.6	_	$\mu\mathrm{s}$	
	Cycle	ΙП	360	400	450	clock	
Horizontal sync. signal	Pulse width	ТНр	5	30	_	clock	
oigiidi	Set up time	THs	12	_	_	ns	
	Hold time	THh	12	_	_	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	12	_	_	ns	
	Hold time	TVh	12	_	_	ns	
	Pulse width	ТЕр		320		clock	
Enable signal (ENAB)	Set up time	TEs	12	_	_	ns	
(Bruil)	Hold time	TEh	12	_	_	ns	
H _{SYNC} - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. signal start position		TVE	2	18	38	line	
Horizontal display period		THd	320			clock	
Vertical display p	period	TVd		240		line	

^{*}When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.

8-2. Input Data Signals and Display position on the screen



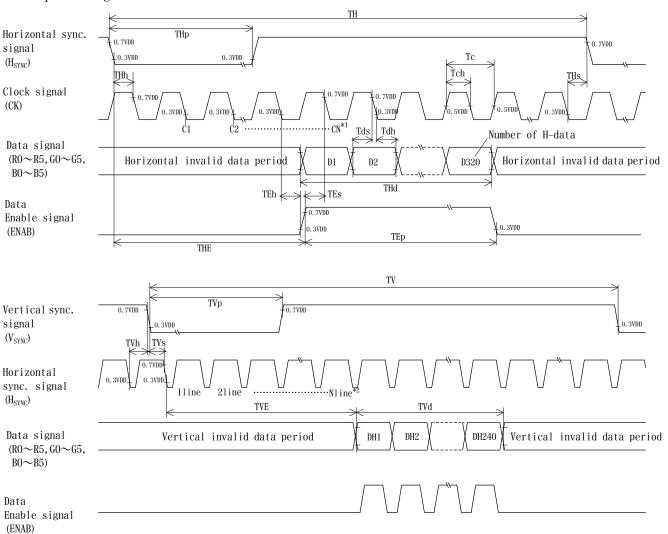


^{*}When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3.

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	9

8-3. Input timing characteristics



- *1 When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).
- *2 When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	${\tt TCG057QVLBA\text{-}G20}$	10

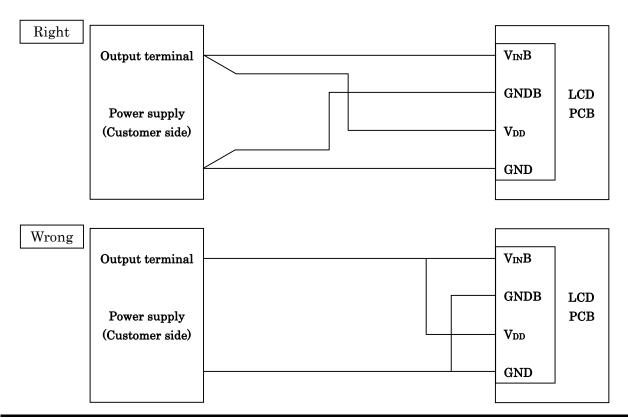
9. Backlight characteristics

Temp.=25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	_	5.5	V	Ta=-20∼70°C
ON-OFF (H)	BLEN	$0.8 V_{\mathrm{IN}} \mathrm{B}$	-	$V_{\rm IN} B$	V	Ta=-20∼70°C
ON-OFF (L)	DLEN	0	-	$0.2 { m V_{IN} B}$	V	Ta=-20∼70°C
LED forward current *1, *2	IF	14	15	16	mA	VBRT=0~1.4V
LED forward current "1, "2	11	2.8	3.0	3.2		VBRT=2.8V
Complex comment	ID	-	440	570	mA	V _{IN} B=3.3V, IF=15mA
Supply current	$I_{IN}B$	-	280	360	mA	V _{IN} B=5.0V, IF=15mA
Operating life time *3, *4	Т	-	40,000	-	h	IF=15mA, Ta=25°C

^{*1} For each "LED"

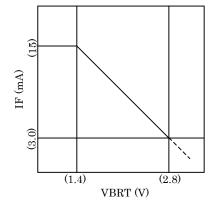
- *2 An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- ${\rm *}3$ When brightness decrease 50% of initial brightness.
- *4 Life time is estimated data. (Condition: IF=15mA, Ta=25°C in chamber)
- * When you start-up, please charge in sequence of V_{IN}B->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->V_{IN}B.
- * Please do not connect the other than our backlight to this output connector on the PCB.
- * In case V_{DD} and $V_{IN}B$ are supplied by a single power source, V_{DD} & $V_{IN}B$, and GND are connected directly and separately from the output on the power source. If the common wire are used for V_{DD} & $V_{IN}B$, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	11

* VBRT-IF characteristics





Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	12

10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	13

12. Precautions for use

12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DEG05-00	TCG057QVLBA-G20	14

13. Reliability test data

Test item Test condition		Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect: No defect: No defect

^{*} Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

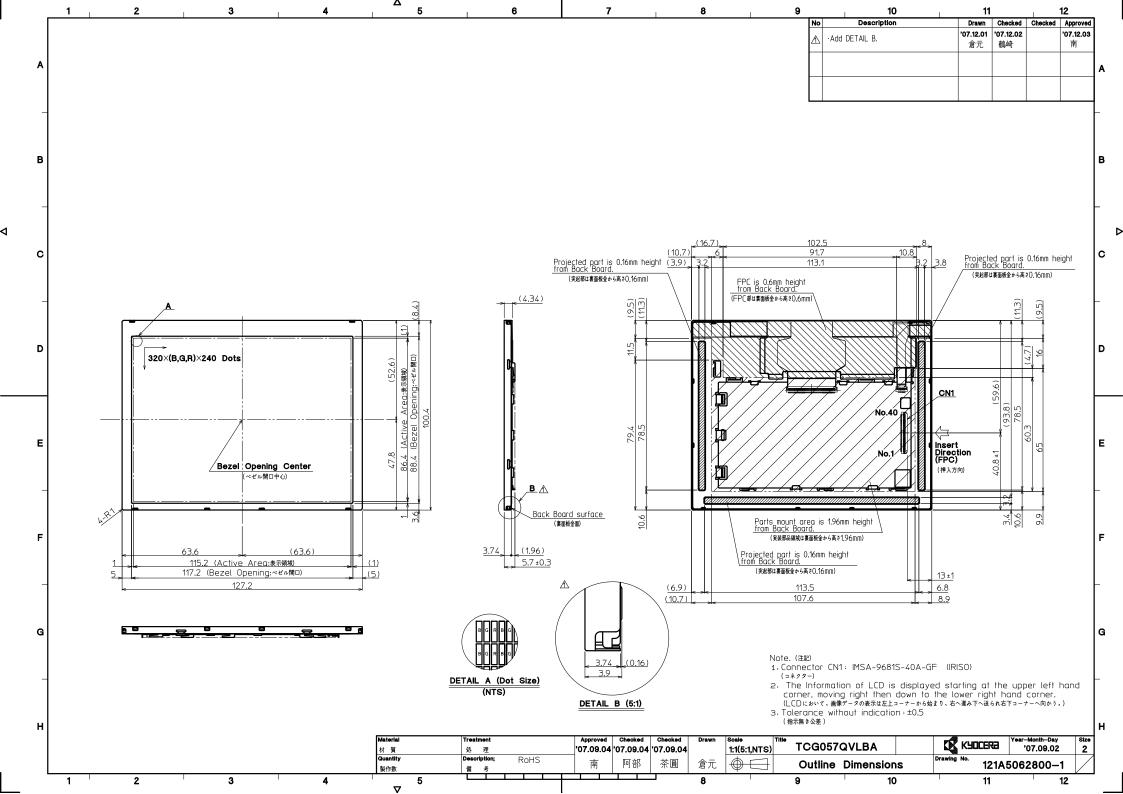


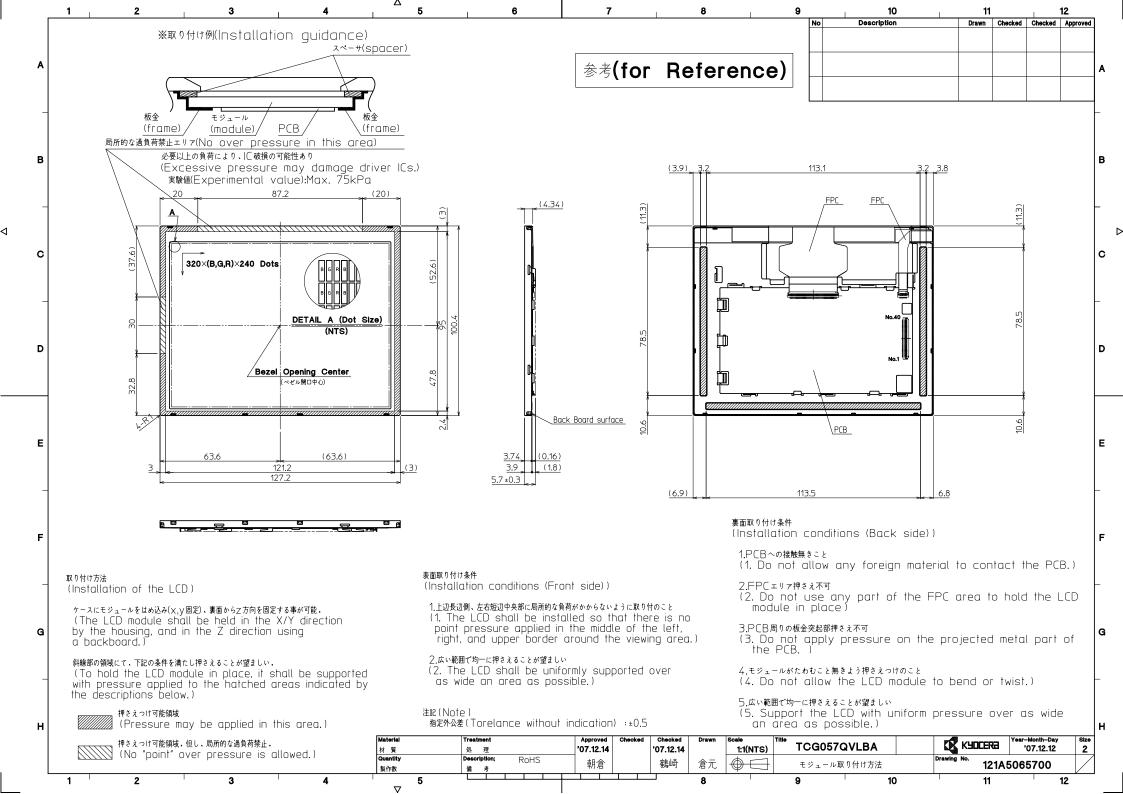
^{*} The LCD is tested in circumstances in which there is no condensation.

^{*} The reliability test is not an out-going inspection.

^{*} The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAF0-E2DEG05-00
Date	May 22, 2008

KYOCERA INSPECTION STANDARD

TYPE: TCG057QVLBA-G20

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by : Engineering dept.			Confirmed by : QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
May 22, 2008	O. Ajisaka	74. Johnson	G Matricmoto	J. Sakaguchi	Ho . Suf



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG05-00	TCG057QVLBA-G20	-

Revision record

Date		Designe	Designed by : Engineering dept. Confirmed by : QA de		: QA dept.		
	Date	Prepa	red	Checked	Approved	Checked	Approved
-		, .					
Rev.No.	Date	Page			Descripti	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG05-00	TCG057QVLBA-G20	1

Visuals specification 1) Note

1) Note			Note				
General	1 Custom						
General		1. Customer identified anomalies not defined within this inspection standard shall be					
	reviewed by Kyocera, and an additional standard shall be determined by mutual consent.						
	2. This inspection standard about the image quality shall be applied to any defect within						
	the active area and shall not be applicable to outside of the area.						
		ion conditions					
	Luminance		: 500 Lux min.				
	Inspection distance		300 mm.				
	Temperature Direction		$:25~\pm~5^{\circ}\mathrm{C}$				
			: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection item			LCD, even when all "Black" data sent to the screen.				
			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			RGBRGB				
			R G B R G B R G B dot defect				
		Black dot defect	The dot is constantly "off" when power applied to the				
		Black dot defect	LCD, even when all "White" data sent to the screen.				
	Adjacent dot		Adjacent dot defect is defined as two or more bright do				
			defects or black dot defects.				
			RGBRGBRGB				
			RGBRGB				
			RGBRGBRGB dot defect				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle	operating.				
		(Polarizer, Cell,					
		Backlight)					
		Appearance	Does not satisfy the value at the spec.				
		inspection					
	Others	LED wire	Damaged to the LED wires, connector, pin, functional				
		failure or appearance failure.					
	Definition	Definition of circle size Definition of linear size					
	of size						
		d = (a + b)/2					
		u = (a +	U // 4				



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEG05-00	TCG057QVLBA-G20	2

2) Standard

cation						_	
Ssification Inspection item Dot Bright dot defect		Judgement standard					
	-		Acceptable number : 4				
defect				Bright dot spacing : 5		5 mm or more	
	Black dot	defect	Acceptable number	: 5			
			Bright dot spacing : 5 mm or more			or more	
	2 dot join	Bright dot	Accontable number : 9				
		defect	Acceptable number		• 4		
		Black dot defect	Acceptable number		: 3		
	3 or more	dots join	Acceptable number		: 0		
	-				ζ		
Others							
Others		Durk dot	Size (mm			ceptable number	
	(011010)					(Neglected)	
			 			5	
						3	
			0.5 < d			0	
anastian	Polonizon (Samatah)					
ispection	r orarizer (Scrawn)	Width (mm)	Longeth ()	Acceptable number	
						(Neglected)	
				I, ≤ 5.0		(Neglected)	
			$0.1 < W \le 0.3$		_ 0.0	0	
iass)	0.		0.3 < W	_		0	
	Polonizon (Bubblo)					
		Dubble)	Cigo (man	.)	Λ α	aantahla mumban	
				$d \leq 0.2$ (Neglected)		i .	
			 			5	
						3	
						0	
	Foreign no	rtielo			!	<u> </u>	
			Sizo (mm	.)	Λοι	ceptable number	
(Circulal shape)				Acc	(Neglected)		
		 			5		
					3		
		0.5 < d		0			
	T7	.r.1.				_	
			377: 3/1 () T (T /)		A 1.1 1	
				(mm)	Acceptable number (Neglected)		
	Scratch		vv ≥ 0.03	1 < 20		(Neglected)	
			$0.03 < W \le 0.1$			(Negrected)	
						0	
			0.1 < W			(According to	
	Others Others aspection r blarizer lass)	Black dot of 2 dot join 3 or more of Total dot dot dot (Circle) Asspection Polarizer (Circle) Foreign particular (Circular)	Black dot defect Black dot defect 2 dot join Bright dot defect Black dot defect 3 or more dots join Total dot defects Others White dot, Dark dot (Circle) Polarizer (Scratch) r clarizer lass) Polarizer (Bubble) Foreign particle (Circular shape)			$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	

