			C N		1000.01
SDEC			Spec No.	TQ3C-8EAF0-E	
SPEC			Date	December 15, 20	JU7
<	<b>TYPE :</b> 5.7 inch G with LED	QVGA tran	smissive	color TFT	
		CON	TENTS		
	<ol> <li>Mecha</li> <li>Absolu</li> <li>Electri</li> <li>Optica</li> <li>Interfa</li> <li>Input</li> <li>LED b</li> <li>Design</li> <li>Lot nu</li> <li>Warra</li> <li>Precau</li> </ol>	ruction and out inical specificat ite maximum ra- ical characteris il characteristic ace signals timing character acklight character acklight character inguidance for a imber identification nty itions for use ility test data	ions atings tics s vristics teristics nalog touch p	Issued	
				Date: Dec.26,	2007
				KYDCERa	
				Hayato LCD Di	vision
	-	ion is subject to ra before orderi	-	KYOCERA CORI KAGOSHIMA HA LCD DIVISION out notice.	
Original	1	Engineering de		Confirmed by:	QA dept.
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 8, 2007	F-Asanc	7d. Johnword	4: Matsumor	O J. Sakaguchi	He Jap

Γ	Spec No.	Part No.	Page	
	TQ3C-8EAF0-E1DDE83-01	TCG057QVLBB-G00	-	

# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

# Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



				Spec No.			Part No.		Page
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Rev.No.	Date	Page			Descripti	ons			
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			0110			10000			



# 1. Application

This document defines the specification of TCG057QVLBB-G00. (RoHS Compliant)

## 2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input) (without constant current circuit for LED backlight)
Touch panel	: Analog type, Non-Glare treatment

## 3. Mechanical specifications

### 3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	127.2 (W)× 100.4 (H) ×6.75 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	$320 \times (B,G,R)$ (W) × 240 (H)	dot
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	Normally White	-
Mass	135	g

\*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

### 3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.05~0.8	Ν
Transmittance	Тур. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



### 4. Absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage for logic		Vdd	0	4.0	V
Input signal voltage	*1	VIN	-0.3	6.0	V
LED forward current	*2	IF	-	30	mA
Reversed voltage	*2	VR	-	5	V
Supply voltage for touch panel		$V_{\mathrm{TP}}$	0	6.0	V
Input current of touch panel	$I_{TP}$	0	0.5	mA	

4-1. Electrical absolute maximum ratings

\*1 Input signal : CK, R0~R5, G0~G5, B0~B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D \*2 For each "AN1-CA1", "AN2-CA2" and "AN3-CA3" Temp. = 25°C

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	°C
Storage temperature	*2	Тято	-30	80	°C
Operating humidity	*3	H <sub>OP</sub>	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

\*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

\*2 Temp. = -30°C $\leq$ 48h , Temp. = 80°C $\leq$ 168h

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)

\*3 Non-condensing

\*4 Temp.≦40°C, 85%RH Max.

Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	$10\sim 55~{ m Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz l minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

\*6 Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

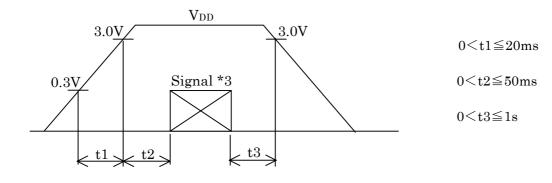
3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



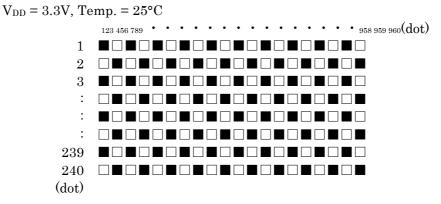
# 5. Electrical characteristics

					Temp. = -2	0∼70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	$V_{DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	$I_{DD}$	*2	-	60	80	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	-	-	100	mVp-p
I	VIL	"Low" level	0	-	0.3Vdd	V
Input signal voltage *3	VIH	"High" level	$0.7 V_{DD}$	-	$V_{DD}$	V

## \*1 $V_{\rm DD}\mbox{-turn-on conditions}$



### \*2 Display pattern:



\*3 Input signal : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

## 5-2. Touch panel

Item	Specification
Supply voltage for touch panel	5.0V
	$xL\sim xR$ : 200 $\Omega$ $\sim$ 1,000 $\Omega$
Terminal resistance	$yU \sim yL : 200\Omega \sim 800\Omega$
Linearity	less than ±2.0%
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$



Spec No. Part No. Page TQ3C-8EAF0-E1DDE83-01 TCG057QVLBB-G00

# 6. Optical characteristics

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

4

					suring spot	$\psi$ 0.011111, 10	1	
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	
	Rise	$\tau_{\rm r}$	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	25	-	ms	
x7· · 1		heta upper		-	80	-	dom	
Viewing angle View deriction		heta lower	$CR \ge 5$	-	80	-	deg.	
: 12 o'clo		$\phi_{ m  LEFT}$	UR≦3	-	80	-	dom	
(Gray inversion)		$\phi$ RIGHT		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	300	500	-	-	
Brightness	Brightness		IF=15mA/Line	165	240	-	$cd/m^2$	
	Red x	x	$\theta = \phi = 0^{\circ}$	0.57	0.62	0.67		
		У		0.32	0.37	0.42		
	Course	x	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
Chromaticity	Green	reen y	$0 = \phi = 0$	0.54	0.59	0.64	_	
coordinates	Blue	x	$\theta = \phi = 0^{\circ}$	0.09	0.14	0.19	-	
	Diue	У	$v - \psi - 0$	0.04	0.09	0.14		
	White	х	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
	wnite	У	$v - \psi = 0$	0.30	0.35	0.40		

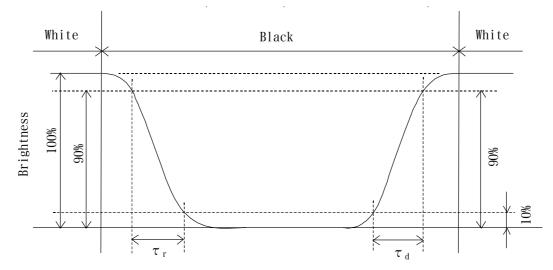
6-1. Definition of contrast ratio

CR(Contrast ratio) =

Brightness with all pixels "White"

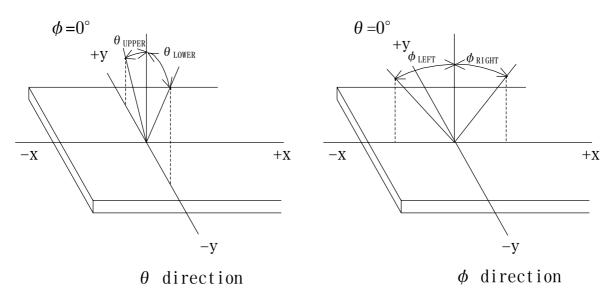
Brightness with all pixels "Black"

### 6-2. Definition of response time

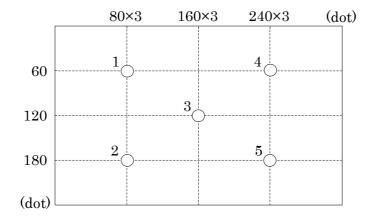




6-3. Definition of viewing angle



6-4. Brightness measuring points



1) Rating is defined on the average in the viewing area.

2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



# 7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	Ι	
3	H <sub>SYNC</sub>	Horizontal synchronous signal (negative)	I	
4	V <sub>SYNC</sub>	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	V <sub>DD</sub>	3.3V power supply	-	
29	V <sub>DD</sub>	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	Ι	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	Ι	*2
32	NC	No connect	-	
33	CA1	Cathode 1	-	1
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

7-1. Pin assignment of LCD panel and LED

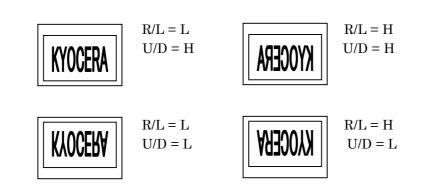
LCD connector Recommended matching FFC or FPC : IMSA-9681S-40A-GF (IRISO)

: 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDE83-01	TCG057QVLBB-G00	7

\*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.



7-2. Pin assignment of touch panel

\*2

No.	Symbol	Description			
1	уL	y-Lower terminal			
2	xL	x-Left terminal			
3	yU	y-Upper terminal			
4	xR	x-Right terminal			

Touch panel side connector	:	1.25mm pitch	
Recommended matching connector	:	Series FE, FFS	(JST)
	:	KCA-K4R	(DMC)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDE83-01	TCG057QVLBB-G00	8

# 8. Input timing characteristics

	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	—	6.3	7.0	MHz	
CIOCK	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12	—	—	ns	
Data	Hold time	Tdh	12	—	—	ns	
	Cuele	тц	50.0	63.6	—	$\mu{ m s}$	
	Cycle	П	360	400	450	clock	
Horizontal sync. signal	Pulse width	THp	5	30	—	clock	
orginar	Set up time	THs	12	—	—	ns	
	Hold time	THh	12	—	—	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Set up timeTHs12 $ -$ nsHold timeTHh12 $ -$ nsCycleTV251262280line	line					
signal	Set up time	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	ns				
signal Enable signal	Hold time	TVh	12	—	—	ns	
	Pulse width	TEp	320		clock		
Enable signal (ENAB)	Set up time	TEs	12	—	—	ns	
	Hold time	TEh	12	—	—	ns	
H <sub>SYNC</sub> - Enable s	ignal phase difference	THE	36	68	88	clock	
Vertical sync. sig	nal start position	TVE	2	18	38	line	
Horizontal displa	ay period	THd	320		clock		
Vertical display	period	TVd		240		line	

### 8-1. Timing characteristics

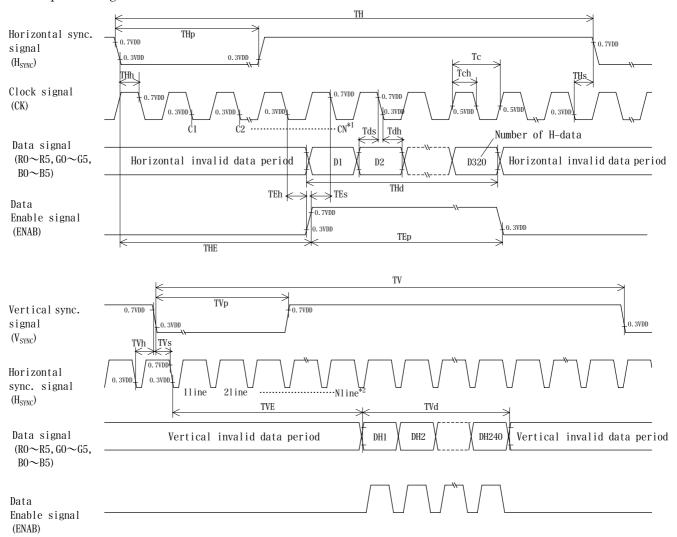
\*When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.

\*When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3. \*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1		D320,DH1
D1,DH <b>2</b>	D2,DH2	D3,DH2		
		-	_	
			B G R	
		I		
			_	
D1,DH <b>2</b> 40	D2,DH240	D3,DH240		





### 8-3. Input timing characteristics

\*1 When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).

\*2 When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).

## 9. LED backlight characteristics

### LED ratings

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	*1	IF	-	15	-	mA	Ta=-20∼70°C
	*1		-	22.1	25.0	V	IF=15mA, Ta=-20°C
Forward voltage		VF	-	21.7	24.5	V	IF=15mA, Ta=25°C
			-	21.3	24.1	V	IF=15mA, Ta=70°C
Operating life time	*2, *3	Т	-	40,000	-	h	IF=15mA, Ta=25°C

\*1 For each "AN1-CA1", "AN2-CA2" and "AN3-CA3"

\*2 When brightness decrease 50% of initial brightness.

\*3 Life time is estimated data. .(Condition : IF=15mA, Ta= $25^{\circ}$ C in chamber).

\* An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.



# 10. Design guidance for analog touch panel (T/P)

- 10-1 Electrical (In customer's design, please remember the following considerations.)
  - 1 Do not use the current regulated circuit.
  - 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
  - 3 Analog T/P can not sense two points touching separately.
  - 4~A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
  - 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

## 10-2 Software

- 1 Do the "User Calibration".
- 2 "User Caribration" may be needed with long term using.
- Include "User Caribration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

## 10-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3 If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



## 11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $TCG057QVLBB-G00 - \Box\Box - \Box\Box - \Box$  MADE IN  $\Box\Box\Box\Box\Box$ 

$\downarrow\downarrow\downarrow$	$\downarrow$	$\downarrow$
$1\ 2$	3	4

- No1. No5. above indicate
  - 1. Year code
  - 2. Month code
  - 3. Date
  - 4. Version Number
  - 5. Country of origin (Japan or China)

 $\downarrow 5$ 

Year	2007	2008	2009	2010	2011	2012
Code	7	8	9	0	1	2

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

## 12. Warranty

### 12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

## 12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



### 13. Precautions for use

- 13-1. Installation of the LCD
  - 1) The LCD shall be installed so that there is no pressure on the LSI chips.
  - 2) The LCD shall be installed flat, without twisting or bending.
  - 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

#### 13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.
- 13-3. LCD operation
  - 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
- Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

### 13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



## 14. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul> <li>No defect</li> <li>No defect</li> <li>No defect</li> <li>No defect</li> </ul>

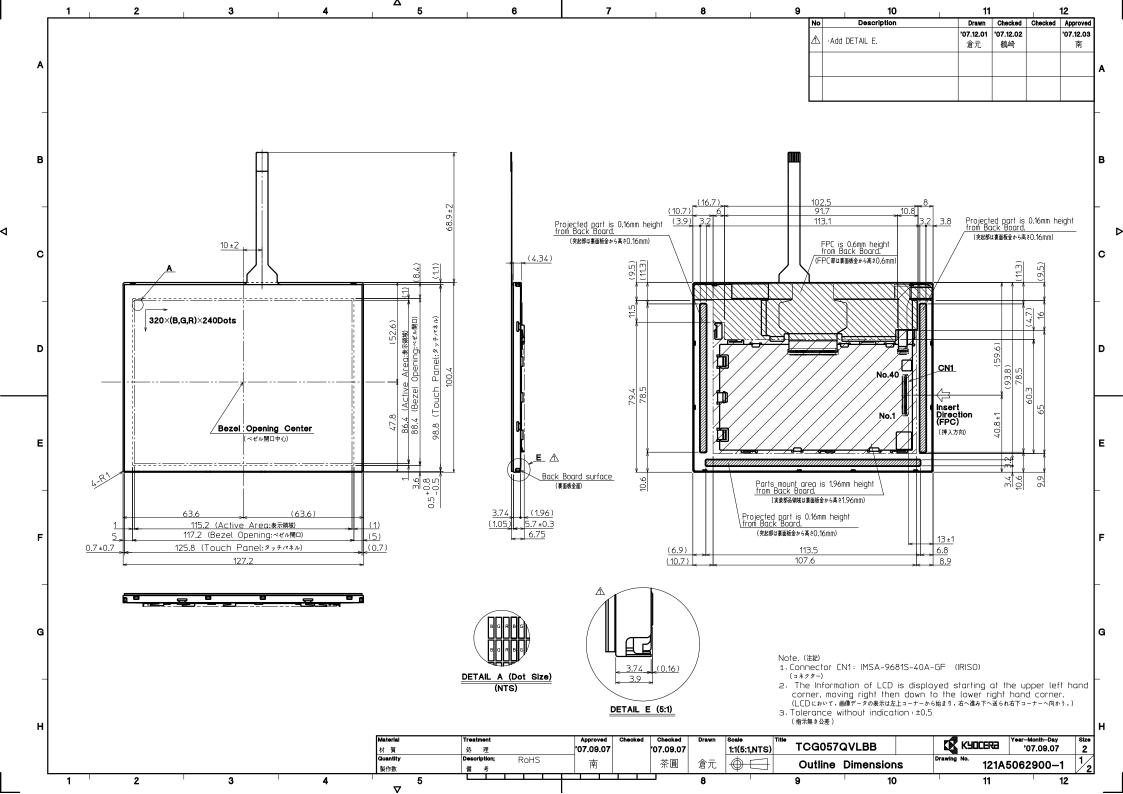
\* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

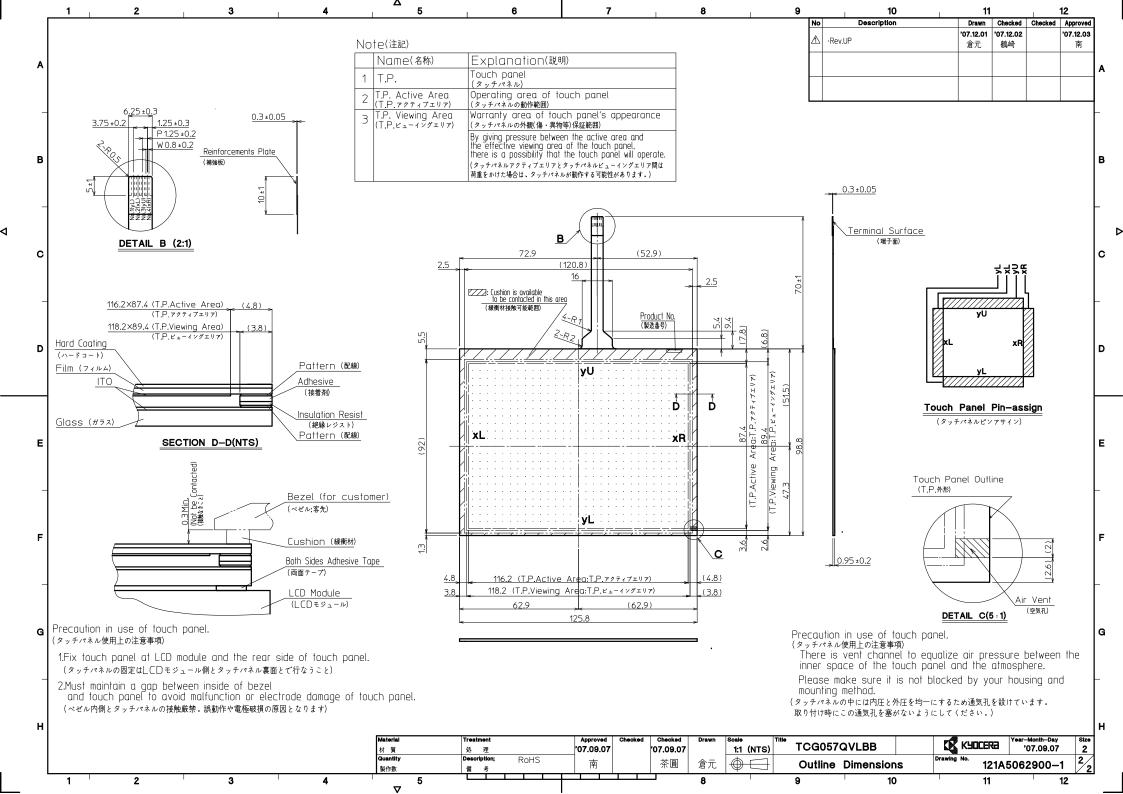
\* The LCD is tested in circumstances in which there is no condensation.

\* The reliability test is not an out-going inspection.

\* The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2DDE81-00
Date	September 8, 2007

# KYOCERA INSPECTION STANDARD

# **TYPE : TCG057QVLBB-G00**

## KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 8, 2007	Y. Yamayaki	74. John	G. Matsumoto	Y. Nibo	Je Sal



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDE81-00	TCG057QVLBB-G00	-

Revision record							
	Date			Engineering of		Confirmed by	
	Date	Prepared		Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	
L		I					



			Note					
General	<ol> <li>Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</li> <li>This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area.</li> </ol>							
	3. Inspection conditions							
	Lumina		: 500 Lux min.					
	Inspect	ion distance	: 300 mm.					
	Temper	rature	$:25 \pm 5^{\circ}\mathrm{C}$					
	Directi	on	: Directly above					
Definition of inspection item	Dot defect	Bright dot defect Black dot defect Adjacent dot	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen.         Inspection tool: 5% Transparency neutral density filter.         Count dot: If the dot is visible through the filter         Don't count dot: If the dot is not visible through the filter         R       B         R       B         R       B         R       B         R       B         R       B         R       B         R       B         R       B         R       B         R       B         Count dot: If the dot is not visible through the filter         Don't count dot: If the dot is not visible through the filter         Don't count dot: If the dot is not visible through the filter         Don't count dot: If the dot is not visible through the filter         B       R         B       R         B       R         B       R         B       R         B       R         B       R         B       R         Count dot       If the dot is not visible through the filter         Dot defect       dot defect         Adjacent dot defect is defined as two or mo					
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight) Appearance	Visible operating (all pixels "Black" or "White") and nor operating.         Does not satisfy the value at the spec.					
		inspection						
	Others	LED wire	Damaged to the LED wire, connector, pin, functional failure or appearance failure.					
	Definition of size	Definition of circle size Definition of linear size						

# Visuals specification



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDE81-00	TCG057QVLBB-G00	2

## 2) Standard

Classif	fication	Inspection item		Judgement standard				
Defect	Dot	Bright dot		Acceptable number : 4				
(in LCD	defect	0		Bright dot spacing		5 mm or more		
glass)	dereet	Black dot defect		Acceptable number		: 5 mm or more		
8-0000				Bright dot spacing		-	ı or more	
		2 dot join Bright dot		Acceptable number		: 2		
			defect Black dot	Acceptable number		: 3		
			defect			. 0		
		3 or more o	-	Acceptable number		:0		
		Total dot d	efects	Acceptable number		: 5 Ma	X	
	Others	White dot,	Dark dot					
		(Circle)		Size (mr		Ac	ceptable number	
					0.2		(Neglected)	
				0.2 < d ≦			5	
				$0.4 < d \leq 0.5 < 1$	• 0.5		3	
				0.5 < d			0	
External	inspection	Polarizer (	Scratch)					
(Defect on	1			Width (mm)	Length (	mm)	Acceptable number	
Polarizer	or			$W \leq 0.1$	_		(Neglected)	
between H	Polarizer			$0.1 < W \le 0.3$	L ≦	≦ 5.0	(Neglected)	
and LCD	glass)			-	5.0 < L		0	
				0.3 < W	_		0	
		Polarizer (	Bubble)					
				Size (mr	n)	Ac	ceptable number	
				$\begin{array}{c} d \ \leq \ 0.2 \\ \hline 0.2 \ < \ d \ \leq \ 0.3 \end{array}$		(Neglected) 5		
				$0.3 < d \le 0.5$		3		
				0.5 < d			0	
		Foreign pa	rticle					
		(Circle sh	ape)	Size (mr	n)	Ac	ceptable number	
				d $\leq$ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
				$0.4 < d \le 0.5$			3	
				0.5 < d			0	
Foreign		Foreign pa	rticle					
		(Linear shape)		Width (mm) Lengt		ngth (mm) Acceptable numb		
		Scratch		$W \leq 0.03$			(Neglected)	
						$\leq 2.0$	(Neglected)	
				$0.03 < W \leq 0.1$	2.0 < L		3	
					4.0 < L		0	
				0.1 < W	-		(According to	
							circular shape)	



Spec No.Part No.PageTQ3C-8EAF0-E2DDE81-00TCG057QVLBB-G003

<b>r</b> ,									
Inspection item	Judgement standard								
Scratch,	(W = Width, L = Length, D = Diameter = (major axis+minor axis)/2)								
Foreign particle	Item Width(mm) Length(mm) Acceptable number								
(Touch screen		$0.08 < d \le 0.1 \qquad L \le$		$4 \leq 4$ 1pces		s within $\phi$ 30mm			
portion)	Scratch	$0.05  <  \mathrm{d} \leq 0.08$	L ≦	$L \leq 6$ 2pce		s within $\phi 20$ mm			
	Scratch	$0.03  <  \mathrm{d} \leq 0.05$	$L \leq$	$L \le 10$ 2pces		s within $\phi 20$ mm			
		$d \leq 0.03$	$L \leq$	20		Neglected			
	Foreign	$0.05 < W \le 0.1$	L ≦	$\leq 5$	2pces	s within $\phi$ 30mm			
	(line like)	$W \le 0.05$	Negle	ected		Neglected			
	Foreign	$0.2 < D \leq$			2pce	s within $\phi$ 30mm			
	(circle like)	$D \leq$	0.2			Neglected			
		ied to the visible area.							
		are foreign particle a		-		-			
	electrical perfo	rmance out of the activ	re area,	we ap	prove o	f this product.			
Glass crack	Item	Size (m	nm)			Acceptable			
(Touch screen						number			
portion)			-	Х	$\leq 3$				
	Conner	>/./	Y	<i></i>	2 pcs				
	crack				$\leq 3$	/panel			
				Z	< t				
	Crack in				$\leq 5$				
	other area			Y	≤1.5	2 pcs			
	than in					/side			
	corner	2		Ζ	$<_{\rm t}$				
				/	,				
	Progressive		$\checkmark$	/		0 pcs			
	crack		~~			(NG even 1pcs)			
		$\rightarrow$							
	Above are appl	ied to the visible area.				<u>                                     </u>			
		are foreign particle a	and dar	nage	affecte	d seriously to the			
		rmance out of the active		-		-			
	-			1		_			
Newton's ring	Neglected.	ſ							
	_								
			<del>ار</del> «		Newton'	s ring			

