				TQ3C-8EAF0-E		
SPEC			Date	December 15, 20	007	
<	TYPE : < 5.7 inch ` with LED	VGA trans	smissive c	olor TFT		
		CON	TENTS			
	 Mecha Absolu Electri Optica Interfa Input a LED b Design Lot nu Warran Precau Reliab 	ruction and outl nical specificat ite maximum ra- ical characteristic ace signals timing character acklight character acklight character mber identification nty itions for use ility test data	ions atings tics s eristics eteristics nalog touch pa	.nel (T/P)		
	15. Outline drawing Issued Date: Dec.26,2007 KUDCERA Hayato LCD Division					
	-	ion is subject to	K L change withou	YOCERA CORI AGOSHIMA HA CD DIVISION at notice.		
	1	ra before orderi	·			
Original Issue Date	Designed by: Prepared	Engineering de Checked	pt. Approved	Confirmed by: Checked	QA dept. Approved	
September 19, 2007	_	7d. Johnson		J. Sakaguchi		

]	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1DDP32-01	TCG057VGLBB-G00	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



					Spec No.	A EQ E1DDDD0	.1	Part No.	GLBB-G00	Page
			D		_	AF0-E1DDP32-0	1	10000770	000-000	-
	Revision record Designed by : Engineering dept. Confirmed by : QA dept.									
	Date	Prepa		-	hecked	Approved	1	Checked	Approve	
Dec	2. 15, 2007	_				4. Matsumoti	J.;	Sakaguchi	Je Var	l
Rev.No.	Date	Page				Descripti				
01	Dec. 15, 2007	1	~Add 3. Mee	2. Construction and outline ~Add comment "Additional circuit" 3. Mechanical specifications ~Change "Mass"						
		2	4. Abs \sim Del		e maximun)	n ratings				
		3	\sim Del	ete (eristics umption value'	"			
		4	6. Opt \sim Delo \sim Cha	ete ()		stics				
		9								
		14	14 15. Outline Drawing ~Change "121A5063100" → "121A5063100-1"							



1. Application

This document defines the specification of TCG057VGLBB-G00. (RoHS Compliant)

2. Construction and outline

LCD Backlight system	: Transmissive color dot matrix type TFT : LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input)
	(without constant current circuit for LED backlight)
Touch panel	: Analog type, Non-Glare treatment

3. Mechanical specifications

3-1. Mechanical specifications of LCD

Item	Specification	Unit
Outline dimensions	127.2 (W)× 100.4 (H) ×6.75 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	$640 \times (B,G,R)$ (W) × 480 (H)	dot
Dot pitch	0.06 (W) × 0.18 (H)	mm
Base color *1	Normally White	-
Mass	135	g

*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Mechanical specifications of touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	$0.05 \sim 0.8$	Ν
Transmittance	Тур. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



4. Absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V _{DD}	0	4.0	V
Input signal voltage *1	VIN	-0.3	6.0	V
LED forward current *2	IF	-	30	mA
Reversed voltage *2	VR	-	5	V
Supply voltage for touch panel	VTP	0	6.0	V
Input current of touch panel	I _{TP}	0	0.5	mA

4-1. Electrical absolute maximum ratings

*1 Input signal : CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D *2 For each "AN1-CA1", "AN2-CA2" and "AN3-CA3" Temp. = 25°C

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	°C
Storage temperature	*2	Tsto	-30	80	°C
Operating humidity	*3	Hop	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -30°C \leq 48h , Temp. = 80°C \leq 168h

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)

*3 Non-condensing

*4 Temp.≦40°C, 85%RH Max.

Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	$10\sim 55~{ m Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz l minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

*6 Acceleration: 490 m/s², Pulse width: 11 ms

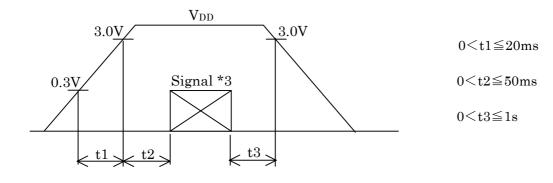
3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



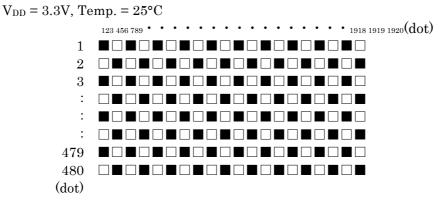
5. Electrical characteristics

					Temp. = -2	0∼70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	V_{DD}	-	3.0	3.3	3.6	V
Current consumption for logic	I_{DD}	*2	-	170	220	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
Least stress less les us *9	VIL	"Low" level	0	-	$0.3 V_{DD}$	V
Input signal voltage *3	VIH	"High" level	$0.7 V_{DD}$	-	V_{DD}	V

*1 $V_{\text{DD}}\text{-turn-on conditions}$



*2 Display pattern:



*3 Input signal : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

5-2. Touch panel

Item	Specification
Supply voltage for touch panel	5.0V
	$xL\sim xR$: 200 Ω \sim 1,000 Ω
Terminal resistance	$yU \sim yL : 200\Omega \sim 800\Omega$
Linearity	less than $\pm 2.0\%$
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$



Spec No.Part No.PageTQ3C-8EAF0-E1DDP32-01TCG057VGLBB-G004

6. Optical characteristics

Measuring spot = ϕ 6.0mm, Temp. = 25°C

					suring spot	ψ 0.011111, 10	1	
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	
Rise		$\tau_{\rm r}$	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	25	-	ms	
x7· · 1		heta upper		-	80	-	dom	
Viewing angle View deriction		heta lower	$CR \ge 5$	-	80	-	deg.	
÷ 6 o'cloc		$\phi_{ m LEFT}$		-	80	-	Jam	
(Gray inversion)		ϕ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	300	500	-	-	
Brightness	Brightness		IF=15mA/Line	140	200	-	cd/m^2	
	Red	х	$\theta = \phi = 0^{\circ}$	0.56	0.61	0.66		
	neu	У		0.32	0.37	0.42		
	Green	х	$\theta = \phi = 0^{\circ}$	0.29	0.34	0.39		
Chromaticity	Green	У	$0 = \psi = 0$	0.52	0.57	0.62	_	
coordinates	Blue	x	$\theta = \phi = 0^{\circ}$	0.09	0.14	0.19	_	
	Diue	У	$v - \psi = 0$	0.06	0.11	0.16		
	White	х	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
	wnite	У	$v - \psi = 0$	0.30	0.35	0.40		

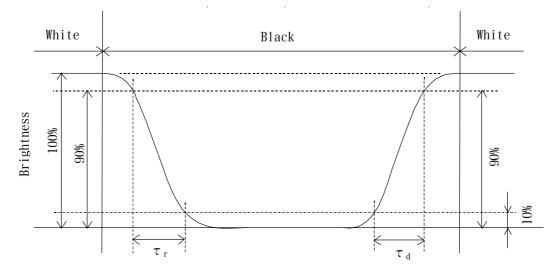
6-1. Definition of contrast ratio

CR(Contrast ratio) =

Brightness with all pixels "White"

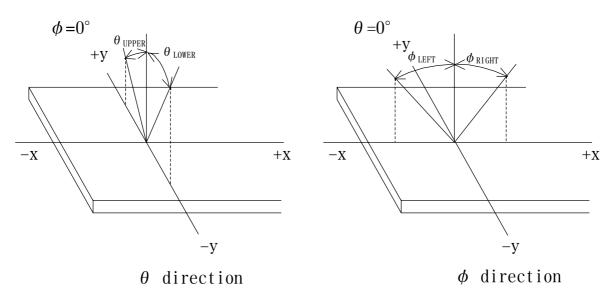
Brightness with all pixels "Black"

6-2. Definition of response time

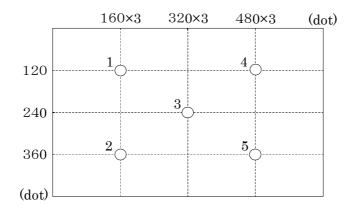




6-3. Definition of viewing angle



6-4. Brightness measuring points



1) Rating is defined on the average in the viewing area.

2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	СК	Clock signal for sampling each data signal	Ι	
3	H _{SYNC}	Horizontal synchronous signal (negative)	Ι	
4	V _{SYNC}	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	V _{DD}	3.3V power supply	-	
29	V _{DD}	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	Ι	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	Ι	*2
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

7-1. Pin assignment of LCD panel and LED

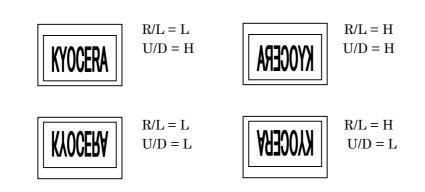
LCD connector Recommended matching FFC or FPC : IMSA-9681S-40A-GF (IRISO)

: 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP32-01	TCG057VGLBB-G00	7

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.



7-2. Pin assignment of touch panel

*2

No.	Symbol	Description
1	yL	y-Lower terminal
2	xL	x-Left terminal
3	уU	y-Upper terminal
4	xR	x-Right terminal

Touch panel side connector	:	1.25mm pitch	
Recommended matching connector	:	Series FE, FFS	(JST)
	:	KCA-K4R	(DMC)



8. Input timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Clock	Frequency	1/Tc	—	25.18	28.88	MHz	
CIOCK	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	10	—	—	ns	
Data	Hold time	Tdh	10	—	—	ns	
	Cycle	TH	30.0	31.8	—	$\mu{ m s}$	
		ТП	770	800	900	clock	
Horizontal sync. signal	Pulse width	THp	5	30	—	clock	
Sigilai	Set up time	THs	10	—	—	ns	
	Hold time	THh	10	_	—	ns	
	Cycle	TV	515	525	560	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	10	—	—	ns	
	Hold time	TVh	10	_	—	ns	
	Pulse width	TEp		640		clock	
Enable signal (ENAB)	Set up time	TEs	10	_	—	ns	
	Hold time	TEh	10	—	—	ns	
H _{SYNC} - Enable signal phase difference		THE	112	144	175	clock	
$H_{\rm SYNC}$ - $V_{\rm SYNC}$ signal phase difference		THV	10	_	—	ns	
Vertical sync. sig	nal start position	TVE	2	35	76	line	
Horizontal displ	ay period	THd	640			clock	
Vertical display	period	TVd	480			line	

8-1. Timing characteristics

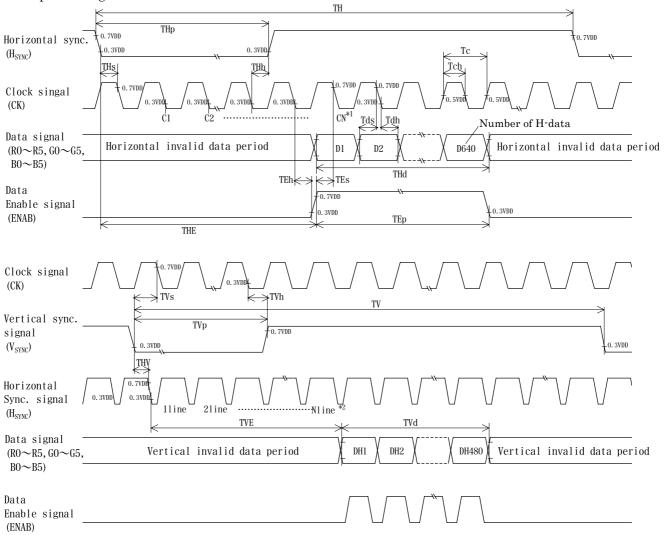
*When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock) as shown in 8-3.

*When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line) as shown in 8-3. *In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Input Data Signals and Display position on the screen

D1, DH1D2, DH1D3, DH1D1, DH2D2, DH2D3, DH2		D640, DH1
	B G R	
D1, DH480 D2, DH480 D3, DH480		





8-3. Input timing characteristics

*1 When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock). *2 When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line).

9. LED backlight characteristics

LED ratings

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	*1	IF	-	15	-	mA	Ta=-20∼70°C
			-	22.1	25.0	V	IF=15mA, Ta=-20°C
Forward voltage	*1	VF	-	21.7	24.5	V	IF=15mA, Ta=25°C
			-	21.3	24.1	V	IF=15mA, Ta=70°C
Operating life time	*2, *3	Т	-	40,000	-	h	IF=15mA, Ta=25°C

*1 For each "AN1-CA1", "AN2-CA2" and "AN3-CA3"

*2 When brightness decrease 50% of initial brightness.

*3 Life time is estimated data. (Condition : IF=15mA, Ta= 25° C in chamber).

* An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.



10. Design guidance for analog touch panel (T/P)

- 10-1 Electrical (In customer's design, please remember the following considerations.)
 - 1 Do not use the current regulated circuit.
 - 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
 - 3 Analog T/P can not sense two points touching separately.
 - 4~A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
 - 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2 Software

- 1 Do the "User Calibration".
- 2 "User Caribration" may be needed with long term using.
- Include "User Caribration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3 If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $TCG057VGLBB \cdot G00 \cdot \Box\Box \cdot \underline{\Box\Box} \cdot \Box \quad MADE IN \quad \underline{\Box\Box\Box\Box\Box}$

$\downarrow\downarrow\downarrow$	\downarrow	\downarrow
$1 \ 2$	3	4

- No1. No5. above indicate
 - 1. Year code
 - 2. Month code
 - 3. Date
 - 4. Version Number
 - 5. Country of origin (Japan or China)

 $\downarrow 5$

Year	2007	2008	2009	2010	2011	2012
Code	7	8	9	0	1	2

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



13. Precautions for use

- 13-1. Installation of the LCD
 - 1) The LCD shall be installed so that there is no pressure on the LSI chips.
 - 2) The LCD shall be installed flat, without twisting or bending.
 - 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
- Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



14. Reliability test data

Test item	Test condition	Test time	Judgement		
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect	
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect	
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	 No defect No defect No defect No defect 	

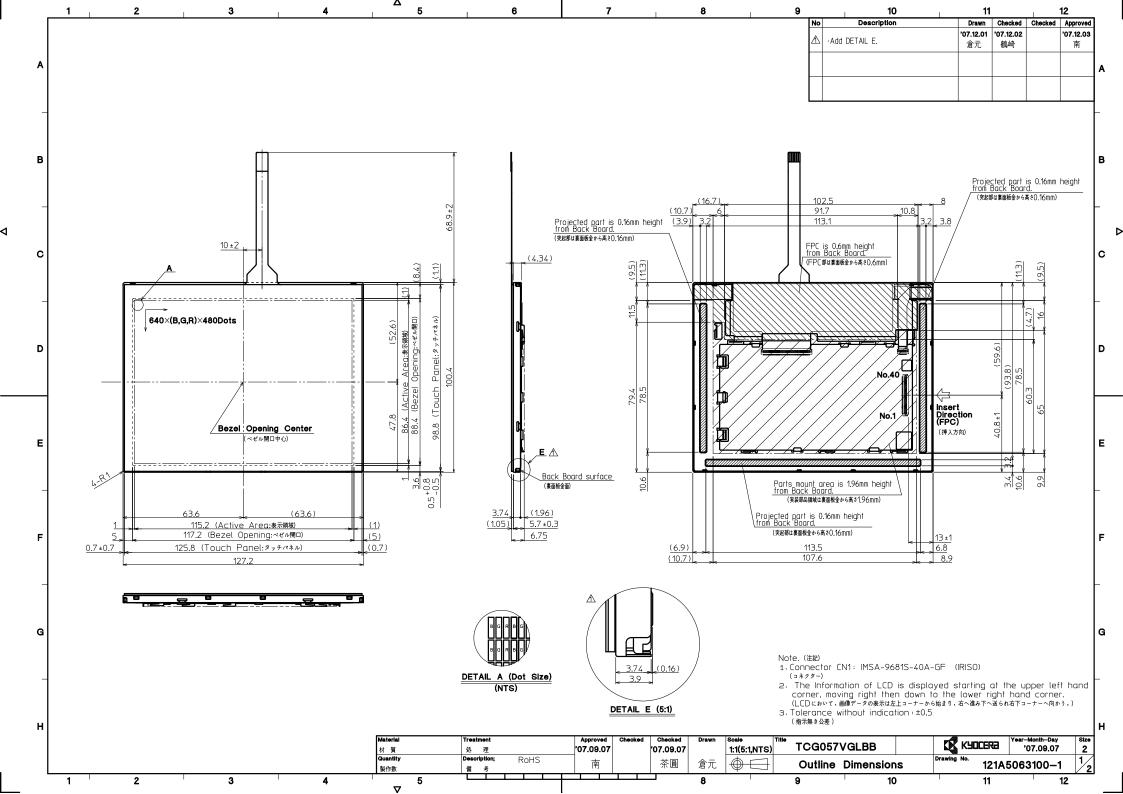
* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

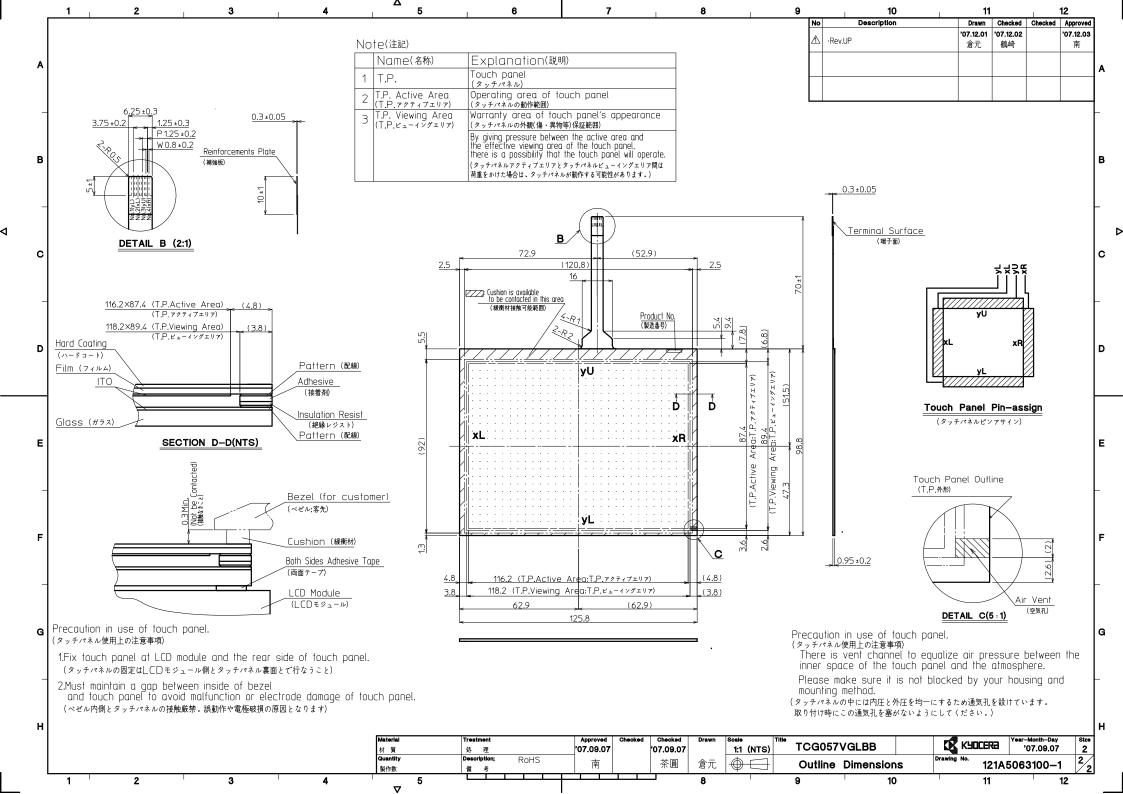
* The LCD is tested in circumstances in which there is no condensation.

* The reliability test is not an out-going inspection.

* The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2DDP32-00
Date	September 19, 2007

KYOCERA INSPECTION STANDARD

TYPE : TCG057VGLBB-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by :	Engineering de	ept.	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
September 19, 2007	Y. Yamayaki	74. John	4. Matxemoto	J. Sakaguchi	Je Jaf	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP32-00	TCG057VGLBB-G00	-

	Revision record							
	Date			Engineering of		Confirmed by		
	Date	Prepa	ared	Checked	Approved	Checked	Approved	
Rev.No.	Date	Page			Descripti	ons		



			Note			
General	 Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 					
	Lumina	ion distance rature	: 500 Lux min. : 300 mm. : 25 ± 5°C : Directly above			
Definition of inspection item	Dot defect	Bright dot defect Black dot defect Adjacent dot	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter Don't count dot: If the dot is not visible through the filter R G B R G B R G B R G B R G B R G B R G B R G B R G B dot defect The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen. Adjacent dot defect is defined as two or more bright dot defects or black dot defects. R G B R G B R G B R G B R G B R G B Adjacent dot defect as defined as two or more bright dot defects or black dot defects.			
	External inspection Others	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight) Appearance inspection LED wire	Visible operating (all pixels "Black" or "White") and non operating. Does not satisfy the value at the spec. Damaged to the LED wire, connector, pin, functional			
	Definition of size	LED wire Damaged to the LED wire, connector, pin, function failure or appearance failure. Definition of circle size Definition of linear size $d = (a + b)/2$				

Visuals specification



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP32-00	TCG057VGLBB-G00	2

2) Standard

2) Standar		T ·	• • .		T Jack ()	. 1 1		
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect				:4		
(in LCD	defect			Bright dot spacing		: 5 mm or more		
glass)		Black dot defect		Acceptable number		5		
		2 dot join Bright dot defect Black dot defect		Bright dot spacing : 5 mm o		5 mm or more		
				Acceptable number	Acceptable number : 2			
				Acceptable number	3			
		3 or more o	lots join	Acceptable number	: (0		
		Total dot d	efects	Acceptable number	:	5 Max		
	Others	White dot,	Dark dot	-				
		(Circle)		Size (mm		Acceptable number	٦	
		(d ≦		(Neglected)	_	
				$0.2 < d \leq$		5	_	
				$0.4 < d \leq$		3		
				0.5< m d		0		
			~ . 1)					
External i	-	Polarizer (Scratch)		(N	7	
(Defect on				Width (mm)	Length (mm		_	
Polarizer				$W \leq 0.1$	-	(Neglected)	_	
between P				$0.1 < W \le 0.3$	$L \leq 5$		_	
and LCD §	glass)			5.0 < I 0.3 < W -		0	_	
				0.3 < ₩		0		
		Polarizer (Bubble)				_	
				Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \leq$		5		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign pa	rticle					
		(Circle shape)		Size (mm))	Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \leq$	0.4	5		
				$0.4 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign pa	rticle					
		(Linear shape)		Width (mm) Length		h (mm) Acceptable number		
				$W \leq 0.03$		(Neglected)	-	
					L ≦		\neg	
				$0.03 < W \le 0.1$	$2.0 < L \le$		\neg	
					4.0 < L	0		
				0.1 < W	-	(According to	\neg	
						circular shape)		
						L - ,		



Spec No.Part No.PageTQ3C-8EAF0-E2DDP32-00TCG057VGLBB-G003

Inspection item	Judgement standard							
Scratch,	(W = Width, L = Length, D = Diameter = (major axis + minor axis)/2)							
Foreign particle	Item	Width(mm)	Lengtl	h(mm) Acc	eptable number		
(Touch screen		$0.08 < d \le 0.1$ $L \le 4$		1pce	1pces within ϕ 30mm			
portion)	Gaugetal	$0.05 < d \le 0.08$ L \le		$L \leq 6$ 2pces		s within $\phi 20$ mm		
	Scratch	$0.03 < d \le 0.05$	L≦	$L \le 10$ 2pces		s within $\phi 20$ mm		
		$d \le 0.03$ L \le		$L \leq 20$		Neglected		
	Foreign	$0.05 < W \le 0.1 \qquad L \le 5$		2pces	s within ϕ 30mm			
	(line like)	$W \leq 0.05$	Negl	ected		Neglected		
	Foreign	$0.2 < D \leq$	0.3		2pce	s within ϕ 30mm		
	(circle like)	$D \leq$	0.2			Neglected		
		lied to the visible area.						
		are foreign particle a		-		-		
	electrical perfo	ormance out of the activ	e area,	we ap	oprove o	f this product.		
Glass crack	Item	Size (m)			Acceptable		
(Touch screen	Item	Size (II	1111/			number		
portion)			1	Х	≤ 3			
	Conner	~ / /	2			2 pcs		
	crack			Y	≤ 3	/panel		
				7	<i></i>	partor		
				Ζ	< t			
	Crack in		-	Х	≤ 5			
	other area		~	Y	≤1.5	2 pcs		
	than in				=1.0	/side		
	corner	2		\mathbf{Z}	< t			
				/				
		-	/	/ /				
	Progressive		X,	/		0 pcs		
	crack	$\langle \vee \rangle$	Y			(NG even 1pcs)		
	Above are app	lied to the visible area.						
		are foreign particle a						
	electrical perfo	rmance out of the active	e area, '	we ap	prove of	f this product.		
Newton's ring	Neglected.							
newtonsing	regieueu.							
	-				NT			
			ッ / ト		Newton'	s ring		

