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| SPEC.NO. | TQ3C-8EAF0-E1DDH35-00 |
| DATE | March 27, 2008 |

FOR : _____

TYPE : TCG075VGLAG-G00

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KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

| | | | | | |
|----------------|--------------------------------|---------------------|--------------------|------------------------|---------------|
| Original | Designed by :Engineering Dept. | | | Confirmed by :QA Dept. | |
| Issue Date | Prepared | Checked | Approved | Checked | Approved |
| March 27, 2008 | <i>D. Ajisaka</i> | <i>7d. Ichimori</i> | <i>Y. Marumoto</i> | <i>J. Sakaguchi</i> | <i>Z. Ash</i> |

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

| | | | | | |
|----------|--------------------------------|---------|--------------|------------------------|----------|
| Date | Designed by: Engineering Dept. | | | Confirmed by: QA Dept. | |
| | Prepared | Checked | Approved | Checked | Approved |
| | | | | | |
| Rev. No. | Date | Page | Descriptions | | |
| | | | | | |

1. Application

This data sheet defines the specification for a 640×(R.G.B)×480 dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with LED backlight. 『RoHS Compliant』

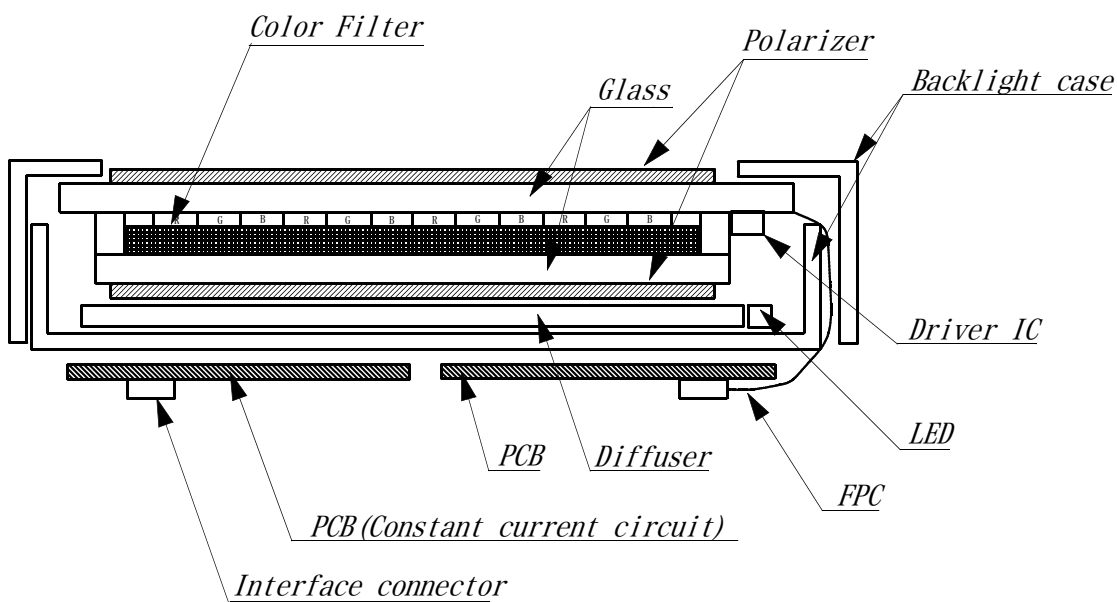
2. Construction and Outline

640×(R. G. B)×480 dots, COG type LCD with LED backlight.

Backlight system : Side-edge type (LED).

Polarizer : Glare treatment.

Additional circuits : Timing controller, Power supply (3.3V input)
Constant current circuit for LED backlight.



This drawing is showing conception only.

3. Mechanical Specifications

| ITEM | SPECIFICATION | UNIT |
|------------------------|--------------------------------|------|
| Outline dimensions | 184 (W) × 139.8 (H) × 12.7 (D) | mm |
| Effective viewing area | 153.7 (W) × 115.8 (H) | mm |
| Dot number | 640 × (R. G. B) (W) × 480 (H) | Dots |
| Dot pitch | 0.079 (W) × 0.237 (H) | mm |
| Display mode *1 | Normally white | — |
| Mass | (380) | g |

*1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

| ITEM | SYMBOL | Min. | Max. | UNIT |
|---------------------------------|--------|------|------|------|
| Power input voltage | VDD | 0 | 4.0 | V |
| Input signal voltage for LCD *1 | Vin | -0.3 | 6.0 | V |
| Supply voltage for backlight | VinB | 0 | 6.0 | V |
| Backlight ON-OFF | BLIN | 0 | VinB | V |
| Brightness adjust voltage | VBRT | 0 | VinB | V |

*1 Input signals : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

| ITEM | SYMBOL | Min. | Max. | UNIT |
|--------------------------|--------|------|------|--------|
| Operating temperature *1 | Top | -10 | 70 | deg. C |
| Storage temperature *2 | Tsto | -30 | 80 | deg. C |
| Operating humidity *3 | Hop | 10 | *4 | %RH |
| Storage humidity *3 | Hsto | 10 | *4 | %RH |
| Vibration | — | *5 | *5 | — |
| Shock | — | *6 | *6 | — |

*1 Operating temperature means a temperature which operation shall be guaranteed.
Since display performance is evaluated at 25 deg.C, another temperature range should be confirmed.

*2 Temp. = -30 °C < 48 h , Temp = 80 °C < 168 h
Store LCD panel at normal temperature/humidity.
Keep it free from vibration and shock.
LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.
(Please refers to " Precautions for use" as detail).

*3 Non-condensation.

*4 Temp. ≤ 40 deg.C, 85%RH Max.
Temp. > 40 deg.C, Absolute Humidity shall be less than 85% RH at 40 deg.C.

*5

| | | |
|-----------------|-------------|---|
| Frequency | 10~55 Hz | Converted to acceleration value : (0.3~9 m/s ²) |
| Vibration width | 0.15 mm | |
| Interval | 10-55-10 Hz | 1 minute |

2 hours in each direction X/Y/Z (6 hours as total)
EIAJ ED-2531

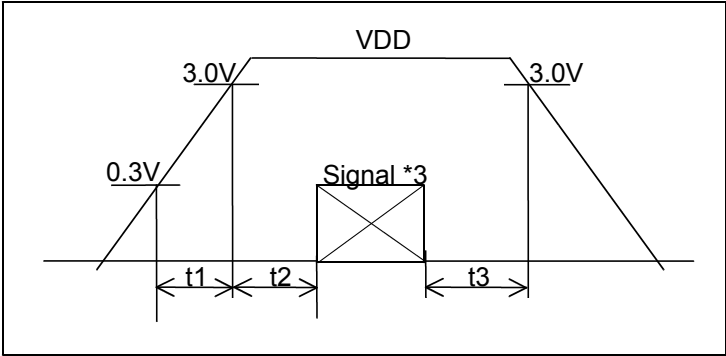
*6 Acceleration: 490m/s²
Pulse width : 11 ms
3 times in each direction : ±X/±Y/±Z.
EIAJ ED-2531

5. Electrical Characteristics

Temp. = -10~70℃

| ITEM | SYMBOL | MIN | TYP | MAX | UNIT |
|--|--------|--------|-----|--------|-------|
| Power input voltage *1 | VDD | 3.0 | 3.3 | 3.6 | V |
| Current consumption *2 | IDD | — | 290 | 435 | mA |
| VDD=3.3V Temp.=25℃ | | | | | |
| Permissive input ripple voltage (VDD=3.3V) | VRP | — | — | 100 | mVp-p |
| Input signal voltage (Low) *3 | VIL | 0 | — | 0.3VDD | V |
| Input signal voltage (High) *3 | VIH | 0.7VDD | — | VDD | V |

*1 VDD-turn-on conditions

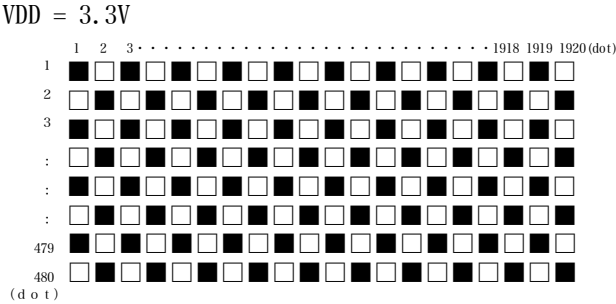


$$0 < t_1 \leq 20 \text{ ms}$$

$$0 < t_2 \leq 50 \text{ ms}$$

$$0 < t_3 \leq 1 \text{ s}$$

*2 Power consumption
pattern :



*3 Input signals : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

6. Optical Characteristics

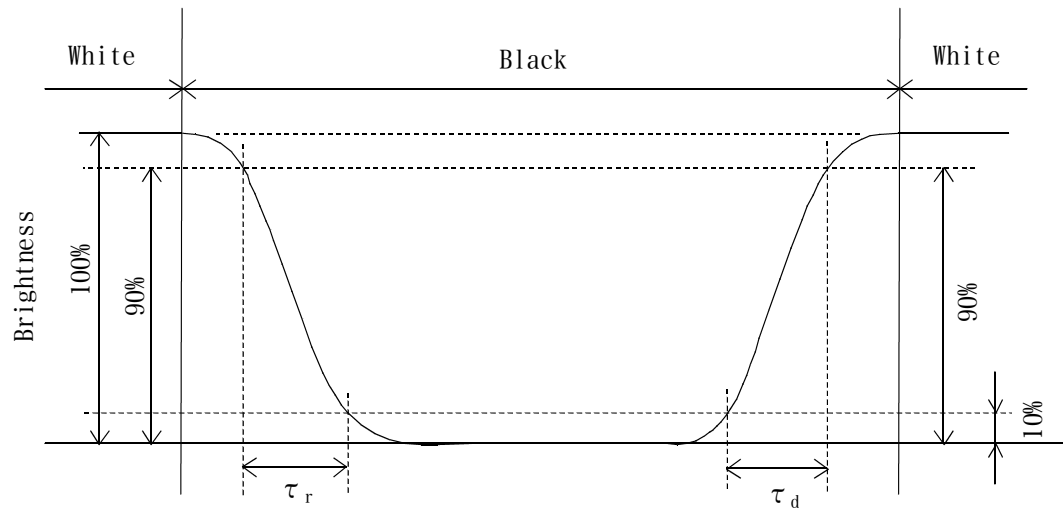
Measuring points = ϕ 6.0mm , Temp. = 25°C

| ITEM | | SYMBOL | CONDITION | | MIN | TYP | MAX | UNIT |
|--------------------------|-------|----------|---------------------------|-------|------|------|------|-------------------|
| Response time | Rise | τ_r | $\theta = \phi = 0^\circ$ | | — | 5 | — | ms |
| | Down | τ_d | $\theta = \phi = 0^\circ$ | | — | 15 | — | ms |
| Viewing angle range | | θ | $CR \geq 5$ | Upper | — | 50 | — | deg. |
| | | | | Lower | — | 70 | — | |
| | | ϕ | | Left | — | 70 | — | deg. |
| | | | | Right | — | 70 | — | |
| Contrast ratio | | CR | $\theta = \phi = 0^\circ$ | | 300 | 450 | — | — |
| Brightness | | | IF=25mA/1LED Line | | 220 | 280 | — | cd/m ² |
| Chromaticity coordinates | Red | x | $\theta = \phi = 0^\circ$ | | 0.54 | 0.59 | 0.64 | — |
| | | y | | | 0.31 | 0.36 | 0.41 | |
| | Green | x | $\theta = \phi = 0^\circ$ | | 0.29 | 0.34 | 0.39 | |
| | | y | | | 0.52 | 0.57 | 0.62 | |
| | Blue | x | $\theta = \phi = 0^\circ$ | | 0.10 | 0.15 | 0.20 | |
| | | y | | | 0.09 | 0.14 | 0.19 | |
| | White | x | $\theta = \phi = 0^\circ$ | | 0.28 | 0.33 | 0.38 | |
| | | y | | | 0.30 | 0.35 | 0.40 | |

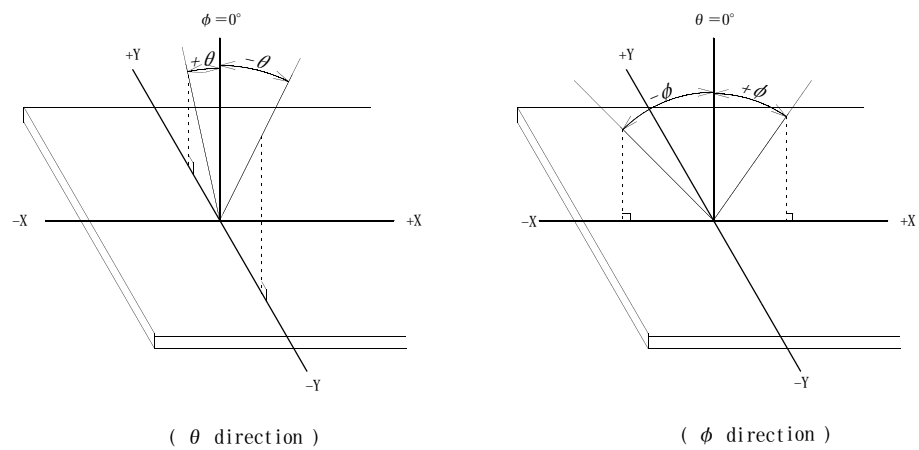
6-1. Contrast ratio is defined as follows:

$$CR = \frac{\text{Brightness at all pixels "White"}}{\text{Brightness at all pixels "Black"}}$$

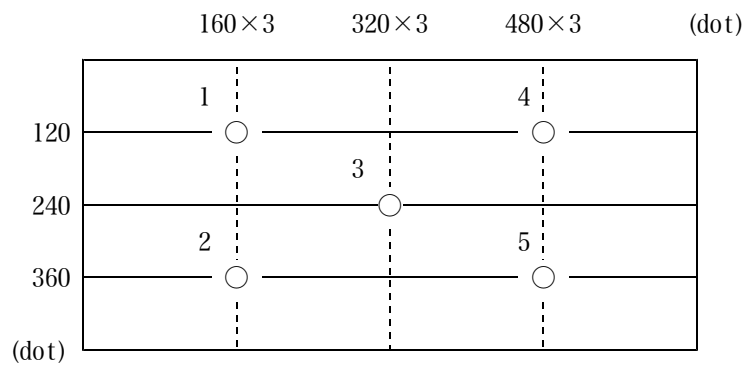
6-2. Definition of response time



6-3. Definition of viewing angle



6-4. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after LED is turned on. (Ambient Temp.=25°C)

7. Interface signals

7-1. CN1:LCD

| PIN NO. | SYMBOL | DESCRIPTION | I/O | Note |
|---------|--------|---|-----|------|
| 1 | GND | GND | — | |
| 2 | CK | Clock signal for sampling each data signal | I | |
| 3 | Hsync | Horizontal synchronous signal (negative) | I | |
| 4 | Vsync | Vertical synchronous signal (negative) | I | |
| 5 | GND | GND | — | |
| 6 | R0 | RED data signal (LSB) | I | |
| 7 | R1 | RED data signal | I | |
| 8 | R2 | RED data signal | I | |
| 9 | R3 | RED data signal | I | |
| 10 | R4 | RED data signal | I | |
| 11 | R5 | RED data signal (MSB) | I | |
| 12 | GND | GND | — | |
| 13 | G0 | GREEN data signal (LSB) | I | |
| 14 | G1 | GREEN data signal | I | |
| 15 | G2 | GREEN data signal | I | |
| 16 | G3 | GREEN data signal | I | |
| 17 | G4 | GREEN data signal | I | |
| 18 | G5 | GREEN data signal (MSB) | I | |
| 19 | GND | GND | — | |
| 20 | B0 | BLUE data signal (LSB) | I | |
| 21 | B1 | BLUE data signal | I | |
| 22 | B2 | BLUE data signal | I | |
| 23 | B3 | BLUE data signal | I | |
| 24 | B4 | BLUE data signal | I | |
| 25 | B5 | BLUE data signal (MSB) | I | |
| 26 | GND | GND | — | |
| 27 | ENAB | Signal to settle the horizontal display position (positive) | I | *1 |
| 28 | VDD | 3.3V power supply | — | |
| 29 | VDD | 3.3V power supply | — | |
| 30 | R/L | Horizontal display mode select signal L : Normal , H : Left / Right reverse mode | I | *2 |
| 31 | U/D | Vertical display mode select signal H : Normal , L : Up / Down reverse mode | I | *2 |
| 32 | NC | No connect | — | |
| 33 | GND | GND | — | |

LCD side connector : 08-6210-033-340-800+ (ELCO)

Recommended matching FFC or FPC : Pitch=0.5mm

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2.
Don't keep ENAB "High" during operation.

*2



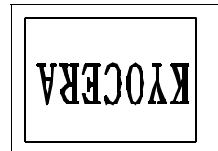
R/L = L
U/D = H



R/L = H
U/D = H



R/L = L
U/D = L



R/L = H
U/D = L

7-2. CN2:LED Backlight

| PIN No. | SYMBOL | DESCRIPTION |
|---------|--------|-------------------------------------|
| 1 | VinB | Supply voltage |
| 2 | VinB | |
| 3 | VinB | |
| 4 | VinB | |
| 5 | VinB | |
| 6 | VinB | |
| 7 | VinB | |
| 8 | VinB | |
| 9 | BLDN | Backlight ON-OFF (H : ON , L : OFF) |
| 10 | VBRT | Brightness adjust voltage |
| 11 | GND | GND |
| 12 | GND | |
| 13 | GND | |
| 14 | GND | |
| 15 | GND | |
| 16 | GND | |
| 17 | GND | |
| 18 | GND | |

LED Backlight side connector
Recommended matching FFC or FPC

: 08-6212-018-340-800+ (ELCO)
: Pitch=0.5mm

8. Timing Characteristics of input signals

8-1. Timing characteristics

| ITEM | | SYMBOL | MIN | TYP | MAX | UNIT | NOTE |
|-------------------------------------|-------------|--------|------|-------|--------|---------|------|
| Clock | Frequency | 1/Tc | — | 25.18 | 28.33 | MHz | |
| | Duty ratio | Tch/Tc | 40 | 50 | 60 | % | |
| Data | Set up time | Tds | 5 | — | — | ns | |
| | Hold time | Tdh | 10 | — | — | ns | |
| Horizontal sync. signal | Cycle | TH | 30.0 | 31.8 | — | μ s | |
| | | | 770 | 800 | 900 | clock | |
| | Pulse width | THp | 2 | 96 | 200 | clock | |
| Vertical sync. signal | Cycle | TV | 515 | 525 | 560 | line | |
| | Pulse width | TVp | 2 | — | 34 | line | |
| Horizontal display period | | THd | 640 | | | clock | |
| Hsync.-Clock phase difference | | THc | 10 | — | Tc-10 | ns | |
| Hsync.-Vsync. phase difference | | TVh | 0 | — | TH-THp | ns | |
| Vertical sync.signal start position | | TVs | 34 | | | line | |
| Vertical display period | | TVd | 480 | | | line | |

* In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position

The horizontal display position is determined by ENAB signal.

| ITEM | | SYMBOL | MIN | TYP | MAX | UNIT | NOTE |
|---------------------------------------|-------------|--------|-----|-----|--------|-------|------|
| Enable signal | Set up time | Tes | 5 | — | Tc-10 | ns | |
| | Pulse width | Tep | 2 | 640 | TH-10 | clock | |
| Hsync.-Enable signal phase difference | | The | 44 | — | TH-664 | clock | |

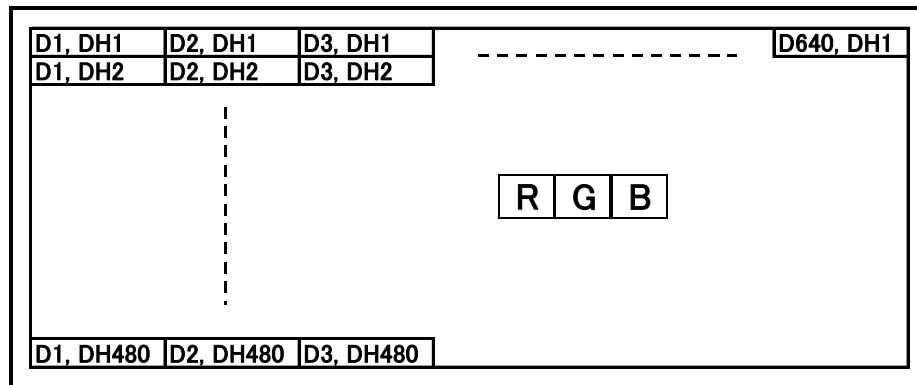
*When ENAB is fixed at "Low," the display starts from the data of C104(clock) as shown in 8-5.

8-3. Vertical display position

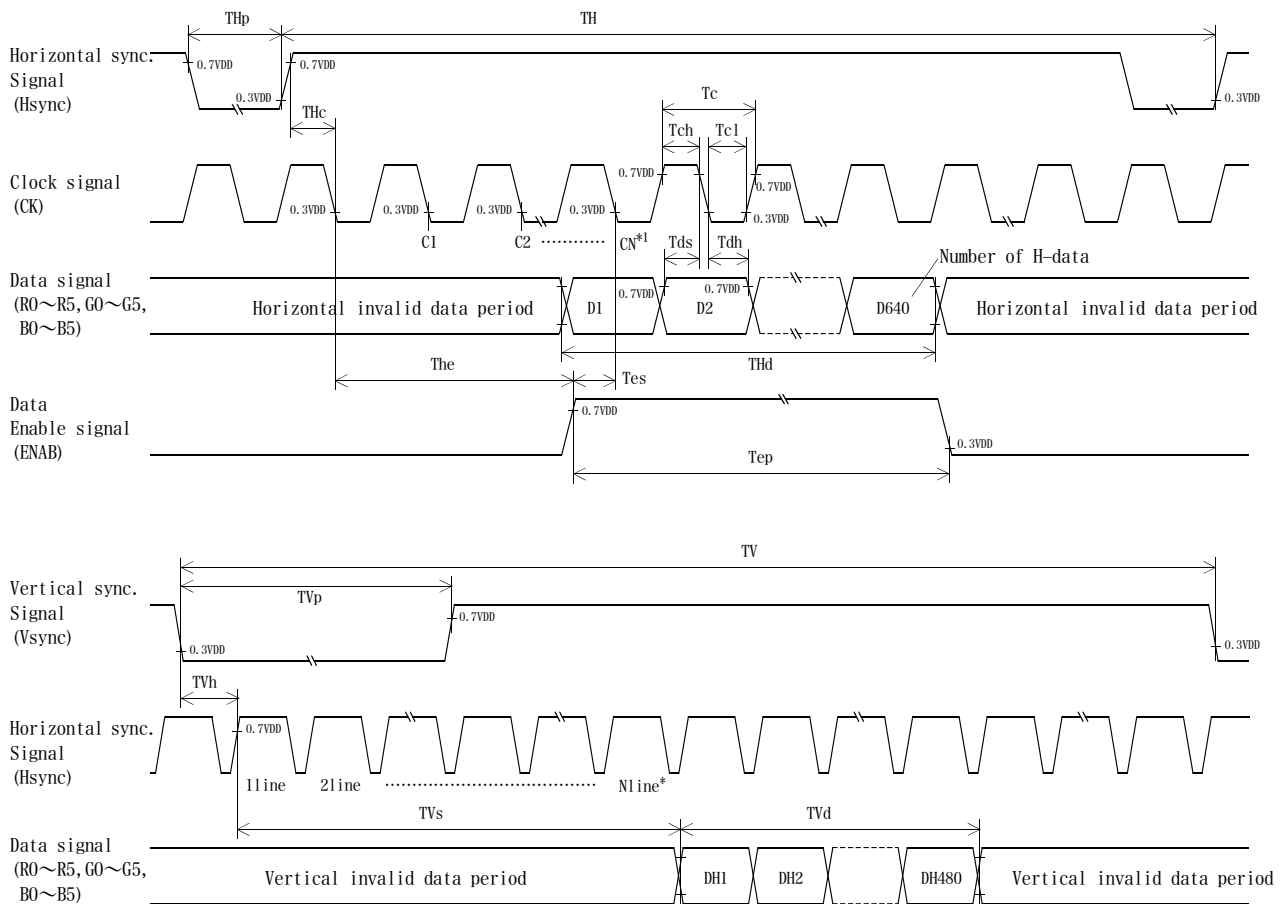
The vertical display position (TVs) is fixed at 34th line.

Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen



8-5. Input Timing Characteristics



*1 When ENAB is fixed "Low" the display starts from the data of C104(Clock).

*2 The vertical display position(TVs) is fixed at 34th line.

9. Backlight Characteristics

Temp. = 25°C

| ITEM | Symbol | MIN | TYP. | MAX | UNIT | NOTE |
|---------------------------------|--------|---------|----------|----------|------|--------------------|
| Supply voltage | VinB | 3.0 | — | 5.5 | V | -10~70°C |
| ON-OFF (ON) | BLEN | 0.8VinB | — | VinB | V | -10~70°C |
| ON-OFF (OFF) | | 0.0 | — | 0.2VinB | | -10~70°C |
| LED forward current *1 *2 *5 | IF | — | 25.0 | — | mA | VBRT=0~1.4V |
| | | — | 5.0 | — | | VBRT=2.8V |
| Supply current | IinB | — | T. B. D. | T. B. D. | mA | VinB=3.3V, IF=25mA |
| | | — | T. B. D. | T. B. D. | | VinB=5.0V, IF=25mA |
| Operating Life *3 *4 | T | — | 50,000 | — | h | IF=25mA |

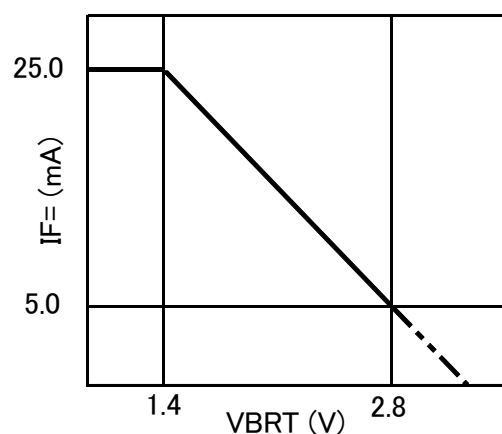
*1 For each LED.

*2 A forward current below 8.0mA may reduce the brightness uniformity of the LED backlight.
This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

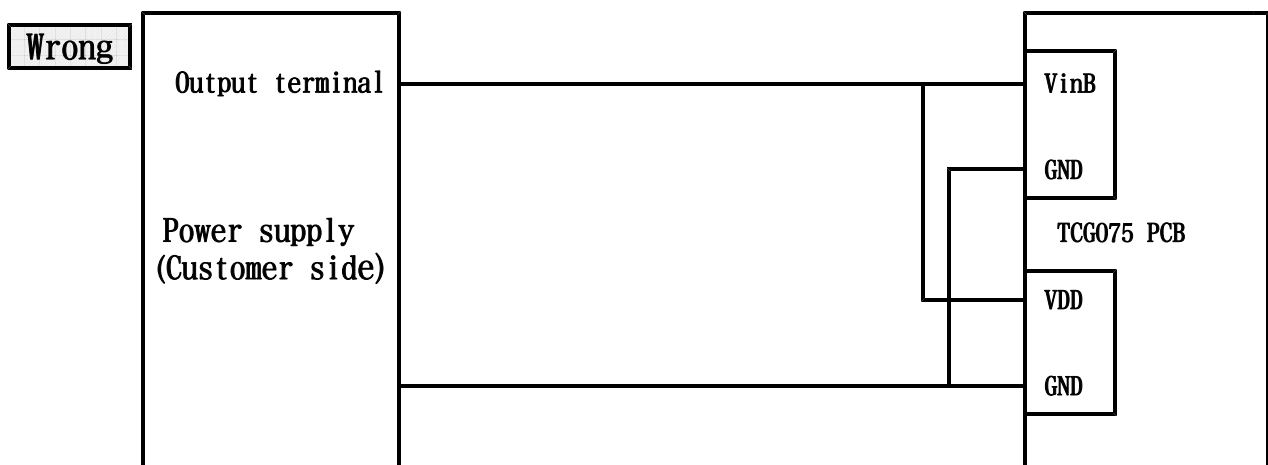
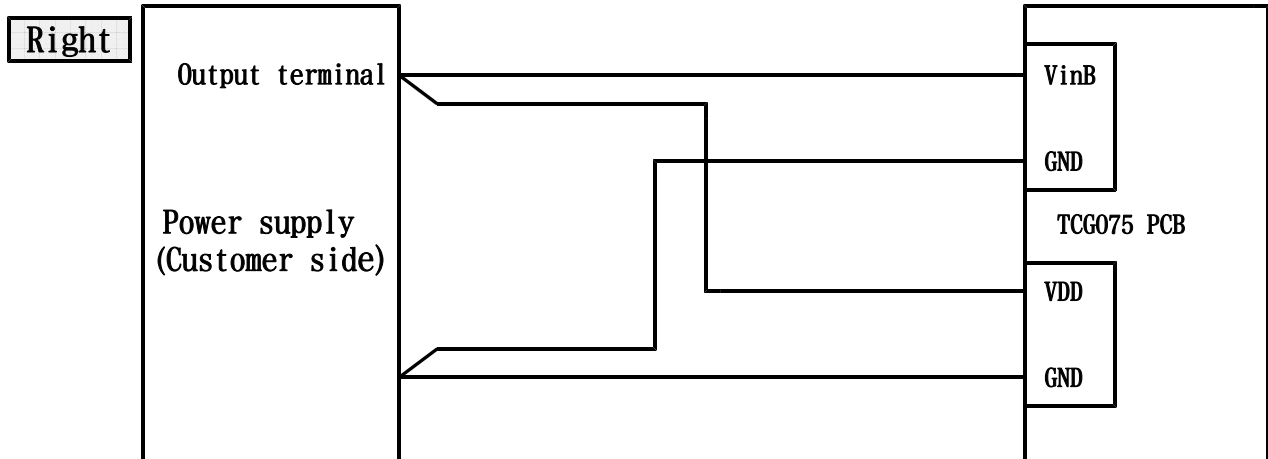
*3 When surface brightness decreases 50% of initial brightness .

*4 Life time is estimated data.

*5 VBRT-IF characteristics

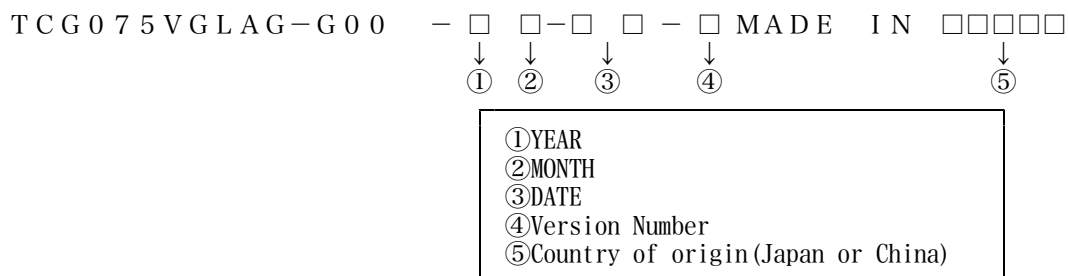


- * When you start-up, please charge in sequence of VinB->BLEN, or VBRT.
When you shut-down, please stop in sequence of BLEN and/or VBRT->VinB.
- * Please do not connect the other than our backlight to this output connector on the PCB.
- * In case VDD and VinB are supplied by a single power source, VDD & VinB, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & VinB, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.



1 0. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



| | | | | | | |
|------|------|------|------|------|------|------|
| YEAR | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 |
| CODE | 8 | 9 | 0 | 1 | 2 | 3 |

| | | | | | | |
|-------|------|------|------|------|------|------|
| MONTH | JAN. | FEB. | MAR. | APR. | MAY. | JUN. |
| CODE | 1 | 2 | 3 | 4 | 5 | 6 |

| | | | | | | |
|-------|------|------|------|------|------|------|
| MONTH | JUL. | AUG. | SEP. | OCT. | NOV. | DEC. |
| CODE | 7 | 8 | 9 | X | Y | Z |

1 1. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

1 2. Precautions for use

12-1. Installation of the LCD

1. The LCD's bezel must be grounded. The heat sink is connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.
2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
3. The LCD shall be installed so that there is no pressure on the LSI chips.
4. The LCD shall be installed flat, without twisting or bending.
5. Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
7. Please refer to the following our recommendable value of Clamp-down torque when installing.
Clamp-down torque : $0.32 \pm 0.03 \text{ N} \cdot \text{m}$
Please set up 'SPEED-LOW,' 'SOFT START-SLOW' when using electric driver.
Recommendable screw P-TITE screw two types nominal dia.3.0 mm installing boss hole depth 4.2 mm Max
Please be careful not to use high torque which may damage LCD module in installation.
8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
9. Do not pull the LED lead wires and do not bend the root of the wires.
Housing should be designed to protect LED lead wires from external stress.
10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.
Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.
It may also change the characteristics of the liquid crystal.
This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage

1. The LCD shall be stored within normal temperature and humidity.
Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
2. Always store the LCD so that it is free from external pressure onto it.

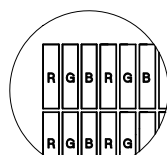
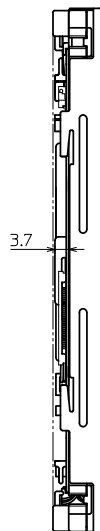
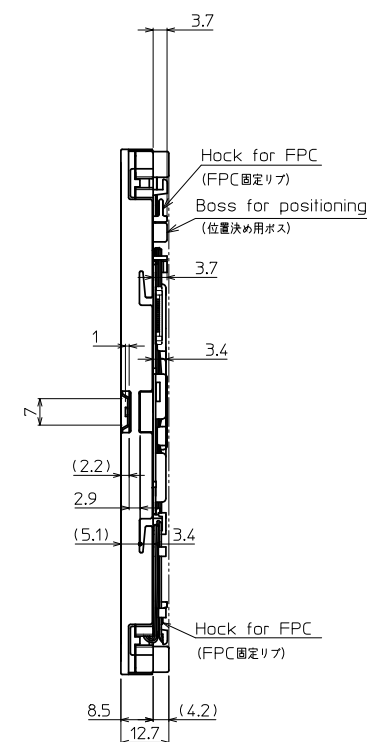
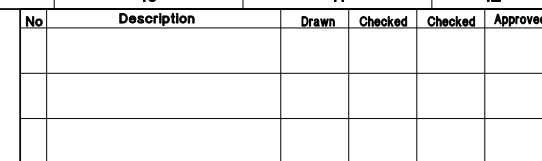
12-5. Screen Surface



1. DO NOT store in a high humidity environment for extended periods.
Image degradation, bubbles, and/or peeling off of polarizer may result.
2. The front polarizer is easily scratched or damaged.
Prevent touching it with any hard material, and from being pushed or rubbed.
3. The LCD screen may be cleaned with a soft cloth or cotton pad.
Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
4. Water may cause damage or discoloration of the polarizer.
Clean any condensation or moisture from any source immediately.
5. Always keep the LCD free from condensation during testing.
Condensation may permanently spot or stain the polarizers.
6. Do not disassemble LCD module because it will result in damage.
7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
8. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body, rinse it off right away with water and soap.

1 3. Reliability Data / Environmental Test

| TEST ITEM | TEST CONDITION | TEST TIME | RESULT |
|--------------------------------|---|-----------|--|
| High Temp. Atmosphere | 80°C | 240 h | Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| Low Temp. Atmosphere | -30°C | 240 h | Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| High Temp. Humidity Atmosphere | 40°C 90 %RH | 240 h | Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| Temp. Cycle | -30°C 0.5 h R.T. 0.5 h 80°C 0.5 h | 10 cycles | Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| High Temp. Operation | 70°C | 500 h | Display Quality : No defect Display Function : No defect Current Consumption : No defect |

- * Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- * The LCD is tested in circumstances in which there is no condensation.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only.
- * The reliability test is conducted only to examine the LCD's capability.



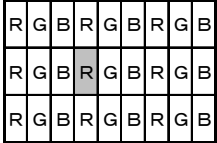
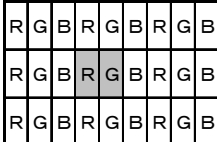
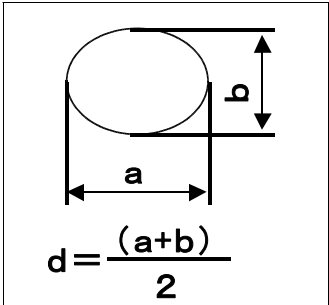
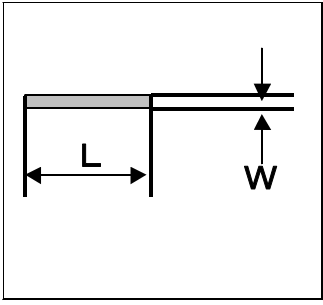
| | | | | | | | | | | |
|-----------------|---------------------|-----------------------|---------|----------------------|-------------|---|----------------------|---|-----------------------------|-----------|
| Material 材 質 | Treatment 処 理 | Approved '08.03.22 | Checked | Checked '08.03.22 | Drawn 野崎 | Scale 1:1 (NTS) | Title TCG075VGLAG |  | Year-Month-Day '08.03.18 | Size 2 |
| Quantity 製作数 | Description; 備 考 | RoHS 阿部 | | 倉元 | |  | Outline Dimensions | Drawing No. 121A5068100 | | 1/2 |

(EMI対策が必要になった場合を考慮してシールドカバーを取り付ける
クリアランスの確保をお願いします。)

Revision Record

| | | | | | |
|----------|--------------------------------|---------|--------------|------------------------|----------|
| Date | Designed by: Engineering Dept. | | | Confirmed by: QA Dept. | |
| | Prepared | Checked | Approved | Checked | Approved |
| | | | | | |
| Rev. No. | Date | Page | Descriptions | | |
| | | | | | |

1) Note

| | Note | | |
|-------------------------------|---|--|---|
| General | <p>1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</p> <p>2. Inspection Conditions Luminance : 500 Lux minimum Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 °C Direction : directly above</p> | | |
| Definition of Inspection item | Dot defect | Bright dot defect | <p>The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible though the filter. Don't count dot: If the dot is not visible through the filter.</p>  <p>dot defect</p> |
| | | Black dot defect | <p>The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.</p> |
| | | Adjacent dot | <p>Adjacent dot defect is defined as two or more bright dot defects or black dot defects.</p>  <p>dot defect</p> |
| | External inspection | Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight) | Visible operating (all pixels "Black" or "White") and non operating. |
| | | Appearance inspection | Does not satisfy the value at the spec. |
| | Others | CFL wires | Damaged to the CFL wires, connector, pin, functional failure, appearance failure. |
| Definition of size | Definition of circle size | | Definition of linear size |
| |  | |  |

2) Standard

| Classification | | Inspection item | | Judgement standard | | |
|--|---------------|--------------------------------------|-------------------|--|-------------------|-------------------|
| defect (in LCD glass) | Dot defect | Bright dot defect | | Acceptable number : 4 bright dots defects Bright dot spacing : 5 mm or more | | |
| | | Black dot defect | | Acceptable number : 5 black dots defects Black dot spacing : 5 mm or more | | |
| | | 2 dots join | Bright dot defect | Acceptable number : 2 | | |
| | | | Black dot defect | Acceptable number : 3 | | |
| | | 3 or more dots join | | Acceptable number : 0 | | |
| | | Total dot defects | | Acceptable number : 5 Max | | |
| | Others | White dot, Dark dot (Circle) | | | | |
| | | | | Size (mm) | Acceptable Number | |
| d<0.2 | | | | (neglected) | | |
| 0.2<d≤0.4 | | | | 5 | | |
| 0.4<d≤0.5 | | | | 3 | | |
| | | 0.5<d | 0 | | | |
| | | | | | | |
| External inspection (Defect on Polarizer or between Polariz- er and LCD glass) | | Polarizer (Scratches) | | | | |
| | | | | Width (mm) | Length (mm) | Acceptable Number |
| | | | | W≤0.1 | — | (neglected) |
| | | | | 0.1<W≤0.3 | L≤5.0 | (neglected) |
| | | | | | 5.0<L | 0 |
| | | 0.3<W | — | 0 | | |
| | | | | | | |
| | | Polarizer (Bubble, Dent) | | | | |
| | | | | Size (mm) | Acceptable Number | |
| | | | | d<0.2 | (neglected) | |
| | | | | 0.2<d≤0.3 | 5 | |
| | | | | 0.3<d≤0.5 | 3 | |
| | | | | 0.5<d | 0 | |
| | | | | | | |
| | | Foreign Particle (Circular shape) | | | | |
| | | | | Size (mm) | Acceptable Number | |
| | | | | d<0.2 | (neglected) | |
| | | | | 0.2<d≤0.4 | 5 | |
| | | | | 0.4<d≤0.5 | 3 | |
| | | | | 0.5<d | 0 | |
| | | | | | | |
| Foreign Particle (Linear shape), Scratches | | | | | | |
| | | Width (mm) | Length (mm) | Acceptable Number | | |
| | | W≤0.03 | — | (neglected) | | |
| | | 0.03<W≤0.1 | L≤2.0 | (neglected) | | |
| | | | 2.0<L≤4.0 | 3 | | |
| | | | 4.0<L | 0 | | |
| 0.1<W | — | (According to Circular shape) | | | | |