SPEC

Spec No.	TQ3C-8EAF0-E1DDH58-00
Date	March 2, 2009

### TYPE: TCG075VGLBB-G20

< 7.5inch VGA transmissive color TFT with LED backlight, constant current circuit for LED backlight and touch panel>

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Issued
Date: Mar.6,2009
KYDCERE
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
March 2, 2009	y Asano	H.Tokumer)	G Matrumoto	J. Sakaguchi	To . Int

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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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## Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
		Prepa	red	Checked	Approved	Checked	Approved
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Rev.No.	Date	Page			Description	ons	



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# 1. Application

This document defines the specification of TCG075VGLBB-G20. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with constant current circuit for LED Backlight)

Touch panel : Analog type, Non-Glare treatment

## 3. Mechanical specifications

#### 3-1. LCD

Item	Specification	Unit
Outline dimensions 1)	173(W)×133(H)×5.85(D)	mm
Active area	151.68(W)×113.76(H) (18.9cm/7.5 inch(Diagonal))	mm
Effective viewing area	153.7(W)×115.8(H)	mm
Dot format	640×(R,G,B)(W)×480(H)	dot
Dot pitch	0.079(W)×0.237(H)	mm
Base color 2)	Normally White	-
Mass	(TBD)	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

#### 3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	(0.05~0.8)	N
Transmittance	(Typ. 79)	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



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### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage for logic		$V_{\mathrm{DD}}$	0	4.0	V
Input signal voltage	1)	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight		$V_{\mathrm{IN}}\mathrm{B}$	0	6.0	V
Backlight ON-OFF		BLEN	0	$V_{\mathrm{IN}}\mathrm{B}$	V
Brightness adjust voltage		VBRT	0	$ m V_{IN}B$	V
Supply voltage for touch panel		$V_{\mathrm{TP}}$	0	6.0	V
Input current of touch panel		${ m I}_{ m TP}$	0	0.5	mA

1) Input signal: CK, R0~R5, G0~G5, B0~B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	$T_{STO}$	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	Нѕто	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. =  $-30^{\circ}$ C< 48h, Temp. =  $80^{\circ}$ C< 168hStore LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ≤ 40°C, 85%RH Max.

Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) **EIAJ ED-2531** 

6) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

EIAJ ED-2531



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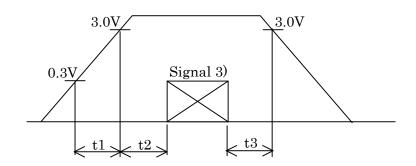
# 5. Electrical characteristics

#### 5-1. LCD

Temp. =  $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption	${ m I}_{ m DD}$	2)	-	(TBD)	(TBD)	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	-	-	100	mVp-p
Innut signal valtage 2)	$ m V_{IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage 3)	$V_{\mathrm{IH}}$	"High" level	$0.7 V_{\mathrm{DD}}$	-	$V_{ m DD}$	V mA mVp-p

### 1) V<sub>DD</sub>-turn-on conditions



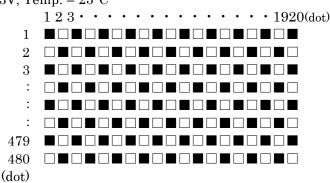
0<t1≦20ms

 $0 \le t2 \le 50 \text{ms}$ 

 $0 \le t3 \le 1s$ 

## 2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



# 3) Input signal : CK, R0 $\sim$ R5, G0 $\sim$ G5, B0 $\sim$ B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D

## 5-2. Touch panel

Item	Specification		
Supply voltage for touch panel	(5.0V)		
The control of the co	$xL\sim xR:(200\Omega\sim 1,000\Omega)$		
Terminal resistance	yU~yL: (200Ω~1,000Ω)		
Linearity	less than $\pm (2.0\%)$		
Insulation resistance	$(100 \mathrm{M}\Omega)$ or more at DC25V		



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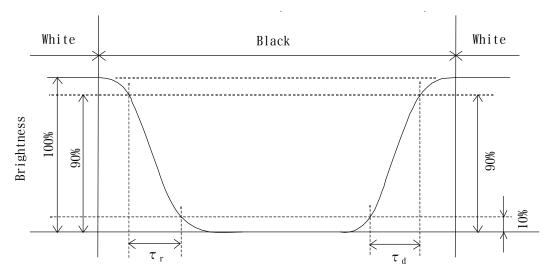
# 6. Optical characteristics

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	τг	$\theta = \phi = 0^{\circ}$	-	5	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	15	-	ms	
		heta upper		-	(50)	-	1	
Viewing angle View direction	range	heta lower	CR≧5	-	(70)	-	deg.	
	: 6 o'clock		CR≦5	-	(70)	-	1	
(Gray inversion)		φ right		-	(70)	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	(300)	(450)	-	-	
Brightness		L	IL=15mA/Line	(140)	(200)	-	cd/m²	
	Red	x	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)		
		У	$\theta = \psi = 0$	(TBD)	(TBD)	(TBD)		
	C	x	0 1 00	(TBD)	(TBD)	(TBD)		
Chromaticity		$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)			
coordinates	DI	x	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)	-	
	Blue	У	$\theta = \psi = 0^{\circ}$	(TBD)	(TBD)	(TBD)		
	<b>VX71</b> - '4 -	x	0 - h -0°	(TBD)	(TBD)	(TBD)		
	White	У	$\theta = \phi = 0^{\circ}$	(TBD)	(TBD)	(TBD)		

## 6-1. Definition of contrast ratio

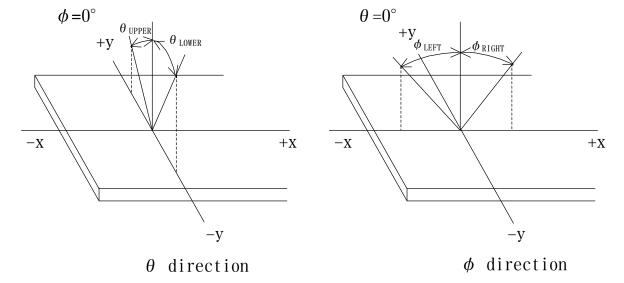
# 6-2. Definition of response time



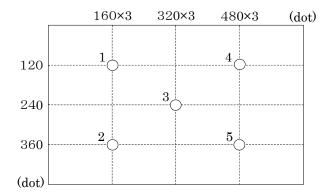


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# 6-3. Definition of viewing angle



## 6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area. (measured point  $1\sim5$ )
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



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# 7. Interface signals

## 7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	В5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	1)
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	2)
31	U/D	Vertical display mode select signal H: Normal, L: Up / Down reverse mode	I	2)
32	NC	No connect	-	
33	V <sub>IN</sub> B	Power supply for LED backlight	-	
34	V <sub>IN</sub> B	Power supply for LED backlight	-	
35	V <sub>IN</sub> B	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H:ON, L:OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight	-	
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

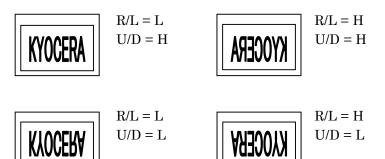
Recommended matching FFC or FPC : 0.5mm pitch



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The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
 In case ENAB is fixed "Low", the horizontal start timing is determined.
 Don't keep ENAB "High" during operation.

2)



# 7-2. Touch panel

No.	Symbol	Description
1	yU	y-Upper terminal
2	xL	x-Left terminal
3	уL	y-Lower terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch

Recommended matching connector : Series FE, FFS (JST)

: KCA-K4R (DMC)



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# 8. Input timing characteristics

### 8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Cll-	Frequency	1/Tc	_	25.18	28.33	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
D.	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10	_	_	ns	
	G 1	ТН	30.0	31.8	_	μs	
Horizontal sync. signal	Cycle		770	800	900	clock	
Signai	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	ny period	THd	640		clock		
Hsync,-Clock pha	ase difference	ТНс	10	_	Tc-10	ns	
Hsync-Vsync. phase difference		TVh	Тс	_	ТН-ТНр	ns	
Vertical sync. sig	TVs		34		line		
Vertical display p	period	TVd		480		line	

1) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

## 8-2. Horizontal display position

Item		Symbol	Min	Typ	Max	Unit	Note
Enable sime	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Tep	2	640	TH-10	clock	
H <sub>SYNC</sub> – Enable signal phase difference		The	44		TH-664	clock	

- 1) When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.
- 2) The horizontal display position is determined by ENAB signal.

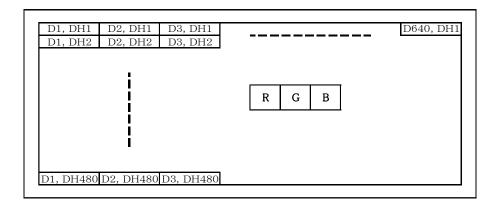
## 8-3. Vertical display position

- 1) The vertical display position (TVs) is 34th line.
- 2) ENAB signal is independent of vertical display position.

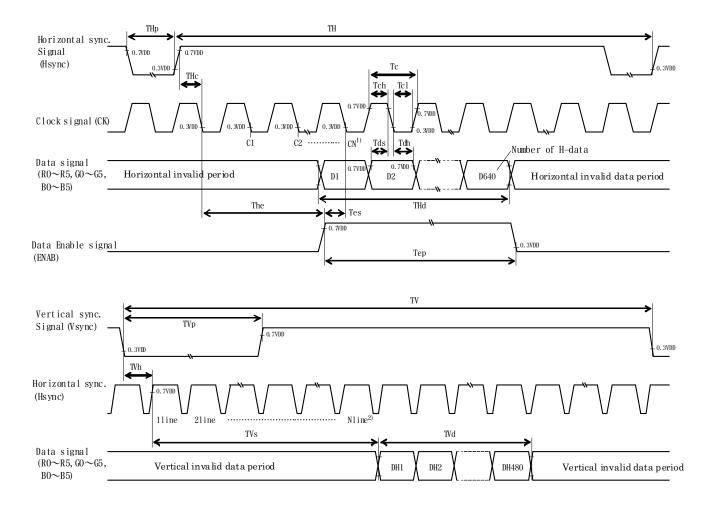


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## 8-4. Input Data Signals and Display position on the screen



## 8-5. Input timing characteristics



- 1) When ENAB is fixed at "Low", the display starts from the data of C104(Clock).
- 2) The vertical display position(TVs) is fixed at 34th line.



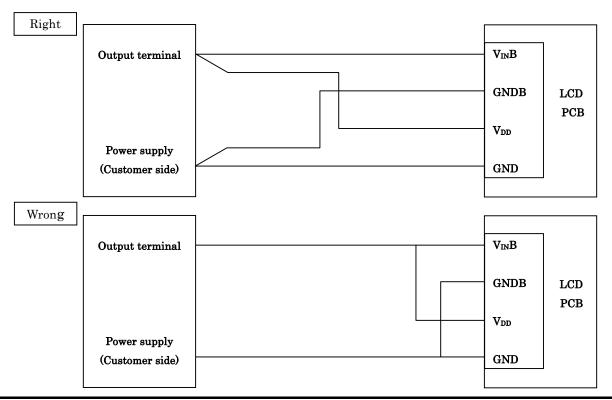
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## 9. Backlight characteristics

Temp.=25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	-	5.5	V	Ta=-20∼70°C
ON-OFF (H)	BLEN	$0.8 V_{\rm IN} B$	-	$V_{\rm IN}B$	V	Ta=-20∼70°C
ON-OFF (L)	BLEN	0.0	-	$0.2 V_{\rm IN} B$	V	Ta=-20∼70°C
LED forward current	IF	14	15	16	A	VBRT=0∼1.4V
1) 2)	1F	2.8	3.0	3.2	mA	VBRT=2.8V
C1	I D	-	(TBD)	(TBD)	Λ	V <sub>IN</sub> B =3.5V, IF=15mA
Supply current	I <sub>IN</sub> B	-	(TBD)	(TBD)	mA	V <sub>IN</sub> B =5.0V, IF=15mA
Operating life 3) 4)	Т	-	(TBD)	-	h	IF=15mA, Ta=25°C

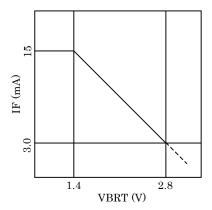
- 1) For each LED.
- 2) A forward current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 3) When brightness decrease 50% of initial brightness.
- 4) Life time is estimated data. (Condition: IF=15mA, Ta=25°C in chamber).
- 5) When you start-up, please charge in sequence of  $V_{IN}B$ ->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT-> $V_{IN}B$ .
- 6) Please do not connect the other than our backlight to this output connector on the PCB.
- 7) In case VDD and V<sub>IN</sub>B are supplied by a single power source, VDD & V<sub>IN</sub>B, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & V<sub>IN</sub>B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





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## 8) 5 VBRT-IF characteristics



## 10. Design guidance for analog touch panel

- 10-1. Electrical (In customer's design, please remember the following considerations.)
  - 1) Do not use the current regulated circuit.
  - Keep the current limit with top and bottom layer.
     (Please refer to "Electrical absolute maximum ratings" for details.)
  - 3) Analog touch panel can not sense two points touching separately.
  - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
  - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

#### 10-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

#### 10-3. Mounting on display and housing bezel

- 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
- 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short.
- 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



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#### 11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2009	2010	2011	2012	2013	2014
Code	9	0	1	2	3	4

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

### 12. Warranty

#### 12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

## 12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 13. Precautions for use

#### 13-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
  - Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

## 13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 13-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
   Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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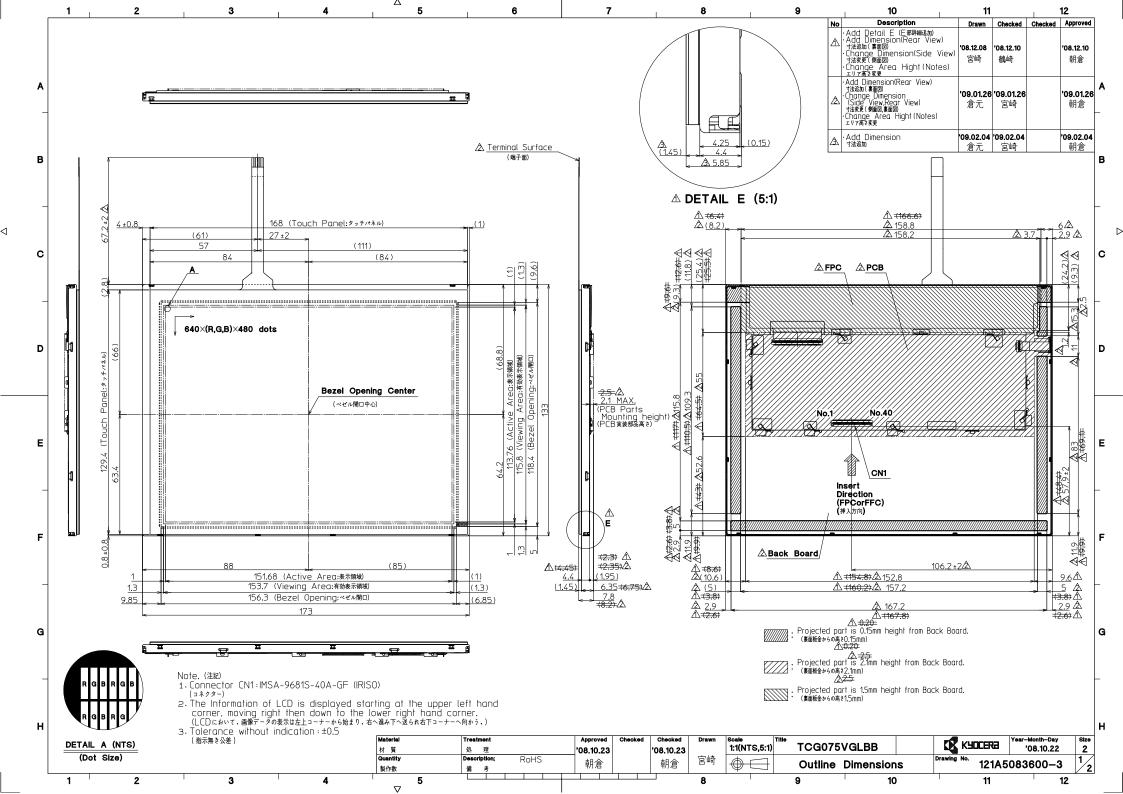
# 14. Reliability test data

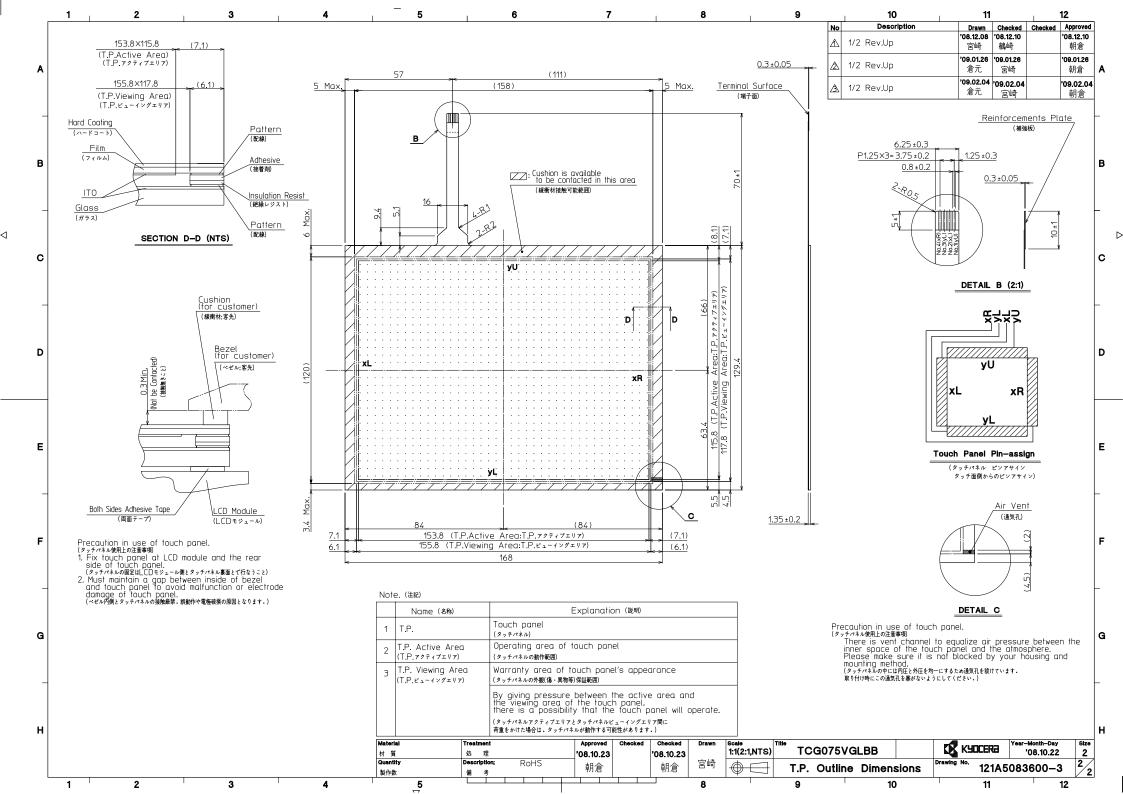
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
High temp. operation	70°C	500h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
Point Activation life	Polyacetal stylus (R0.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.







Spec No	TQ3C-8EAF0-E2DDH61-00
Date	March 2, 2009

# KYOCERA INSPECTION STANDARD

TYPE: TCG075VGLBB-G20

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
March 2, 2009	y deano	H-Tokumuri	G. Matriemoto	J. Sakaguchi	H. Jul



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# Revision record

Data		Designed by : Engineering dept.		Confirmed by : QA dept.			
	Date	Prepa	red	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	



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# Visuals specification

#### 1) Note

1) Note	T		N				
C 1	1 0 :	1	Note				
General	<ol> <li>Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</li> </ol>						
		pection standard about the image quality shall be applied to any defect within					
	the effective viewing area and shall not be applicable to outside of the area.						
	3. Inspection conditions  Luminance : 500 Lux min.						
		ion distance	300 mm.				
	Temper		: 25 ± 5°C				
	Direction		: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection item			LCD, even when all "Black" data sent to the screen.				
			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.  RGBRGBRGB  RGBRGBRGB  dot defect				
		Black dot defect	The dot is constantly "off" when power applied to the				
			LCD, even when all "White" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.				
			R G B R G B R G B R G B R G B R G B R G B R G B R G B				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle (Polarizer, Cell, Backlight)	operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Others	CFL wire	Damaged to the CFL wires connector, pin, functional failure or appearance failure.				
	Definition of size	Definition of					
	or size	d = (a + b)/2					



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### 2) Standard

2) Standa		I _					_	
	Classification Inspection item		Judgement standard					
Defect	Dot	Bright dot	defect	Acceptable number : 4				
(in LCD	defect					or more		
glass)		Black dot defect		Acceptable number : 5				
				Bright dot spacing : 5 mm or more				
		2 dot join	2 dot join Bright dot Acceptable number : 2					
			defect	Acceptable number		• 4		
			Black dot defect	Acceptable number		: 3		
		3 or more		Acceptable number		: 0		
		Total dot d	-	Acceptable number		: 5 Max	Ÿ	
	Others	White dot,		Acceptable number		· 5 Maz	Δ.	
	Others	(Circle)	Dark uot	Size (mm		Α.		
		(Circie)		Size (mm d ≤		Acc	ceptable number (Neglected)	
				0.2 < d ≤			5	
				0.4 < d ≤			3	
				0.5 < d	0.0		0	
						I.		
	inspection	Polarizer (	Scratch)					
(Defect on				Width (mm)	Length (mm)		Acceptable number	
Polarizer				$W \leq 0.1$			(Neglected)	
between I	Polarizer			$0.1 < W \le 0.3$		≦ 5.0	(Neglected)	
and LCD	glass)			0.0 < W	5.0 < L		0	
				0.3 < W			0	
		Polarizer (	Bubble)					
				Size (mm)		Acc	ceptable number	
				d ≦			(Neglected)	
				0.2 < d ≦			5	
				0.3 < d ≤	0.5		3	
				0.5 < d			0	
		Foreign pa	ırticle					
		(Circular	shape)	Size (mm)		Acc	Acceptable number	
				d ≤ 0.2		(Neglected)		
				0.2 < d ≦			5	
				$0.4 < d \le 0.5$		3		
				0.5 < d			0	
	Foreign particle							
(Li <sub>1</sub>		(Linear s	hape)	Width (mm)	Length	(mm)	Acceptable number	
		Scratch		$W \le 0.03$			(Neglected)	
					L	$\leq 2.0$	(Neglected)	
				$0.03 < W \le 0.1$	$2.0 < L \le 4.0$		3	
					4.0 < L		0	
				0.1 < W	_		(According to	
							circular shape)	



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Inspection item	Judgement standard							
Scratch,	( W = Width, L = Length, D = Diameter = (major axis+minor axis)/ 2)							
Foreign particle	Item	Width(mm) Length(mm) Acc			ceptable number			
(Touch screen		d ≤ 0.03	$L \le 20$		Neglected			
portion)		$0.03 < d \le 0.05$	L ≤ 10	2рсе	es within φ20mm			
	Scratch	$0.05 < d \le 0.08$	$L \le 6$		es within φ20mm			
		$0.08 < d \le 0.1$	$L \le 4$	<del></del>	es within φ30mm			
	Foreign	$W \le 0.05$	Neglected	-	Neglected			
	(line like)	$0.05 < W \le 0.1$	$L \le 5$	2pce	es within $\phi$ 30mm			
	Foreign	D ≤	0.2		Neglected			
	(circle like)	0.2 < D ≤	0.3	2pce	es within $\phi$ 30mm			
	Above are applie	d to the visible area.						
	Unless there are	re foreign particle and o	damage affected	seriou	usly to the electrical			
	performance out	of the active area, we appro	ove of this produc	et.				
Glass crack					Acceptable			
(Touch screen	Item	Size (m	nm)		number			
portion)			_   X	<b>≦</b> 3				
			z ^ ^	=0				
	Conner crack	×× × 1/2	Y	<b>≦</b> 3	2 pcs			
					/panel			
				<t				
		\/v	. X	<b>≦</b> 5				
	Crack in	× × × × × × × × × × × × × × × × × × ×	· · ·	_0				
	other area		Y	$\leq 1.5$	2 pcs			
	than in corner				/side			
	corner	3		$\leq$ t				
				10				
			//					
	Progressive	_	//		0 pcs			
	crack		X/		(NG even 1pcs)			
	Crack	$\overline{}$	(IVG even Ipes)					
		$\longrightarrow$						
		d to the visible area.						
		re foreign particle and			asly to the electrical			
	performance out	of the active area, we appro	ove of this produc	t.				
Newton's ring	Neglected.							
			_					
			N N	ewton'	's ring			
				C W LOII	5 1111 <u>8</u>			

