SPEC

Spec No.	TQ3C-8EAF0-E1DKH06-00
Date	December 22, 2007

### TYPE: THG057VGLAH-H000

< 5.7 inch VGA transflective color TFT with LED backlight>

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Issued
Date: Dec.28,2007
KUDERRA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
December 22, 2007	y Asano	Id Topumore)	G. Matsumoto	J. Sakaguchi	To Sal

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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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# Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
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# 1. Application

This document defines the specification of THG057VGLAH-H000. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transflective color dot matrix type TFT

Backlight system : LED

Polarizer : Glare Anti-reflection treatment

Additional circuit : Timing controller, Power supply (3.3V input)

With Constant current circuit for LED Backlight

#### 3. Mechanical specifications

Item	Specification	
Outline dimensions	135 (W)× (104.8) (H) × 8.85 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Effective viewing area	117.2 (W) × 88.4 (H)	mm
Dot format	640×(R,G,B) (W) × 480 (H)	dot
Dot pitch	0.06 (W) × 0.18 (H)	mm
Base color *1	Normally White	-
Mass	160	g

<sup>\*1</sup> Due to the characteristics of the LCD material, the color varies with environmental temperature.



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#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	VinB	0	6.0	V
Backlight ON-OFF	BLEN	0	VinB	V
Brightness adjust voltage	VBRT	0	VinB	V

<sup>\*1</sup> Input signal: CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	$T_{\mathrm{STO}}$	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

<sup>\*1</sup> Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

\*6 Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

EIAJ ED-2531



<sup>\*2</sup> Temp. = -30°C< 48h , Temp. = 80°C< 128h

<sup>\*3</sup> Non-condensing

<sup>\*4</sup> Temp.≤40°C, 85%RH Max.

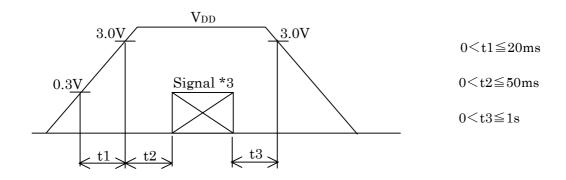
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# 5. Electrical characteristics

Temp. =  $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	$ m V_{DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	*2	-	(TBD)	(TBD)	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	-	-	100	mVp-p
I	$ m V_{IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage *3	$V_{\mathrm{IH}}$	"High" level	$0.7V_{\mathrm{DD}}$	-	$V_{\mathrm{DD}}$	V

#### \*1 $V_{\rm DD}$ -turn-on conditions



\*2 Display pattern:

\*3 Input signal : CK, R0~R5, G0~G5, B0~B5, H\_SYNC, V\_SYNC, ENAB, R/L, U/D



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# 6. Optical characteristics

# 6-1. Reflective mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	Τr	$\theta = \phi = 0^{\circ}$	-	10	-	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	20	-	ms
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	10	20	-	-
Reflectance		ρ	$\theta = \phi = 0$ °	7	10	-	%

# 6-2. Transmissive mode

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
Dagaran an tima	Rise	τr	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	20	-	ms	
		heta upper		-	45	-		
Vierring on ale		$ heta_{ m LOWER}$	CR≧10	-	80	-	deg.	
Viewing angle range		$\phi$ left	CR≦10	-	80	-	J	
		φ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	100	150	-	-	
Brightness		L	IF=15mA/Line	(160)	(200)	-	cd/m²	
	Red	X	$\theta = \phi = 0^{\circ}$	(0.51)	(0.56)	(0.61)		
	Rea	У	$\theta = \psi = 0$	(0.31)	(0.36)	(0.41)		
	Green	X	$\theta = \phi = 0^{\circ}$	(0.31)	(0.36)	(0.41)		
Chromaticity	Green	У	$\theta = \psi = 0$	(0.49)	(0.54)	(0.59)		
coordinates	D1	X	$\theta = \phi = 0^{\circ}$	(0.11)	(0.16)	(0.21)	-	
	Blue	У	$\theta = \psi = 0$	(0.12)	(0.17)	(0.22)		
	White	X	$\theta = \phi = 0^{\circ}$	(0.27)	(0.32)	(0.37)		
	willte	White y	$\theta = \psi = 0$	(0.31)	(0.36)	(0.41)		



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#### 6-3. Definition of Reflectance

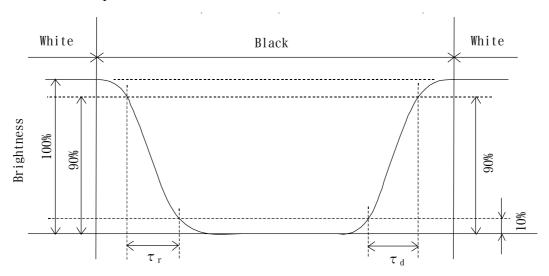
$$\rho \, (\text{Reflectance}) \, = \, \frac{\text{Measured Reflection Brightness}}{\text{Reflection Brightness against Standard White Board}} \, \times 100(\%)$$

# 6-4. Definition of Contrast(Reflective Mode)

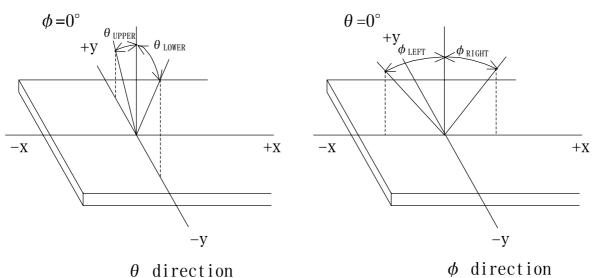
$$CR(Contrast) = \frac{Reflectance at all pixels "White"}{Reflectance at all pixels "Black"}$$

#### 6-5. Definition of Contrast(Transmissive mode)

#### 6-6. Definition of response time



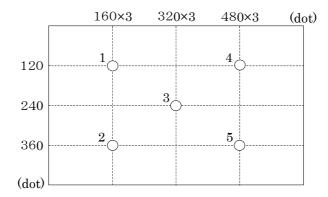
# 6-7. Definition of viewing angle





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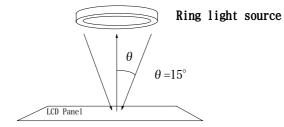
# 6-8. Brightness measuring points



- 1) Rating is defined as the white luminance(brightness) at center of display screen(3).
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)

#### 6-9. Measurement method of reflectance







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# 7. Interface signals

# 7-1. Pin assignment of LCD panel

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	$H_{\mathrm{SYNC}}$	Horizontal synchronous signal (negative)		
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	*2
32	NC	No connect	-	
33	GND	GND	-	

LCD connector(CN1) : 08-6210-033-340-800+ (ELCO)

Recommended matching FFC or FPC : 0.5mm pitch



<sup>\*1</sup> The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

\*2



R/L = LU/D = H



R/L = HU/D = H



R/L = LU/D = L



R/L = HU/D = L

# 7-2. Pin assignment of LED

No.	Symbol	Description
1	VinB	
2	VinB	
3	VinB	
4	VinB	
5	VinB	Supply voltage
6	VinB	
7	VinB	
8	VinB	
9	BLEN	Backlight ON-OFF(H:ON, L:OFF)
10	VBRT	Brightness adjust voltage
11	GND	
12	GND	
13	GND	
14	GND	GND
15	GND	GND
16	GND	
17	GND	
18	GND	

LCD connector : 08-6210-018-340-800+ (ELCO)

Recommended matching FFC or FPC : 0.5mm pitch



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# 8. Input timing characteristics

#### 8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
C1 1	Frequency	1/Tc	_	25.18	28.33	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10	_	_	ns	
	Cycle	TH	30.0	31.8	_	$\mu  \mathrm{s}$	
Horizontal sync. signal	Cycle	I I II	770	800	900	clock	
Signar	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	ny period	THd	640			clock	
Hsync,-Clock phase difference		ТНс	10	_	Tc-10	ns	
Hsync-Vsync. phase difference		TVh	0	_	ТН-ТНр	ns	
Vertical sync. signal start position		TVs	34		line		
Vertical display	period	TVd	480		line		

<sup>\*</sup>In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

# 8-2. Horizontal display position

The horizontal display position is determined by ENAB signal.

Item		Symbol	Min	Тур	Max	Unit	Note
Enable signal	Set up time	Tes	5	_	Tc-10	ns	
	Pulse width	Tep	2	640	TH-10	clock	
H <sub>SYNC</sub> – Enable signal phase difference		The	44	_	104	clock	

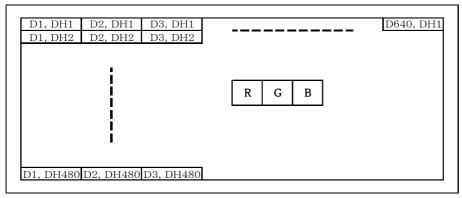
<sup>\*</sup> When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

#### 8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line.

Note) ENAB signal is independent of vertical display position.

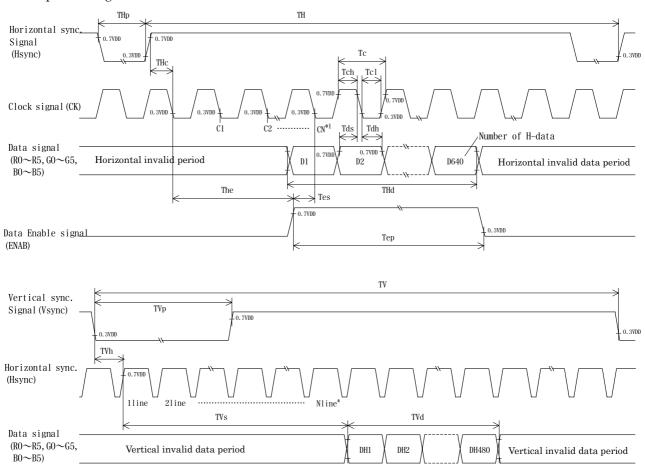
# 8-4. Input Data Signals and Display position on the screen





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# 8-5. Input timing characteristics



<sup>\*1</sup> When ENAB is fixed "Low" ,the display starts from the data of C104(Clock).



<sup>\*2</sup> The vertical display position(TVs) is fixed at 34th line.

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### 9. Backlight characteristics

#### 9-1. CFL ratings

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	VinB	3.0		5.5	V	-20∼70°C
ON-OFF(H)	BLEN	0.8VinB	-	VinB	V	-20∼70°C
ON-OFF(L)	BBBI	0.0	_	0.2VinB	,	-20∼70°C
LED forward current	112	_	(15.0)	_	A	VBRT=0∼0.8V *1
*1*2	IF	_	(2.5)	_	mA	VBRT=2.8V *1*2
C 1	T. D	_	(600)	(750)	Α.	VinB=3.3V, IF=15mA *1
Supply current	IinB	_	(400)	(500)	mA	VinB=5.0V, IF=15mA *1
Operation life time *3*4	Т	_	(20,000)	_	h	IF=15mA *1

<sup>\*1</sup> For each "AN1-CA1", "AN2-CA2", "AN3-CA3" and "AN4-CA4"

- \* When you start-up, please charge in sequence of VinB->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->VinB.
- \* Please do not connect the other than our backlight to this output connector on the PCB.
- \* In case VDD and VinB are supplied by a single power source, VDD & VinB, and GND are connected directly and separately from the output on the power source. If the common wire are used for VDD & VinB, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.

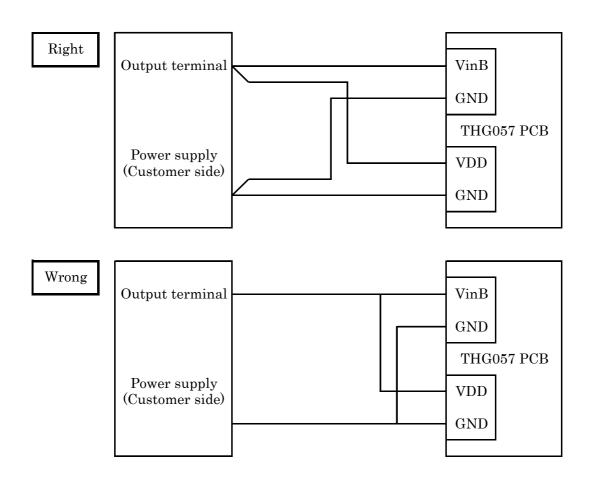


<sup>\*2</sup> An input current below 5.0mA may reduce the brightness uniformity of the LED backlight.

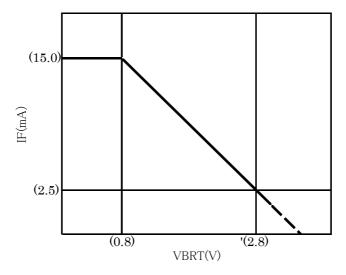
This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

<sup>\*3</sup> When brightness decrease 50% of initial brightness.

<sup>\*4</sup> Life time is estimated data. (Condition: IF=15mA, Ta=25°C in chamber)



#### 9-2. VBRT-IF characteristics



VBRT-IF characteristics (reference) Ta= $25^{\circ}$ C



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#### 10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Code         7         8         9         0         1         2	Year	2007	2008	2009	2010	2011	2012
	Code	7	8	9	0	1	2

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 11. Warranty

# 11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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#### 12. Precautions for use

#### 12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
- 5) Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque: 0.32±0.03N·m

Please set up 'SPEED-LOW', 'SOFT START-SLOW' when using electric driver. Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5mm Max.

Please be careful not to use high torque which may damage LCD module in installation.

6) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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# 13. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>

<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

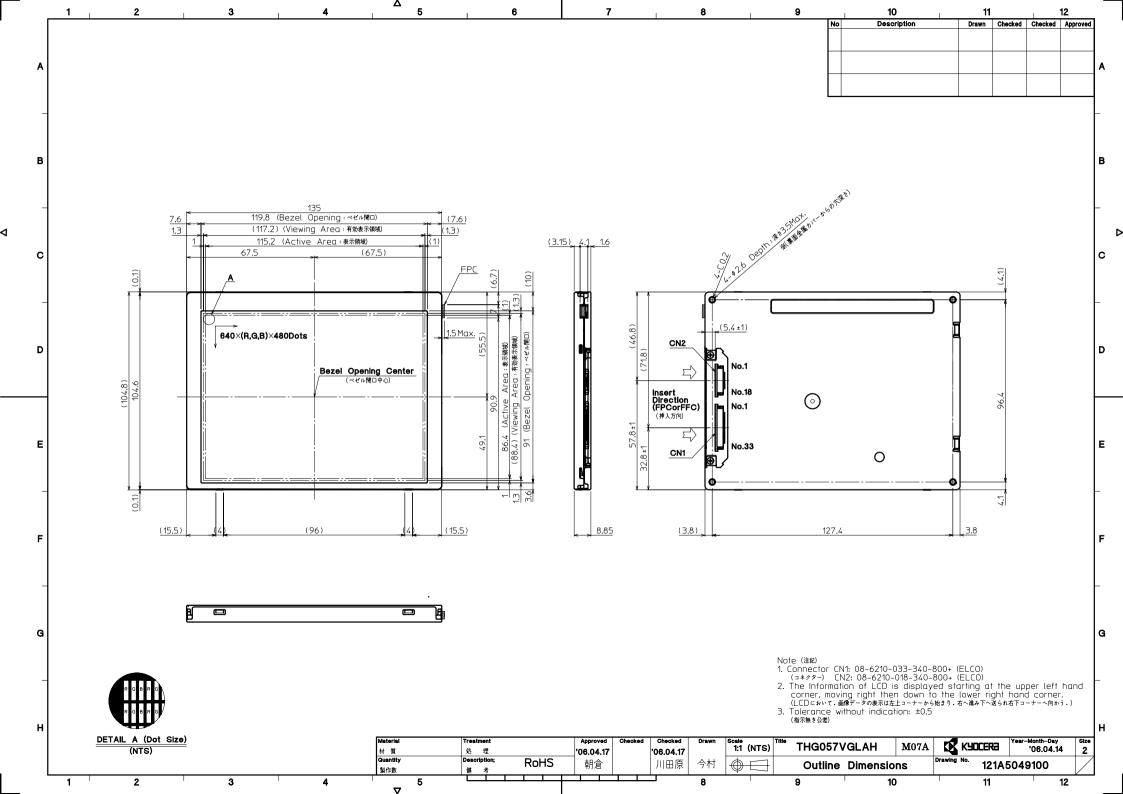


<sup>\*</sup> The LCD is tested in circumstances in which there is no condensation.

<sup>\*</sup> The reliability test is not an out-going inspection.

<sup>\*</sup> The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.



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Date	December 22, 2007

# **KYOCERA INSPECTION STANDARD**

 ${\bf \underline{TYPE:THG057VGLAH-H000}}$ 

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
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# Visuals specification 1) Note

1) Note	Т		N			
~ .			Note			
General	reviewe consent 2. This ins	ther identified anomalies not defined within this inspection standard shall be add by Kyocera, and an additional standard shall be determined by mutual standard shall be determined by mutual spection standard about the image quality shall be applied to any defect within				
	the viewing area and shall not be applicable to outside of the area.					
	3. Inspecti	on conditions	: 500 Lux min.			
		ion distance	: 300 mm.			
	Temper		: 25 ± 5°C : Directly above			
	Direction	on				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the			
inspection item			LCD, even when all "Black" data sent to the screen.			
			Inspection tool: 5% Transparency neutral density filter.			
			Count dot: If the dot is visible through the filter			
			Don't count dot: If the dot is not visible through the			
			filter  RGBRGBRGB  RGBRGBRGB  dot defect			
		Black dot defect	The dot is constantly "off" when power applied to the			
			LCD, even when all "White" data sent to the screen.			
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot			
			defects or black dot defects.			
			R G B R G B R G B R G B R G B R G B R G B R G B R G B			
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non			
	inspection	Foreign particle (Polarizer, Cell, Backlight)	operating.			
		Appearance	Does not satisfy the value at the spec.			
		inspection				
	Others	CFL wire	Damaged to the CFL wire, connector, pin, functional			
			failure or appearance failure.			
	Definition	Definition of	circle size Definition of linear size			
	of size	d = (a -	+ b)/2			



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#### 2) Standard

2) Standard				7.1				
Classification Inspection item		Judgement standard						
Defect			Acceptable number : 4					
(in LCD   defect					<u> </u>		n or more	
glass)		Black dot defect		Acceptable number		: 5	5	
				Bright dot spacing	Bright dot spacing : 5 mm		or more	
	2 dot join Bright do defect		Bright dot defect	Acceptable number : 2				
			Black dot defect	Acceptable number		: 3		
		3 or more	dots join	Acceptable number		: 0		
		Total dot d	-	Acceptable number		: 5 Max		
	Others	White dot,		respective				
	Others	(Circle)	Dark dot	Size (mm	,)	Δα	ceptable number	
		(Officie)		d ≤		Acc	(Neglected)	
				$0.2 < d \le 0.4$		(ivegrected)		
				0.4 < d ≤			3	
				0.5 < d			0	
		<b>D</b> 1 /	· · · · · ·					
	inspection	Polarizer (Scratch)		( )		<u>, I</u>		
(Defect on				Width (mm)	Length (	mm)	Acceptable number	
Polarizer				$W \leq 0.1$			(Neglected)	
	between Polarizer			$0.1 < W \le 0.3$	$\frac{L}{5.0 < L}$	≦ 5.0	(Neglected)	
and LCD glass)				0.3 < W	0.0 \ L		0	
				0.0 < 11			0	
		Polarizer (	Bubble)			1		
				Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \le 0.3$		5		
				0.3 < d ≤	0.5		3	
				0.5 < d		]	0	
		Foreign pa				1		
		(Circle shape)		Size (mm)		Acceptable number		
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
1				$0.4 < d \le 0.5$		3		
				0.5 < d			0	
1		Foreign particle						
		(Linear shape)		Width (mm)	Length (mm)		Acceptable number	
Se		Scratch		$W \leq 0.03$			(Neglected)	
						$\leq 2.0$	(Neglected)	
				$0.03 < W \le 0.1$	$2.0 < L \le 4.0$		3	
					4.0 < L		0	
				0.1 < W	_		(According to	
							circular shape)	

