SPEC. NO.	TQ3C-8EAFO-E1DKB14-00				
DATE	September 7, 2007				

SPEC

FOR	•	
LOW	•	

TYPE: THG062HVLAJ-G00

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KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by	:Engineering	Confirmed by :QA Dept.		
Issue Date	Prepared	Prepared Checked		Checked	Approved
September 7, 2007	7. Crodera	y Yamazaki	J Matxemoto.	J. Sakaguchi	To last

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date		Design	ed by:	Engineering Dept. Confirmed by: QA Dept.		QA Dept.	
рате	-	Prepared		Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ons	

1. Application

This data sheet defines the specification for a $(640 \times R.G.B) \times 240$ dot, amorphous silicon TFT transflective color dot matrix type Liquid Crystal Display with LED backlight. [RoHS Compliant]

2. Construction and Outline

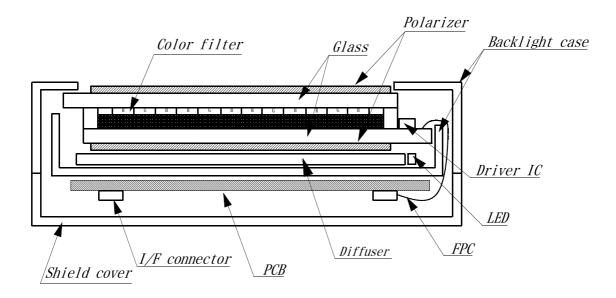
 $(640 \times R.G.B) \times 240$ dots, COG type LCD with LED backlight.

Backlight system : Side-edge type (LED).

Polarizer : Glare treatment.

Additional circuits: Timing controller, Power supply (3.3V input)

Touch panel : Analog type. Non-Grare treatment.



This drawing is showing conception only.

3. Mechanical Specifications

3-1. Mechanical specification of LCD panel

ITEM	SPECIFICATION	UNIT
Outline dimensions	174.2 (W) \times 73.4 (H) \times 14.0 (D)	mm
Effective viewing area	149.8 (W) × 57.4 (H)	mm
Dot number	(640×R.G.B) (W) × 240 (H)	Dots
Dot pitch	$0.077~(W)~\times~0.231~(H)$	mm
Display mode *1	Normally white	_
Mass	(200)	g

^{*1} Due to the characteristics of the LCD material, the color vary with environmental temperature.

3-2. Mechanical Specifications of touch panel

ITEM	SPECIFICATION	UNIT
Input	Radius-0.8 stylus or Finger	_
Actuation Force	0.5 N ± 0.3 N	_
Transmittance	Тур. 80	%
Surface hardness	pencil hardness 2H or more according	_

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	0	4.0	V
Input signal voltage :	1 Vin	-0.3	6.0	V
Forward current	2 IF	_	(17)	mA
Reversed voltage :	2 VR	_	(5)	V
Touch panel supply voltage	Vtp	0	6.0	V
Touch panel Input current	Itp	0	0.5	mA

CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q "AN1-CA1", "AN2-CA2", "AN3-CA3", "AN4-CA4". *1 Input signals :

*2 For each

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature	*1	Тор	(-20)	(70)	°C
Storage temperature	*2	Tsto	(-30)	(80)	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		_	*5	*5	_
Shock		_	*6	*6	_

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Temp. = (-30) °C < 24 h , Temp = (80) °C < 24 h

Store LCD panel at normal temperature/humidity.

Keep it free from vibration and shock.

LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.

(Please refers to 12. Precautions for use as detail).

Non-condensation.

Temp. $\leq 40^{\circ}\text{C}$, 85%RH Max.

Temp. $> 40^{\circ}\text{C}$, Absolute Humidity shall be less than 85% RH at 40°C.

*5

Frequency	10∼55 Hz	Converted to acceleration value:		
Vibration width	0.15 mm	acceleration value: $(0.3 \sim 9 \text{ m/s}^2)$		
Interval	10-55-10 Hz	1 minute		

2 hours in each direction X/Y/Z (6 hours as total) EIAJ ED-2531

 $\begin{array}{ccc} *6 & Acceleration: & 490 \text{m/s}^2 \\ & Pulse \ width: & 11 \ \text{ms} \end{array}$

3 times in each direction : $\pm X/\pm Y/\pm Z$.

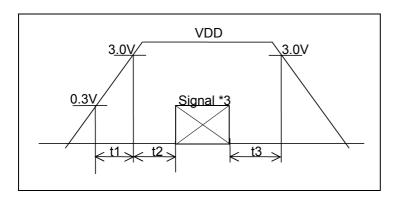
EIAJ ED-2531

5. Electrical Characteristics

 $VDD = +3.3V \pm 0.3V$, Temp. = (TBD)

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	
Power input voltage *1	VDD=3.3V	VDD	3.0	3.3	3.6	V
Current consumption *2	۷س=3.3۷	IDD	_	(230)	(300)	mA
Permissive input ripple v	VRP	_	_	(100)	mVp-p	
Input signal voltage (Low) *3		VIL	0	_	0.3VDD	V
Input signal voltage (H	igh) *3	VIH	0.7VDD	_	+5.5	V

*1 VDD-turn-on conditions



 $0 < t \ 1 \le 2 \ 0 \, \text{ms}$

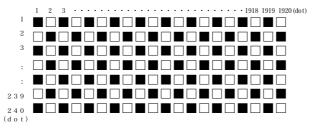
 $0 < t \ 2 \le 5 \ 0 \, \text{ms}$

 $0 < t \ 3 \le 1 s$

*2 Power consumption

Pattern:

VDD = 3.3V, V/Q=H



*3 Input signals : CK, RO~R5, GO~G5, BO~B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

5–2. Touch Panel

5-2-1. Terminal resistance

Between xL and xR : 200 \sim 1000 Ω Between yU and yL : 200 \sim 1000 Ω

5-2-2. Linearity

 $\pm 1.5\%$ x: 1.5% or less

y: 1.5% or less

5-2-3. Insulation resistance $100\text{M}\Omega$ or more at DC25V

6. Optical Characteristics

6-1. Reflective mode

Measuring points = ϕ 6.0mm , Temp. = 25°C

ITEM	ITEM S		CONDITION	MIN	TYP	MAX	UNIT
Response	Rise	τr	$\theta = \phi = 0^{\circ}$		(10)	_	ms
time Down τd		$\theta = \phi = 0^{\circ}$	_	(10)	_	ms	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	(20)	(35)	_	_
Refrectance		ρ	$\theta = \phi = 0^{\circ}$	(15)	(25)	_	%

6-2. Transmissive mode

ITEM		SYMBO L	COND	ITION	MIN	TYP	MAX	UNIT
Response Rise		τr	$\theta = q$	<i>b</i> =0°	_	(10)	_	ms
time	Down	τd	$\theta = q$	<i>p</i> =0°	_	(10)	_	ms
				Upper	_	(40)	_	dog
Viewing engle	Manaa	θ	CD > F	Lower	_	(40)	_	deg.
Viewing angle	range	φ	CR≧ 5	Left	_	(60)	_	dog
				Right	_	(70)	_	deg.
Contrast rati	Contrast ratio		$\theta = \phi = 0^{\circ}$		(25)	(40)	_	_
Brightness		L	IF=15mA/	1LED line	(40)	(60)	_	cd/m²
	Red	Х	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	(TBD)	
	kea	У			(TBD)	(TBD)	(TBD)	
	Green	Х	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	(TBD)	
Chromatiaity	Green	у			(TBD)	(TBD)	(TBD)	
Chromaticity coordinates	Blue	Х	0 _ 4	V −U°	(TBD)	(TBD)	(TBD)	_
	Diue	у	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	(TBD)	
	White	X	0	γ −0°	(TBD)	(TBD)	(TBD)	
	WIII LE	$\theta = \phi = 0^{\circ}$		v –U	(TBD)	(TBD)	(TBD)	

6-3. Definition of Reflectance

$$\rho \; (\text{Reflectance}) \; = \; \frac{\text{Measured Reflection Brightness}}{\text{Reflection Brightness against Standard White Board}} \; \times \; 100 \; (\%)$$

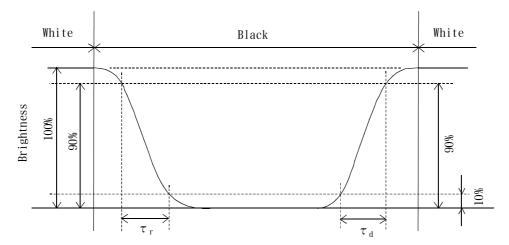
6-4. Definition of Contrast (Reflective Mode)

$$CR (Contrast) = \frac{Reflectance at all pixels "White"}{Reflectance at all pixels "Black"}$$

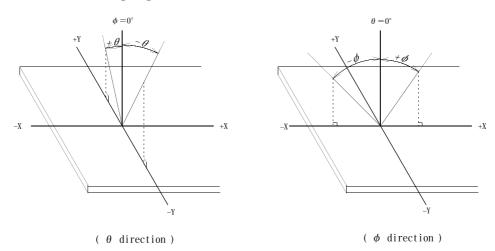
6-5. Definition of Contrast (Transmissive Mode)

$$CR (Contrast) = \frac{Brightness at all pixels "White"}{Brightness at all pixels "Black"}$$

6-6. Definition of Response time



6-7. Definition of viewing angle



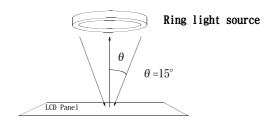
6-8. Measuring points

	160×3	320×3	480×3	(dot)
	1	 	4	
60	<u> </u>	1	<u> </u>	
120	i ! !	3	i i	
120	2	:	5	
180	<u> </u>		<u> </u>	
(dot)	 	 	; ; ;	

- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after LED is turned on. (Ambient Temp. =25°C)
- 3) Backlight : IF = 15mA / 1 LED line

6-9. Measurement method of reflecttance





7. Interface signals 7-1. LCD

PIN NO.	SYMBOL	DESCRIPTION	1/0	Note
1	GND	GND		
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	Vsync	Vertical synchronous signal (negative)	I	
5	GND	GND	_	
6	RO	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	_	
13	GO	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	_	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	_	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	VDD	3.3V power supply	_	
29	VDD	3.3V power supply	_	
30	R/L	Horizontal display mode select signal	I	*2
	II (D	H: Normal , L: Left / Right reverse mode		
31	U/D	Vertical display mode select signal	I	*2
	TT (0	L : Normal , H : Up / Down reverse mode		
32	V/Q	H: Normal	I	
33	GND	GND	-	

: 08-6210-033-340-800+ (ELCO) LCD side connector

Recommended matching FFC or FPC : 0.5mm pitch

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.

*2



R/L = HU/D = L



R/L = LU/D = L



R/L = HU/D = H



R/L = LU/D = H

7-2. LED Backlight

PIN No.	SYMBOL	DESCRIPTION
1	AN1	Anode1
2	AN2	Anode2
3	AN3	Anode3
4	AN4	Anode4
5	CA4	Cathode4
6	CA3	Cathode3
7	CA2	Cathode2
8	CA1	Cathodel

LED Backlight side connector : FPC

Recommended matching connector : IMSA-9637S (IRISO)

7–3. Touch panel

PIN No.	SYMBOL	DESCRIPTION
1	уU	y-Upper terminal
2	хL	x-Left terminal
3	уL	y-Lower terminal
4	xR	x-Right terminal

LCD side FPC : 1.25mm pitch

Recommended matching connector: Series 6216 (ELCO)

: Series FE, FFS (JST) : Series SFD (FCI)

8. Timing Characteristics of input signals

8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Clock	Frequency	1/Tc	_	25. 18	28.33	MHz	V/Q=H
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5			ns	
Data	Hold time	Tdh	10	_	_	ns	
Horizontal sync.	Cycle	TH	30.0	31.8	-	μs	V /O II
signal		TH	770	800	900	clock	V/Q=H
	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	V/Q=H
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	y period	THd	640		clock		
HsyncClock phas	e difference	ТНс	10	_	Tc-10	ns	
HsyncVsync. phase difference		TVh	0	_	ТН-ТНр	ns	
Vertical sync.signal start position		TVs	34			line	V/Q=H
Vertical display	period	TVd		240		line	

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

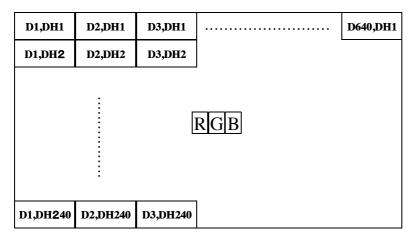
ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Enable signal	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Тер	2	640	TH-10	clock	
HsyncEnable signal phase difference		The	44	_	TH-664	clock	V/Q=H

^{*}When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

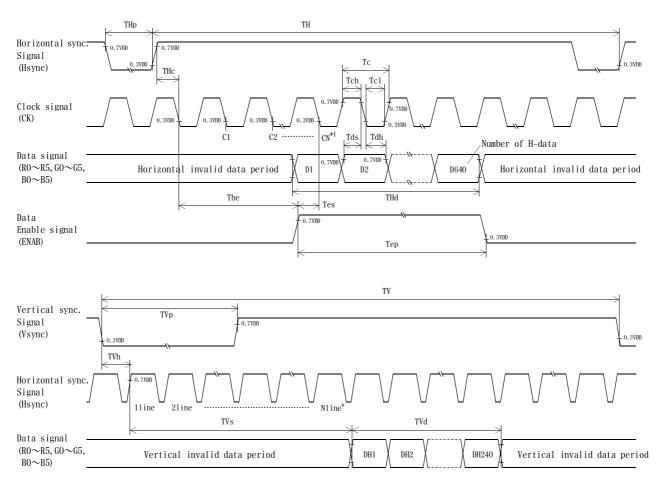
8-3. Vertical display position

The vertical display position (TVs) is fixed at 34th line (V/Q=H). Note) ENAB signal is independent of vertical display position.

8-4. Input Data Signals and Display position on the screen



8-5. Input Timing Characteristics



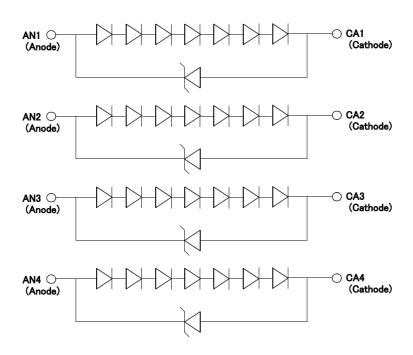
- *1 When ENAB is fixed at V/Q="H", the display starts from the data of C104(Clock).
- *2 The vertical display position(TVs) is fixed at 34^{th} line(V/Q=H).

9. Backlight Characteristics

Temp. = 25° C

ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Forward current *1 IF			(15)		mA	Ta=25°C
		_	(23.8)	(26.0)	V	IF=(15) mA, Ta=-(20) °C
Forward voltage	VF	_	(21.7)	(23.8)	V	IF=(15)mA, Ta=25°C
		_	(21.5)	(23.5)	V	IF=(15)mA, Ta=(70)°C
Operating life *2	Т	_	(TBD) *3	_	h	IF=(15)mA, Ta=25°C

*1 For each "AN1-CA1", "AN2-CA2", "AN3-CA3" and "AN4-CA4".



- *2 When surface brightness decreases 50% of initial brightness.
- *3 Life time is estimated data.
- * A forward current below 8.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

10. Design Guidance for Analog Touch-Panel (T/P)

10-1. Electrical

In customer's design, please remember the following considerations.

- 1. Do not use the current regulated circuit.
- 2. Keep the current limit with top and bottom layer. (See Sec, 4-1)
- 3. Analog T/P can not sense two point touching separately.
- 4. A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read the T/P position data.
- 5. Analog T/P is also a "Capacitor" in an equivalent circuit.

 Design your sensing circuit and low-pass filter with considering this "Capacitor" value.
- 6. Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1. Do the "User Calibration".
- 2. "User Caribration" may be needed with long term using. Include "User Caribration" menu in your software.
- 3. When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

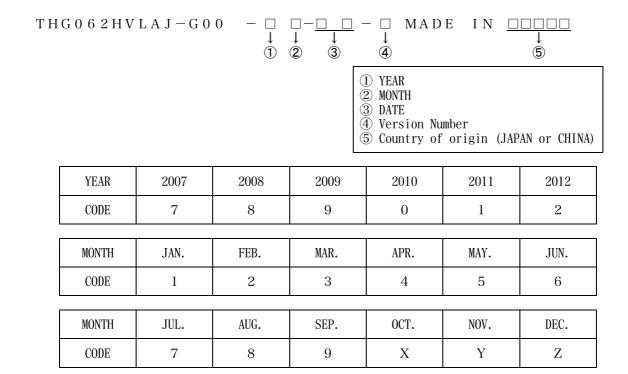
10-3. Mounting on display and housing bezel

- 1. Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2. Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3. If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur.

This will cause sometimes a short circuit.

11. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

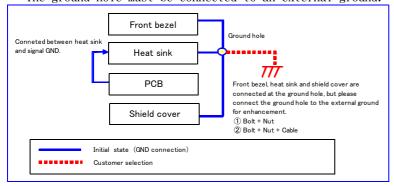
11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

1.2. Precautions for use

12-1. Installation of the LCD

1. The LCD's bezel must be grounded. The heat sink and shield cover are connected at the ground hole. The ground hole is located on the right side of the LCD when viewed from the front. The ground hole must be connected to an external ground.



- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:0.265 \pm -0.025N.m Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .

Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 4.2 ± 0.5 mm

Please be careful not to use high torque which may damage LCD module in installation.

7. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

12-4. Storage

1. The LCD shall be stored within normal temperature and humidity.

Store in a dark area and protected the LCD from direct suplight or file.

Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.

2. The LCD should be packaged to prevent damage.

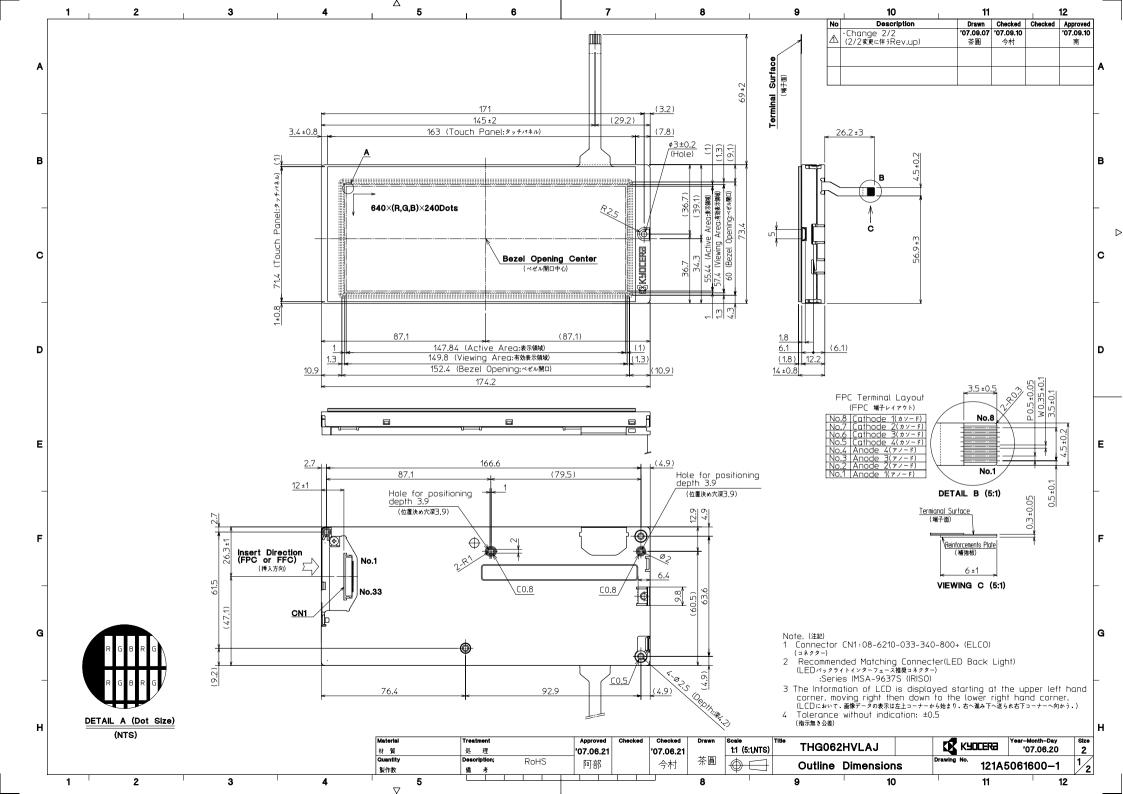
12-5. Screen Surface

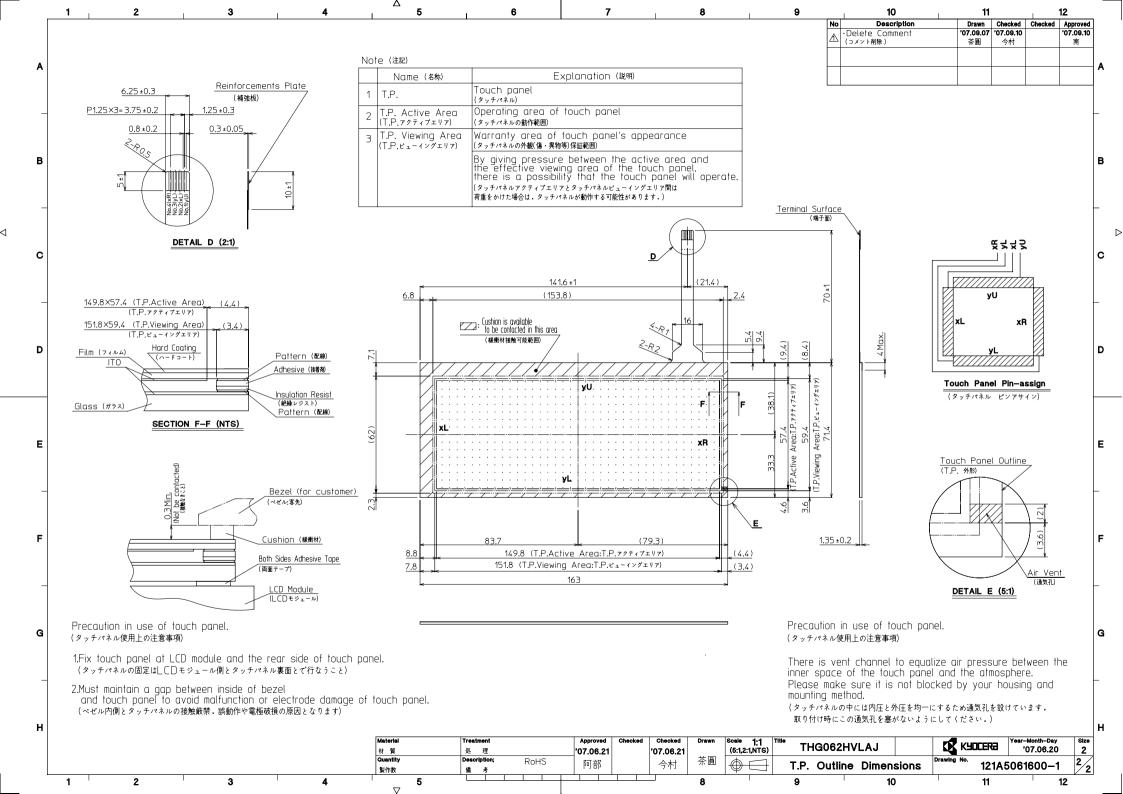
- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2. Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3. When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4. Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6. Do not pull the LED cable and do not bend the root of the wires. Housing should be designed to protect LED cable from external stress.
- 7. Do not disassemble LCD module because it will result in damage.
- 8. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used
- 9. Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

13. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	80°C	240h	Display Quality : (TBD) Display Function : (TBD) Current Consumption : (TBD)
Low Temp. Atmosphere	−30°C	240h	Display Quality : (TBD) Display Function : (TBD) Current Consumption : (TBD)
High Temp. Humidity Atmosphere	40°C 90 %RH	240h	Display Quality : (TBD) Display Function : (TBD) Current Consumption : (TBD)
Temp. Cycle	-30°C 0.5 h R.T. 0.5 h 80°C 0.5 h	10 cycles	Display Quality : (TBD) Display Function : (TBD) Current Consumption : (TBD)
High Temp. Operation	70°C	240h	Display Quality : (TBD) Display Function : (TBD) Current Consumption : (TBD)
Point Activation life	Polyacetal stylus (RO.8) Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance : No defect Insulation resistance : No defect Linearity : No defect Actuation Force : No defect

- st Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- * The LCD is tested in circumstances in which there is no condensation.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





SPEC. NO.	TQ3C-8EAF0-E2DKB14-00
DATE	September 7, 2007

FOR	•	
$I \cap I$	•	

KYOCERA INSPECTION STANDARD

<u>TYPE</u>: THG062HVLAJ-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	Confirmed by :QA Dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
September 7, 2007	7. Credera	y. Yamazaki	J. Matsumoto.	J. Sakaguchi	To las

Revision Record

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рате	-	Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ons	

1) Note

			Note			
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.					
	2. Inspection Conditions Luminance : 500 Lux minimum Inspection distance : 300 mm (from the sample) Temperature : $25 \pm 5 \degree C$ Direction : directly above					
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter. RGBRGBRGB RGBRGB RGBRGB RGBRGBRGB RGBRGBRGB			
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white data sent to the screen.			
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.			
			RGBRGBRGB RGBRGBRGB			
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.			
		Appearance inspection	Does not satisfy the value at the spec			
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.			
	Definition of size	Definition of ci				

2) Standard

Classification		Inspection item		Judgement standard					
defect (in LCD glass) Dot defect Bright dot defect Black dot defect		fect	Acceptable number : 4 bright dots defended : 5 mm or more						
		Black dot defect		Acceptable number : 5 black dots defects Black dot spacing : 5 mm or more					
		2 dots join Bright dot defect Black dot defect		Acceptable number : 2					
				Acceptable number : 3					
		3 or more dots join		Acceptable number : 0					
Others		Total dot defects		Acceptable number : 5 Max					
		White dot, Dark dot (Circle)		Size(mm) Acceptable Number					
				d<0.2			(neglected)		
				0.2 <d≦0< td=""><td>0. 4</td><td colspan="3">5</td></d≦0<>	0. 4	5			
				0.4 <d≦< td=""><td>0. 5</td><td colspan="3">3</td></d≦<>	0. 5	3			
				0.5 <d< td=""><td></td><td colspan="3">0</td></d<>		0			
				1	I				
	pection	Polarizer(Scratches)		Width (mm)	Length(mm)		Acceptable Number		
(Defect o Polarize	er or			W≦0.1	_		(neglected)		
between -er and					L≦5.0		(neglected)		
glass)				$0.1 < W \le 0.3$	5.0 < L		0		
				0.3 <w< td=""><td colspan="2">-</td><td colspan="2">0</td></w<>	-		0		
		Polarizer Touch panel (Bubble, Dent)							
				Size(mm)			Acceptable Number		
				d<0.2		(neglected)			
				0. 2 < d ≤ 0. 3		5			
				0.3 <d≦0.5< td=""><td colspan="2">3</td></d≦0.5<>		3			
				0.5 <d< td=""><td colspan="2">0</td></d<>		0			
		Foreign Particle(Circular shape)		Size(mm)		Λ	Acceptable Number		
		Shape)		d<0.2		(neglected)			
				0. 2 < d ≤ 0. 4		5			
				$0.4 < d \le 0.5$		3			
				0.5 <d< td=""><td colspan="2">0</td></d<>		0			
				-					
		Foreign Particle (Linear shape), Scratches		Width (mm)	Length(m	ım)	Acceptable Number		
				W≦0.03	_		(neglected)		
				$0.03 < W \le 0.1$	L≦2.0		(neglected)		
					2.0 <l≦4.0< td=""><td colspan="2">3</td></l≦4.0<>		3		
					4.0 <l< td=""><td colspan="2">0</td></l<>		0		
				0.1 <w -<="" td=""><td colspan="2">(According to Circular shape)</td></w>		(According to Circular shape)			

Inspection item	Judgement standard								
-	-								
	(D = Average Diameter = (major axis + minor axis) / 2								
		W dt h		Lengt h(n		ccept abl e			
		$0.1 \ge W > 0.08$ $4 \ge L$		1	lpcs within φ 30mm				
	Scrat ch					2pcs within ϕ 20mm			
Scratch,			$0.05 \ge W > 0.03$ $10 \ge L$			2pcs within ϕ 20mm			
Foreige particle	E		0. 03≥W 20≥L			negl ect ed 2pcs within ϕ 30mm			
(Touch Screen portion)	For ei ge		<i>№</i> 0. 05	5≧ L		neglected			
	(line	(line $0.05 \ge W$ neglected like)					1		
	For ei ge	0. 3≥ D> 0. 2			2nc	2pcs within φ 30mm			
	(circle	0. 3 ≦ D			2pc	negl ect ed			
	l i ke)	0. Z = D				negreered			
	TTRC)	11 NC)							
	Above are applied to the visible area.								
	Unless there are foreign particles and damage affected								
	seriously to the electrical performance in the visible								
	area, we approve of this product.								
	+								
Glass crack									
(Touch Screen portion)					Cracks in other area				
					than in corner				
	Ludgam					4/			
	_								
						7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7			
	dar d								
		X	Y	Z	X	Y	Z		
		≦3	≦3	<t< td=""><td>≦ 5</td><td>≤ 1.5</td><td><t< td=""></t<></td></t<>	≦ 5	≤ 1.5	<t< td=""></t<>		
		2pcs/panel		2pcs/si de					
		Progressi ve crack							
	Judgeme								
	nt stan								
	dar d								
		0 (N)							
		Opcs(NG even 1pcs)							
	Above are applied to the visible area.								
Unless there are foreign particles and damage									
	-	eriously to the electrical performance in the visible							
	area, we	area, we approve of this product.							
	1								