

SPECIFICATION FOR APPROVAL

(•) Preliminary Specification

() Final Specification

Title

55.0" WUXGA TFT LCD

BUYER	General
MODEL	10/0
	-ase

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LC550EUN		
SUFFIX	SFM2 (RoHS Verified)		

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY S	IGNATURE DATE	APPROVED BY O. H. Lee / Team Leader	SIGNATURE DATE
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/		PREPARED BY K. B. Park / Engineer	
Please return 1 copy for your confirmation with your signature and comments.		PD Product Design LG Display Co., I	

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.0	Feb,8, 2013	-	Preliminary Specification(First Draft)
0.1	Jul,10, 2013	-	Changed model name(LC550EUN-SFM1 → LC550EUN-SFM2)
1.0	Aug,14, 2013	-	Final specification
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1. General Description

The LC550EUN is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element.

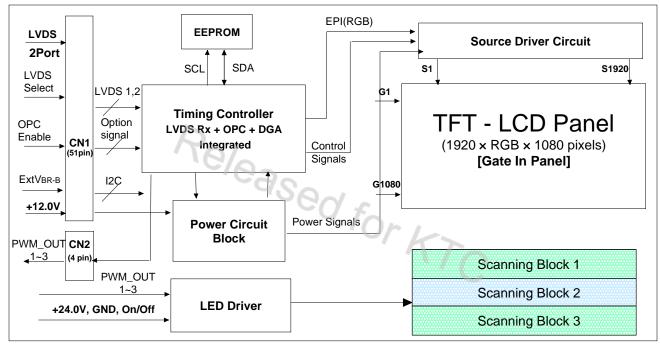
It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes.

Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7 Milion colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high colorgamut, high color depth and fast response time are important.



General Features

Active Screen Size	54.64 inches(1387.80mm) diagonal
Outline Dimension	1244.6(H) × 720.9(V) × 9.9(B) / 22.6(D) mm (Typ.)
Pixel Pitch	0.630 mm x 0.630 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8bit, 16.7 Million colors
Luminance, White	350 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 87.58W (Typ.) [Logic= 6.88W, LED Driver=80.7W(ExtVbr_B=100%)]
Weight	16.5Кg (Тур.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(2H), Anti-glare treatment of the front polarizer (Haze 1%(Typ.))

2. Absolute Maximum Ratings

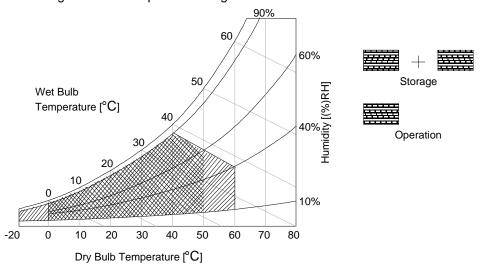
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Parameter		Symbol	Va	lue	Unit	Note
Fala	Parameter		Min	Max	Unit	
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	VDC	
	ON/OFF	Voff / Von	-0.3	+5.5	VDC	1
Driver Control Voltage	Brightness	EXTVBR-B	-0.3	+4.0	VDC	
T-Con Option Selection	Voltage	VLOGIC	-0.3	+4.0	VDC	
Operating Temperature		Тор	0	+50	°C	
Storage Temperature		Tst	-20	+60	°C	2,3
Panel Front Temperature		TSUR	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	
Storage Humidity	~~ea	Нѕт	10	90	%RH	2,3

Table 1. ABSOLUTE MAXIMUM RATINGS

Note 1. Ambient temperature condition (Ta = 25 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below.
 - Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Parameter	Symbol		Value	Unit	Note	
Parameter	Symbol	Min	Тур	Max	Unit	Note
Circuit :						
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Bower Input Current	li op	-	573	745	mA	1
Power Input Current	ILCD	-	840	1092	mA	2
Power Consumption	PLCD		6.88	8.94	Watt	1
Rush current	IRUSH	-	-	5.0	А	3
R	ExtV _{BR-B}	5	-	100	%	On Duty
Brightness Adjust for Back Light		1	-	100	%	4
с , с	ExtV _{BR-B} Frequency	40	50/60	80	Hz	
Pulse Duty Level	High Level	2.5		3.6	Vdc	HIGH : on duty LOW : off duty
(PWM)	Low Level	0	イト	0.8	Vdc	LOW : off duty

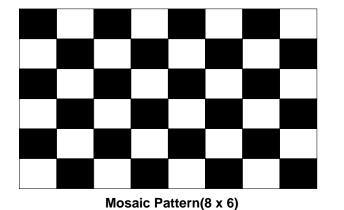
Note 1. The specified current and power consumption are under the V_{CD}=12.0V, Ta=25 2°C, f_V=60Hz condition, and mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.

- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

4. ExtV_{BR-B} signal have to input available duty range and sequence. After Driver ON signal is applied, ExtV_{BR-B} should be sustained from 5% to 100% more than 500ms. After that, ExtV_{BR-B} 1% and 100% is possible

For more information, please see 3-6-2. Sequence for LED Driver.

5. Ripple voltage level is recommended under \pm 5% of typical voltage



White : 255 Gray Black : 0 Gray

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Doromotor		Symbol		Values		Linit	Notos	
Parameter			Symbol	Min	Тур	Max	Unit	Notes
LED Driver :								
Power Supply Input	Voltage		VBL	22.8	24.0	25.2	Vdc	1
Power Supply Input	Current		IBL	-	3.36	3.62	A	1
Power Supply Input Current (In-Rush)		In-rush	-	-	6.0	A	$V_{BL} = 22.8V$ ExtV _{BR-B} = 100% 3	
Power Consumption	า		PBL	-	80.7	86.9	W	1
Input Voltage for Control System	00/0#	On	V on	2.5	-	5.0	Vdc	
Control System On/Off Signals Off		V off	-0.3	0.0	0.7	Vdc		
LED :	LED :							
Life Time				30,000	50,000		Hrs	2

Notes :

- 1. Electrical characteristics are determined after the unit has been 'ON and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B: 100%), it is total power consumption.
- 2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at 25±2°C.
- 3. The duration of rush current is about 200ms. This duration is applied to LED on time.
- 4. Even though inrush current is over the specified value, there is no problem if it spec of fuse is satisfied.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF(manufactured by JAE) or GT05P-51S-H38(manufactured by LSM) or IS050-C51B-C39(manufactured by UJU)
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description		Symbol	Description
1	NC	No Connection (Note 4)	27	NC	No connection
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	ExtVBR-B	External PWM (from System)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	OPC Enable	'H' = Enable , 'L' or NC = Disable	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	NC	No connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	NC	No connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground (Note 6)
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	NC	No connection	50 VLCD Power Supply +12.0V		
25	NC	No connection	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

Note 1. All GND(ground) pins should be connected together to the LCD modules metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. #1~#6 & #9 NC (No Connection): These pins are used only for LGD (Do not connect)
- 5. Specific pins(pin No. **#10**) are used for OPC function of the LCD module.

If not used, these pins are no connection. (Please see the **Appendix VI** for more information.) 6. Specific pin No. **#44** is used for "No signal detection" of system signal interface.

It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Module

Master -LED Driver Connector : 20022WR - H14B2(Yeonho) or Compatible - Mating Connector : 20022HS - 14B2 (Yeonho) or Compatible

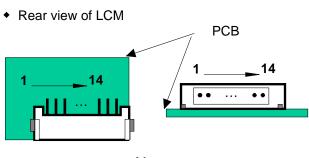
Table 5-1. LED DRIVER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	Status	Back Light Status	2
12	Von/off	Backlight ON/OFF control	3
13	NC	Don't care	
14	NC	Don't care	

Notes :1. GND should be connected to the LCD module's metal frame.

2. Normal : Low (under 0.7V) / Abnormal : Open

3. Each impedance of pin #12 is over 50 [K] .



Status
 Driver
 SSM3K
 7002
 1k2
 status

<Master>

3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

ITE	м	Symbol	Min	Тур	Мах	Unit	Note			
Horizontal	Display Period	tH∨	960	960	960	tCLK	1920 / 2			
	Blank	tнв	100	140	240	tCLK	1			
	Total	tHP	1060	1100	1200	tCLK				
	Display Period	t∨∨	1080	1080	1080	Lines				
Vertical	Blank	t∨B	20 (228)	45 (270)	69 (300)	Lines	1			
	Total	tVP	1100 (1308)	1125 (1350)	1149 (1380)	Lines				
				10						

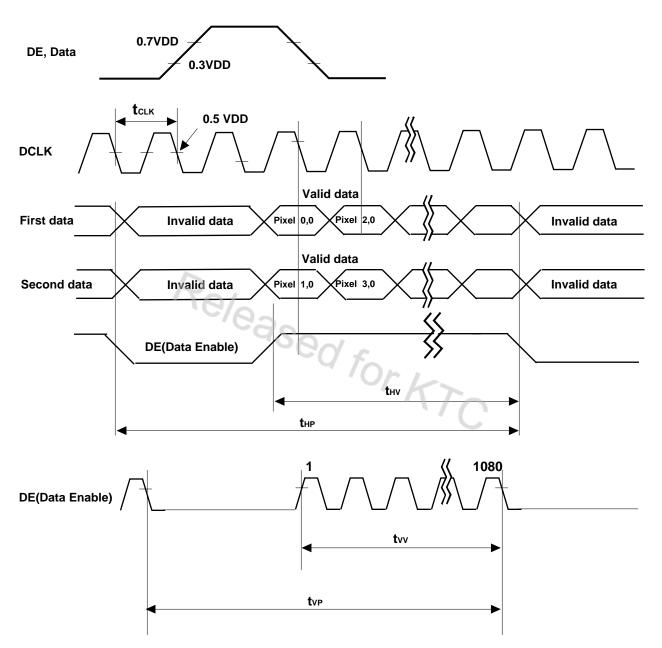
Table 6. TIMING TABLE (DE Only Mode)

ITE	М	Symbol	Min	Тур	Max	Unit	Note
	DCLK	fclk	63.00	74.25	78.00	MHz	
	Horizontal	fн	57.3	67.5	70	KHz	2
Frequency	Vertical	f∨	57 (47)	60 (50)	63 (53)	Hz	2 NTSC (PAL)

- Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
 - 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
 - % Timing should be set based on clock frequency.

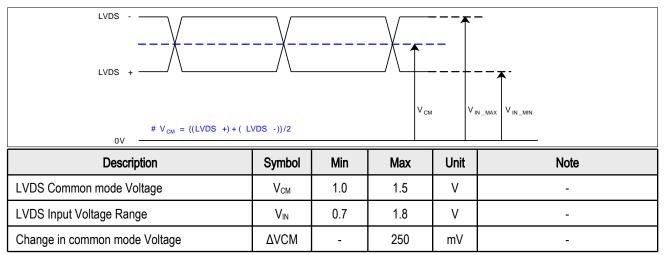
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

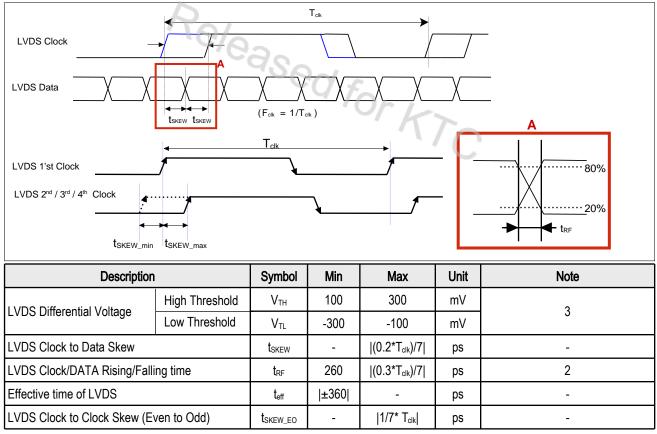


3-4-2. LVDS Input Signal Characteristics

1) DC Specification

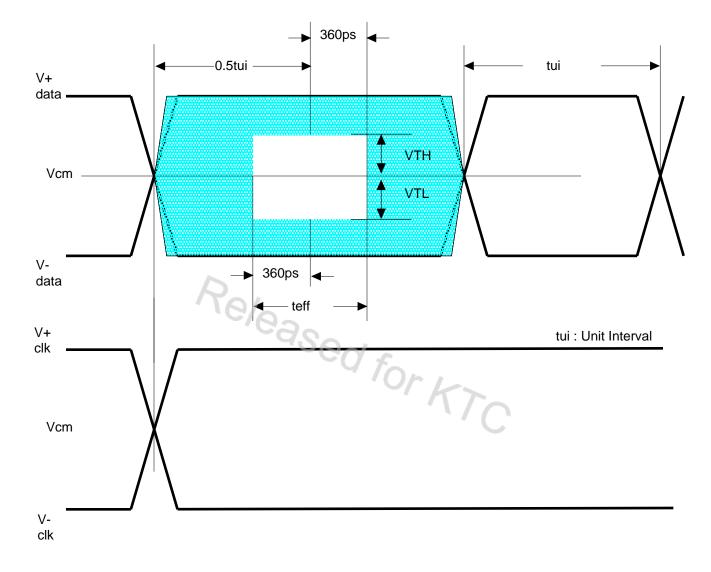


2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range.
- 3. LVDS Differential Voltage is defined within $t_{\mbox{\tiny eff}}$



3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

 Table 7. COLOR DATA REFERENCE

				Input Cold	or Data		
	Color	RED MSB) LSB	GREI MSB	EN LSB	BLUE MSB	LSB
		R7 R6 R5 R4 R	3 R2 R1 R0	G7 G6 G5 G4 G	63 G2 G1 G0	B7 B6 B5 B4 B3	B2 B1 B0
	Black	0 0 0 0 0	000	00000	0000	00000	000
	Red (255)	1 1 1 1 1	1 1 1	00000	0000	00000	0 0 0
	Green (255)	0 0 0 0 0	0 0 0	1 1 1 1 1	1 1 1	00000	000
Basic	Blue (255)	00000	0 0 0	00000	0000	11111	111
Color	Cyan	00000	0 0 0	1 1 1 1 1	1 1 1	11111	111
	Magenta	1111	111	00000	000	11111	111
	Yellow	1 1 1 1 1	111	11111	1 1 1	00000	0 0 0
	White	11111	111	11111	1 1 1	11111	111
	RED (000)	00000	000	00000	000	00000	0 0 0
	RED (001)	0 0 0 0 0	001	00000	0000	00000	0 0 0
RED					0		
	RED (254)	11111	110	00000	0000	00000	000
	RED (255)	11111	111	00000	0000	00000	000
	GREEN (000)	0 0 0 0 0	0 0 0	00000	000	00000	000
	GREEN (001)	0 0 0 0 0	0 0 0	00000	001	00000	0 0 0
GREEN							
	GREEN (254)	0 0 0 0 0	0 0 0	11111	1 1 0	00000	0 0 0
	GREEN (255)	0 0 0 0 0	0 0 0	11111	111	00000	000
	BLUE (000)	0 0 0 0 0	0 0 0	00000	0000	00000	0 0 0
	BLUE (001)	0 0 0 0 0	0 0 0	00000	0000	00000	001
BLUE							
	BLUE (254)	0 0 0 0 0	0 0 0	00000	0000	11111	110
	BLUE (255)	0 0 0 0 0	0 0 0	00000	0000	11111	111

3-6. Power Sequence

3-6-1. LCD Driving circuit

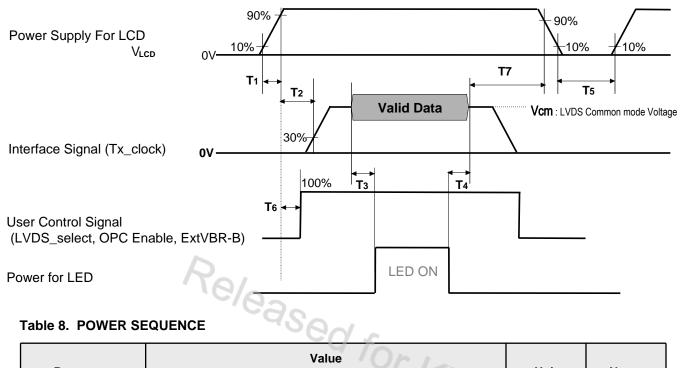


Table 8. POWER SEQUENCE

D		1114	Neter		
Parameter	Min	Тур	Max	Unit	Notes
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
Т3	400	-	-	ms	3
T 4	200	-	-	ms	3
T5	1.0	-	-	S	4
T6	0	-	T2	ms	5
T 7	0	-	-	ms	6

1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied. Note :

- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. T5 should be measured after the Module has been fully discharged between power off and on period.
- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
- × Please avoid floating state of interface signal at invalid period.
- % When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

3-6-2. Sequence for LED Driver

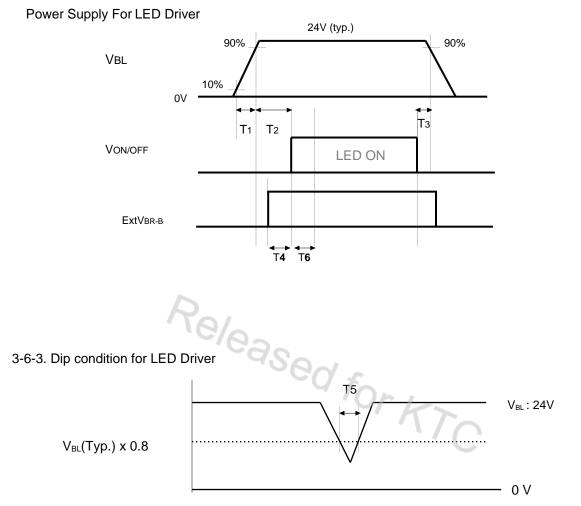


Table 9. Power Sequence for LED Driver

Parameter	Values			Linita	Pomorko
Parameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	10	-	-	ms	
T4	0	-	-	ms	
T5	-	-	10	ms	V _{вL} (Тур) х 0.8
Т6	500	-	-	ms	2

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. Even though T1 is over the specified value, there is no problem if T spec of fuse is satisfied.

2. In T6 section, $\mathsf{ExtV}_{\mathsf{BR-B}}$ should be sustained from 5% to 100% .

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON and stable in a dark environment at 25±2°C. The values are specified at 50cm from the LCD surface at a viewing angle of and equal to 0°. FIG. 1 shows additional information concerning the measurement equipment and method.

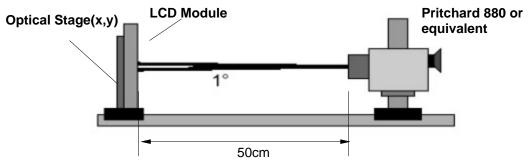


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, EXTVBR-B =100%

	_	R			Value			
	Parameter		Symbol	Min	Тур	Max	Unit	Note
Contrast Ra	atio		CR	1000	1400	-		1
Surface Lu	minance, wh	ite	LWH	280	350		cd/m ²	2
Luminance	Variation		WHITE 9P	70	1			3
Response ⁻	T :	Variation	G to G		6	9		5
Response	Time	Gray to Gray (BW)	G to G BW		8	12	ms	4
RED	PED	Rx		0.642				
		RED	Ry		0.335			
		GREEN	Gx		0.310			
Color Coor	dinates	OREEN	Gy	Тур	0.604	Тур		
[CIE1931]	BLUE	Bx	-0.03	0.152	+0.03			
	[CIE1931] BLUE	DLUL	Ву		0.061	-		
		WHITE	Wx		0.281			
		VVIII L	Wy		0.288			
Color Temp	erature				10,000		К	
Color Gamu	ıt				68		%	
		right(=0°)	r (x axis)	89	-	-		
Viewing	2D	left (=180°)	l (x axis)	89	-	-		
Angle	(CR>10)	up (=90°)	u (y axis)	89	-	-	degree	6
		down (=270°)	d (y axis)	89	-	-		
Gray Scale				-	2.2	-		7

Note : 1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = Surface Luminance with all white pixels

It is measured at center 1-point.

- Surface luminance are determined after the unit has been 'ON and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance, WHITE is defined as :

WHITE(9P) = Minimum (Lon1, Lon2~ Lon8, Lon9) / Maximum (Lon1, Lon2~ Lon8, Lon9)*100

Where L_{on1} to L_{on9} are the luminance with all pixels displaying white at 9 location For more information, see the FIG. 2.

4. Response time is the time required for the display to transit from any gray to white (Rise Time, T_k) and from any gray to black (Decay time, T_b). For additional information see the FIG. 3.
 ※ G to G_{BW} Spec stands for average value of all measured points.

Photo Detector : RD-80S / Field : 2°

5. G to G is Variation of Gray to Gray response time composing a picture

	(Xi- u) ²	XI = Individual Data
G to G () =	N	<pre>u = Data average N : The number of Data</pre>

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis

which

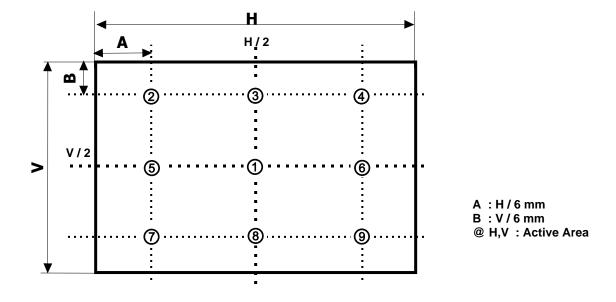
is normal to the LCD module surface. For more information, see the FIG. 4.

7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. Gray scale specification

Gray Level	Luminance [%] (Typ)
LO	0.07
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100



Measuring point for surface luminance & measuring point for luminance variation.

FIG. 2 9 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".

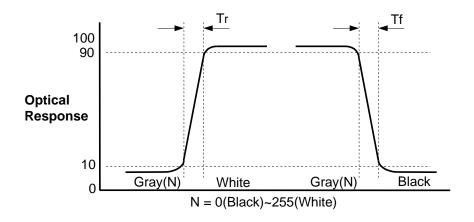
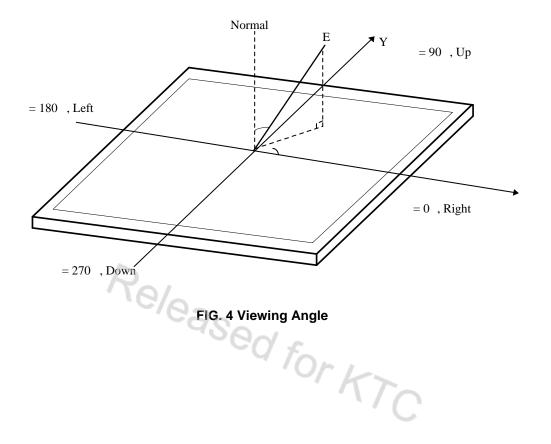


FIG. 3 Response Time

Dimension of viewing angle range



5. Mechanical Characteristics

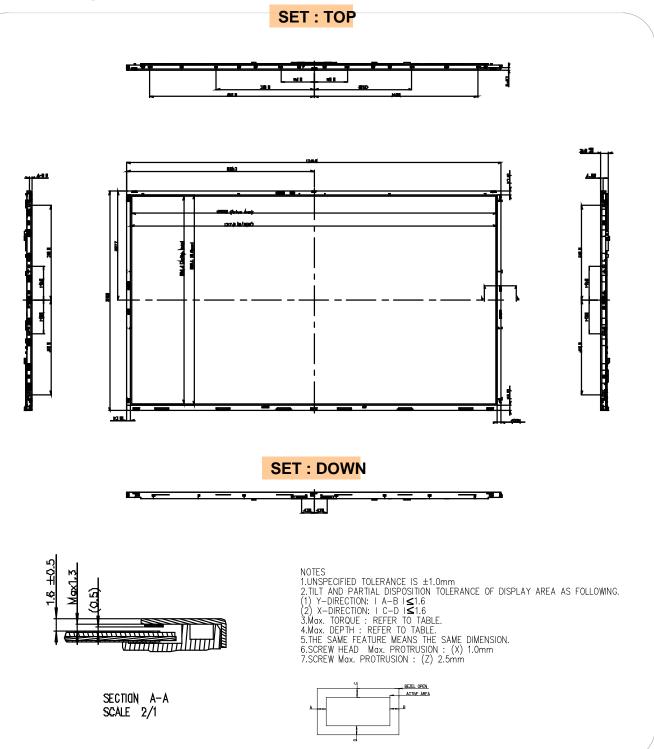
Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

Item	Value	
	Horizontal	1244.6 mm
Outline Dimension	Vertical	720.9 mm
	Depth	9.9 mm
Denslares	Horizontal	1217.6 mm
Bezel Area	Vertical	688.4 mm
A stine Diselan Area	Horizontal	1209.6 mm
Active Display Area	Vertical	680.4 mm
Weight	16.5 Kg (Typ.), 17.5 kg (Max.)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

[FRONT VIEW]



11 S(From Front Sole)

VOLO TAP

1

-)AAA

*[*2]

٠

[REAR VIEW]

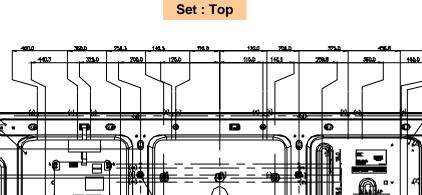
17.4[from Front Sole]

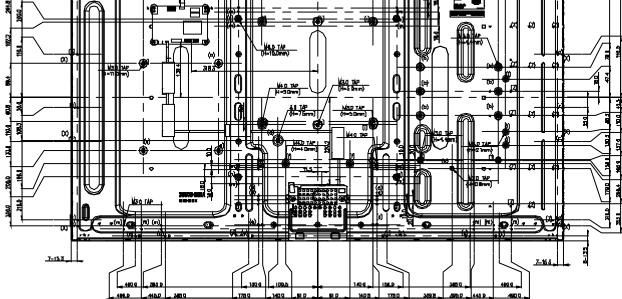
18.2[from Frant Sole]

1.9[from Frant Schi]

140[from Front Sole]

2





Set : Down

ITEM	TAP	Max Depth (mm)	Torque (kgf.cm)	Notes
(a)	M3.0	Max 10.0	Max 8.0	4ea
(b)	M3.0	Max 6.0	Max 8.0	6ea
(c)	M3.0	Max 5.0	Max 8.0	30ea
(d)	M3.0	Max 7.0	Max 8.0	1ea
(e)	M4.0	Max 7.0	Max 10.0	10ea
(f)	M4.0	Max 3.5	Max 10.0	2ea
(g)	M4.0	Max 4.5	Max 10.0	1ea
(h)	M3.0	Max 4.5	Max 8.0	2ea
(1)	M3.0	Max 4.5	Max 8.0	3ea
(j)	M3.0	Max 6.0	Max 8.0	2ea
(k)	M3.0	Max 2.5	Max 8.0	1ea
(1)	M3.0	Max 3.5	Max 8.0	3ea
(m)	M3.0	Max 7.0	Max 8.0	14ea
(n)	M6.0	Max 12.0		4ea

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, Each direction per 10 min
6	Shock test (non-operating)	Shock level : 30Grms Waveform : half sine wave, 11ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc.
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association.
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class 1M)

- 2. Caution
 - : LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.

b) CISPR 22 "Information technology equipment – Radio disturbance characteristics – Limit

and

methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.

c) CISPR 13 "Sound and television broadcast receivers and associated equipment – Radio disturbance

characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Packing

8-1. Information of LCM Label

a) Lot Mark



A,B,C : SIZE(INCH) E : MONTH D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	E	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one Pallet : 18 pcs
- b) Pallet Size : 1440 mm(W) X 1140 mm(D) X 950 mm(H)

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes

circuit break

by electro-chemical reaction.

- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 - Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the

adhesives

used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they

cause

chemical damage to the polarizer

- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength. 9-2. Operating Precautions

- (1) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.
- (9) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

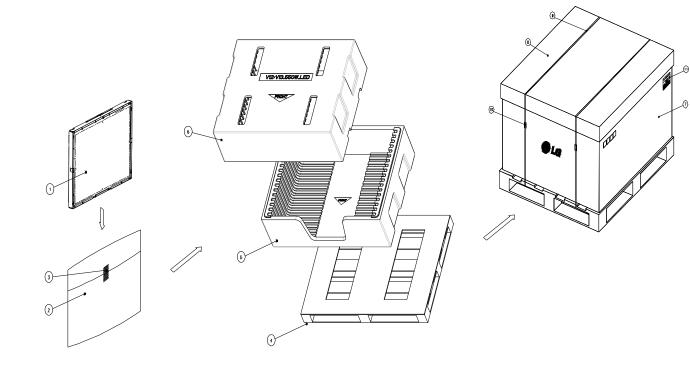
9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
- It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

APPENDIX-I

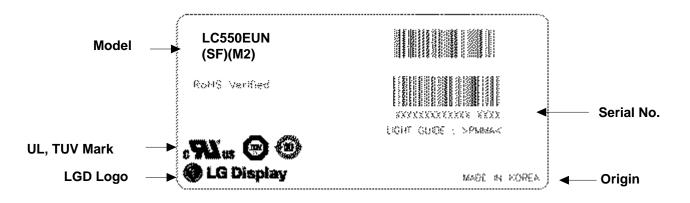
Pallet Ass'y



	NO	DESCRIPTION	MATERIAL					
	1	LCD Module	-					
	2 BAG		55INCH					
	3	TAPE	MASKING 20MM X 50M					
	4	PALLET	Plywood (1440X1140X134.5)					
	5	PACKING_BOTTOM	EPS					
	6 PACKING_TOP		EPS					
	7	ANGLE PACKING	PAPER PAPER					
	8	ANGLE COVER						
	9	BAND	PP					
	10BAND,CLIP11LABEL		STEEL					
			YUPO PAPER 80G 100X70					

APPENDIX- II-1

LCM Label



Released for KTC

APPENDIX- II-2

Pallet Label



APPENDIX- III-1

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L" or "NC")

Host System	THC63LVD103				
24 Bit	or Compatible				Timing
RED0	33		I		Controller
RED1	34	FI-	RE51S-		
RED2	35]	
RED3	36				
RED4	37 TA-	31	12		RO0N
RED5	38 TA+	30	13	100 🗧	RO0P
RED6	59		10		
RED7	61	29			DOWN
	4 TB-	28	14	100 >	RO1N
	5 TB+	20	15		RO1P
GREEN0	40				
GREEN1	41 TC-	25	16		RO2N
GREEN2	42 TC+	24	17	100 🗧	RO2P
GREEN3	44			, , , , , , , , , , , , , , , , , , ,	
GREEN4	45	23	10		
GREEN5	46 TCLK-	22	19	100 🗧	ROCLKN
GREEN6	62 TCLK+		20		ROCLKP
GREEN7	63				
	6 TD-	21	22		RO3N
	8 TD+	20	23	100 🗧	RO3P
BLUE0	48		20		
BLUE1	49				
BLUE2	50				
BLUE3	52				
BLUE4	53				
BLUE5	54		7		VESA/ JEIDA
BLUE6	64				
BLUE7	1				
	9				
	11		1		
Hsync	55				
Vsync	57	GND		LCM Module	
Data Enable	Data Enable 58				
CLOCK	12				

Note: 1. The LCD module uses a 100 Ohm[] resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- III-2

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

Host System	THC63LVD103				
24 Bit	or Compatible				Timing
RED0	4		1		Controller
RED1 –	5	FI-I	RE51S-		
RED2	59]	
RED3	61				
RED4	33 TA	31	12		ROON
RED5	34 TA+	30	13	100 🗧	RO0P
RED6	35				
RED7	36	29			DOAN
	37 TB·	20	14	100 >	RO1N
			15		RO1P
GREEN0	6	05			
GREEN1	8 TC·	25	16		RO2N
GREEN2	62 TC+	24	17	100 🗧	RO2P
GREEN3	63				
GREEN4	40 40	23	40		
GREEN5		- 22	19	100 🗧	ROCLKN
GREEN6	42 TCLK+		20	100 <	ROCLKP
GREEN7	44	~ 10			
	45 TD-		22		RO3N
	46	20	23	100 🗧	RO3P
BLUE0	9		20		
BLUE1	11				
BLUE2	64				
BLUE3	1				
BLUE4	48				
BLUE5	49		7		VESA / JEIDA
BLUE6	50				
BLUE7	52				
	53				
	54				
Hsync	55				
Vsync	57	VCC		LCM Module	
Data Enable	58	č			
CLOCK	12				

Note :1. The LCD module uses a 100 Ohm[] resistor between positive and negative lines of each receiver input.

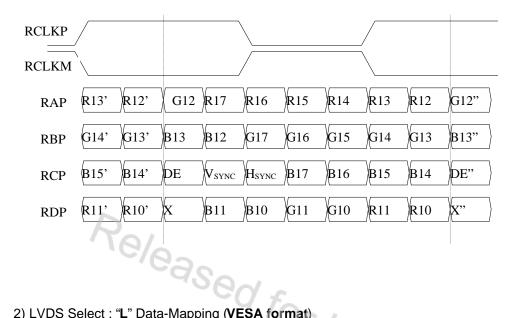
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- IV

LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



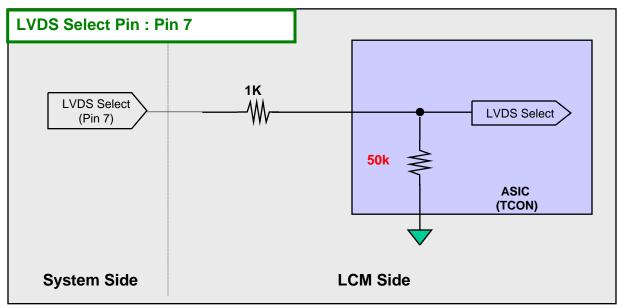
2) LVDS Select : "L" Data-Mapping (VESA format)

2) LVDS S	elect :		а-марр	oing (vi	25A 10	rmat)	K	7-		
RCLKP	/									
RCLKM	`\									
RAP	R11'	R10'	G10	R15	R14	R13	R12	R11	R10	G10"
RBP	G12'	G11'	B11	B 10	G15	G14	G13	G12	G11	B15"
RCP	B13'	B12'	DE	V _{SYNC}	H _{SYNC}	B15	B14	B13	B12	DE"
RDP	R17'	R16'	x	B17	B16	G17	G16	R17	R16	X"

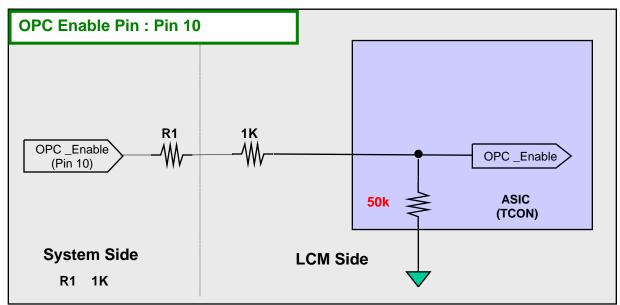
APPENDIX- V

Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of OPC Enable Selection pin

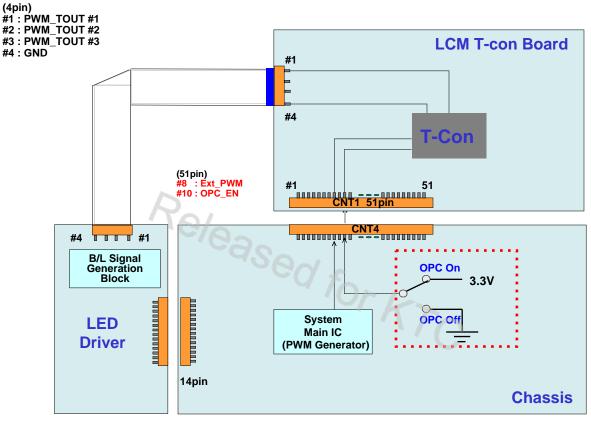


APPENDIX- VI

Scanning and OPC Design Guide

 \diamond When OPC Enable is "L", OPC Output = System Dimming.

OPC Output(PWM Signal) is synchronized with V-Sync Freq. of System in T-Con Board.





- \diamond PWM Specification (VDD = 3.3V) @ OPC
 - 1. PWM High Voltage Range : 2.5V~3.6V
 - 2. PWM Low Voltage Range : 0.0V~0.8V

