

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	11.6" Full HD TFT LCD
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Customer	
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LD116WF1
Suffix	SPN2

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
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APPROVED BY	SIGNATURE
REVIEWED BY	
PREPARED BY	
Products Engineerin LG Display Co.,	



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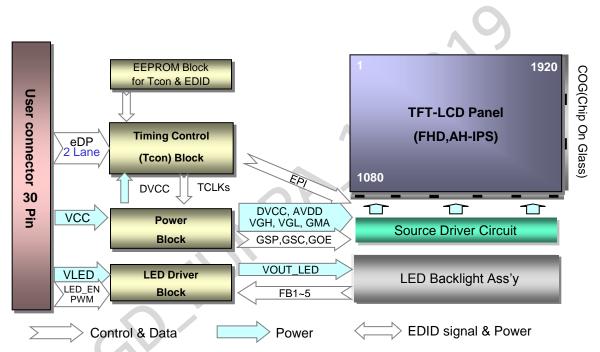
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver.
1.0	Jul.18.2014	-	First Draft	V1.0
1.1	Aug.14.2014	6	PWM duty ratio : 5%(min)→ 1%(min)	V1.0
		8	[Connector] GT05Q-30S-H10, LSM, 30, 0.5 Compatibles → GT05Q-30S-H10, LSM, 30, 0.5	-
		13	Luminance Variation (5P) is added	-
		24	Update Label Description	-
1.2	Aug.18.2014	6	Update PWM Frequency range	V1.0
		19	Update Mechanical Drawing	-
1.3	Dec. 24. 2014	19-20	Update Mechanical Rear View Drawing	V1.0
		24	Update Label Description	-
·····				



1. General Description

The LD116WF1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has 11.6 inches diagonally measured active display area with FHD resolution (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,772,216 colors. The LD116WF1 has been designed to apply the interface method that enables low power, high speed, low EMI. The LD116WF1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LD116WF1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	11.6 inches diagonal
Outline Dimension	270.0 (H, Typ.) × 160.0 (V, Typ.) × 2.25 (D, Max. w/o PCB) [mm]
Pixel Pitch	0.1335 mm x 0.1335 mm
Pixel Format	1920 horiz. By 1080 vert. Pixels RGB strip arrangement
Color Depth	8-bit, 16,772,216 colors
Luminance, White	400 cd/m ² (Typ. 5 point)
Power Consumption	Total 4.2W (Max.) Logic : 1.08W (Max. @32*32 Mosaic), B/L : 3.12W (Max. @VLED12V)
Weight	147 g (Max.) / 137 g (Typ.)
Display Operating Mode	Transmissive mode, Normally Black
Surface Treatment	Glare treatment of the front Polarizer
RoHS Compliance	Yes
BFR / PVC / As Free	Yes for all

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

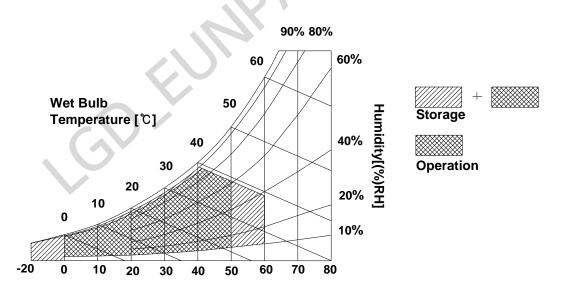
Parameter	Symbol	Val	ues	Units	Notes	
Falameter	Symbol	Min	Max	Units		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 \pm 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Table 1. ABSOLUTE MAXIMUM RATINGS

Note : 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.

Note : 2. Storage Condition is guaranteed under packing condition.



Dry Bulb Temperature [℃]

3. Electrical Specifications

3-1. Electrical Characteristics

The LD116WF1 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

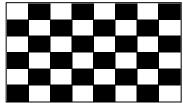
Porometer		Ourseland		Values	11-14		
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
LOGIC :							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc		306	327	mA	2
Power Consumption		Pcc		1.01	1.08	W	2
Power Supply Inrush Current		ICC_P	-	<u> </u>	1500	mA	3
Differential Impedance		Zm	90	100	110	Ω	4
BACKLIGHT : (with LED Drive	ər)						
LED Power Input Voltage		Vled	5.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	254	260	mA	6
LED Power Consumption		Pled	-	3.05	3.12	W	6
LED Power Inrush Current		ILED_P	-	-	1500	mA	7
PWM Duty Ratio			1	-	100	%	8
PWM Jitter		-	0	-	0.05	%	9
PWM Impedance		Zрwм		-	330	kΩ	
PWM Frequency		Fрwм	200	3765	5000	Hz	10
PWM High Level Voltage		V _{PWM_H}	1.5	-	3.6	V	
PWM Low Level Voltage		V _{PWM_L}	0	-	0.6	V	
LED_EN High Voltage		Vled_en_h	1.5	-	3.6	V	
LED_EN Low Voltage		Vled_en_l	0	-	0.6	V	
Life Time			12,000	-	-	Hrs	11

Table 2. ELECTRICAL CHARACTERISTICS

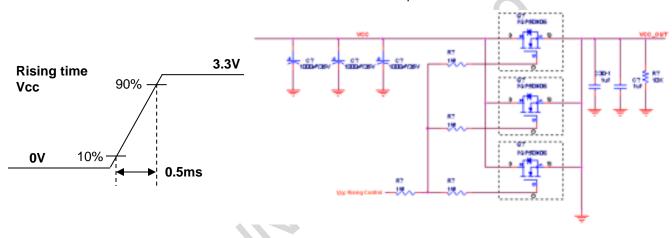


Note)

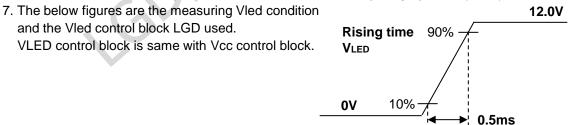
- 1. The measuring position is the connector of LCM and the test conditions are under 25 °C, fv = 60Hz, Black pattern.
- 2. The specified lcc current and power consumption are under the Vcc = 3.3V , 25 ℃, fv = 60Hz condition and Mosaic pattern.



- 2. This Spec. is the max load condition for the cable impedance designing.
- 3. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form eDP Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25 °C.
- 6. The current and power consumption with LED Driver are under the VIed = 12.0V, 25 ℃, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- Also this spec is effective at DPWM mode only and wavy noise can be found except typical frequency. 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum
- value specified in table 7. under general user condition.

3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

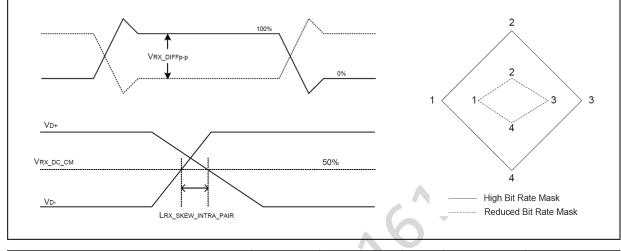
Table 3. MODULE CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	NC	No Connection	[Interface Chip]
2	GND	LCM Ground	LCD : Parade, DP643 (LCD Controller
3	Lane 1_N	Complement Signal Link Lane 1	Including eDP Receiver.
4	Lane 1_P	True Signal Link Lane 1	[Connector]
5	GND	LCM Ground	GT05Q-30S-H10, LSM, 30, 0.5
6	Lane 0_N	Complement Signal Link Lane 0	
7	Lane 0_P	True Signal Link Lane 0	[Mating Connector] 20453-030T-11, I-PEX, 30, 0.5
8	GND	LCM Ground	compatibles
9	AUX_CH_P	True Signal Auxiliary Channel	
10	AUX_CH_N	Complement Signal Auxiliary Channel	[Connector pin arrangement]
11	GND	LCM Ground	
12	LCD_VCC	LCD Logic and driver power	Rear side
13	LCD_VCC	LCD Logic and driver power	
14	LCD_Self_Test	LCD Panel Self Test	
15	GND	LCM Ground	
16	GND	LCM Ground	РСВ
17	HPD_IN	HPD signal pin	Pin1
18	GND	LCM Ground	
19	GND	LCM Ground	Cable inserting direction
20	GND	LCM Ground	[LCD Module Rear View]
21	GND	LCM Ground	
22	LED_EN	LED Backlight On/Off	
23	LED_PWM	System PWM Signal input for dimming	
24	NC_Reserved	EDID_CLK (LGD Internal Use)	
25	NC_Reserved	EDID_DATA (LGD Internal Use)	
26	VLED	LED Backlight Power	
27	VLED	LED Backlight Power	
28	VLED	LED Backlight Power	
29	VLED	LED Backlight Power	
30	NC	No Connection	

3-3. eDP Signal Timing Specifications

3-3-1. DC Specification

The VESA Display Port related DC specification is compliant with the VESA Display Port Standard v1.2.



Description	Symbol	Min	Тур	Max	Unit	Notes
Differential peak-to-peak Input voltage	V _{RX_DIFF p-p}	100		1320	mV	-
Rx input DC common mode voltage	V _{RX_DC_CM}		0		V	-

3-3-2. AC Specification

The VESA Display Port related AC specification is compliant with the VESA Display Port Standard v1.2.

Description	Symbol	Min	Тур	Max	Unit	Notes
Unit Interval for high bit rate (2.7Gbps/lane)	UI_High_Rate	-	370	-	ps	Range is nominal ± 350 ppm. Display Port Link Rx does not
Unit Interval for reduced bit rate (1.62Gbps/lane)	UI_Low_Rate	-	617	-	ps	require local crystal for link clock generation
Lane-to-Lane skew	V Rx-SKEW- INTER_PAIR	-	-	5700	ps	-
	V Rx-SKEW-	-	-	60	ps	For high bit rate
Lane intra-pair skew	INTRA_PAIR	-	-	260	ps	For reduced bit rate

3-4. Signal Timing Specifications

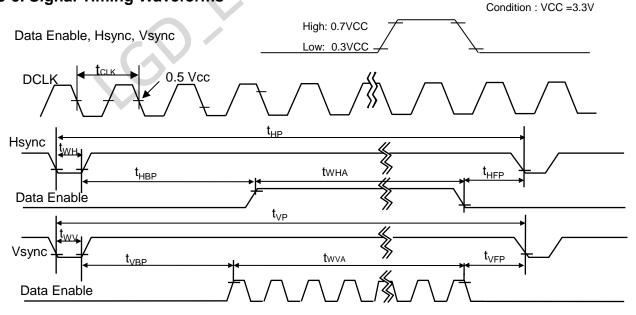
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of eDP Tx/Rx for its proper operation.

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	137.5	138.1	139	MHz	
	Period	tw _{HA}	2048	2052	2060		
Hsync	Width	t _{HP}	30	32	34	tCLK	
	Active	t _{wH}		1920			
	Period	tw _{vA}	1118	1122	1124		
Vsync	Width	t _{vP}	6	6	6	tHP	
	Active	t _{wv}		1080			
	Horizontal back porch	t _{HBP}	50	52	56		
Data Enable	Horizontal front porch	t _{HFP}	46	48	50	tCLK	
	Vertical back porch	t _{vBP}	30	33	34		
	Vertical front porch	t _{vFP}	2	3	4	tHP	

Table 4. TIMING TABLE

Appendix) all reliabilities are specified for timing specification based on refresh rate of 60Hz. However, LD116WF1 has a good actual performance even at lower refresh rate (e.g. 40Hz or 50Hz) for power saving mode, whereas LD116WF1 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz, 40Hz at Power save mode. Don't care Flicker level (power save mode).

3-5. Signal Timing Waveforms



3-6. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

									Inp	out Co	olor D	ata							
	Color			R	Ð					GRE	EEN					BL	UE		
		MSE						MSE					LSB						LSB
	İ	R 5		R 3	R 2	R 1	R 0		G 4	G 3		G 1	G 0			В3	B 2		
	Black	0	0		0	0	0	0 		0	0	0	0	0	0	0	0	0	0
	Red	1	1	1 	1 	1 1	1 1	0 	0	0	0	0		0	0	0	0	0	0
	Green	0	0		0	0	0	1 	1 	1 	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	. 0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED										·····							 		
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					•••••						• • • • • • • •						••••• 		
	GREEN (62)	0	0	0	0	0	0	 1	1	 1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	 1	 1	 1		 1	1	0	0	0	0	 0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0		0	 0	0	0	0	0	0	 0	0	0	0	 0	1
BLUE	·····			•••••						· · · · ·	 					· · · · · ·	 		
	BLUE (62)	0	0	0	0	0	0	 0	0	0	0	0	0	 1	1	1	 1	 1	0
	BLUE (63)	0	0	0	0	0	0	 0	0	0	0	0	0	1	1	1		1	1

Table 5. COLOR DATA REFERENCE



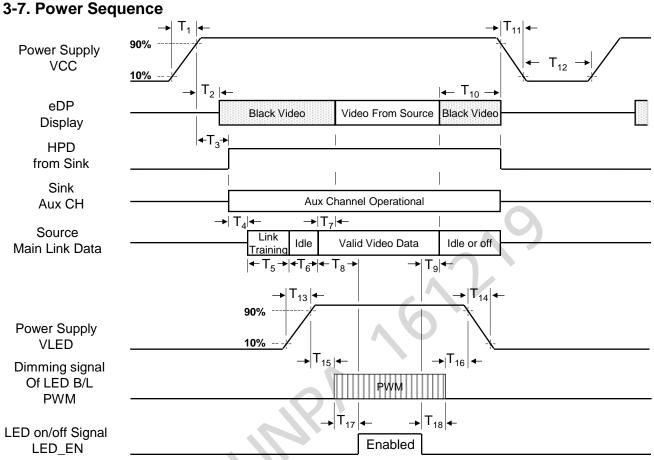


 Table 6. POWER SEQUENCE TABLE

Timing	Required	Lin	nits	Units	Notes
Timing	By	Min	Max	Units	Notes
T ₁	Source	0.5	10	ms	-
T ₂	Sink	0	200	ms	-
T ₃	Sink	0	200	ms	-
T ₄	Source	-	-	ms	-
T ₅	Source	-	-	ms	-
T ₆	Source	-	-	ms	-
T ₇	Sink	0	50	ms	-
T ₈	T ₈ Source		-	ms	LGD recommend Min 200ms
T ₉	Source	-	-	ms	-

Timing	Required	Lin	nits	Units	Notes
Timing	By	Min	Min Max		Notes
T ₁₀	Source	0	500	ms	-
T ₁₁	Source	-	10	ms	-
T ₁₂	Source	500	-	ms	
T ₁₃	Source	0.5	10	ms	-
T ₁₄	Source	0.5	10	ms	-
T ₁₅	Source	10	-	ms	-
T ₁₆	Source	10	-	ms	-
T ₁₇	Source	0	-	ms	-
T ₁₈	Source	0	-	ms	-

Note) 1. Do not insert the mating cable when system turn on.

2. Valid Data have to meet "3-3. eDP Signal Timing Specifications"

3. Video Signal, LED_EN and PWM need to be on pull-down condition on invalid status.

4. LGD recommend the rising sequence of VLED after the Vcc and valid status of Video Signal turn on.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

Optical Stage(x,y)

FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 6. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 69MHz

Deveneter	Ourse had		Values		Linita	Natas
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	500	800	-		1
Surface Luminance, white	L _{WH}	340	400	-	cd/m ²	2
Luminance Variation	\$	-	1.2	1.4		Reference (5P)
	δ_{WHITE}	-	1.4	1.6		3 (13P)
Response Time	Tr _R + Tr _D	-	25	30	ms	4
Color Coordinates						
RED	RX	0.568	0.598	0.628		
	RY	0.322	0.352	0.382		
GREEN	GX	0.307	0.337	0.367		
	GY	0.539	0.569	0.599		
BLUE	BX	0.124	0.154	0.184		
	BY	0.090	0.120	0.150		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right($\Phi=0^{\circ}$)	Θr	-	85	-	degree	
x axis, left (Φ=180°)	ΘΙ	-	85	-	degree	
y axis, up (Φ =90°)	Θu	-	85		degree	
y axis, down (Φ =270°)	Θd	-	85		degree	
Gray Scale						6



Note)

1. Contrast Ratio (CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

Surface Luminance is measured at the center point of active area.

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

 $LWH = Average(L1, L2, \dots L5)$

3. The variation in surface luminance , The panel total variation (δ WHITE) is determined by measuring LN at each test position 1 through 13 and then defined as following numerical formula. For more information see FIG 2.

 δ WHITE = Maximum(L1,L2, ... L13) / Minimum(L1,L2, ... L13)

- 4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white (Decay Time, TrD). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray scale specification

Gray Level	Luminance [%] (Typ)
LO	0.1
L7	0.67
L15	4.33
L23	11
L31	20.2
L39	35.1
L47	53.2
L55	74.9
L63	100

FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>

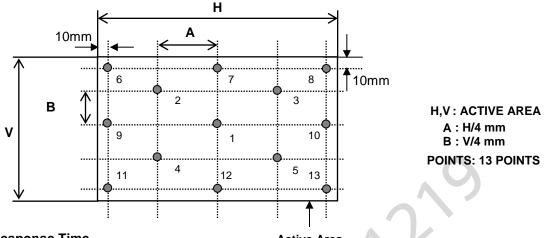
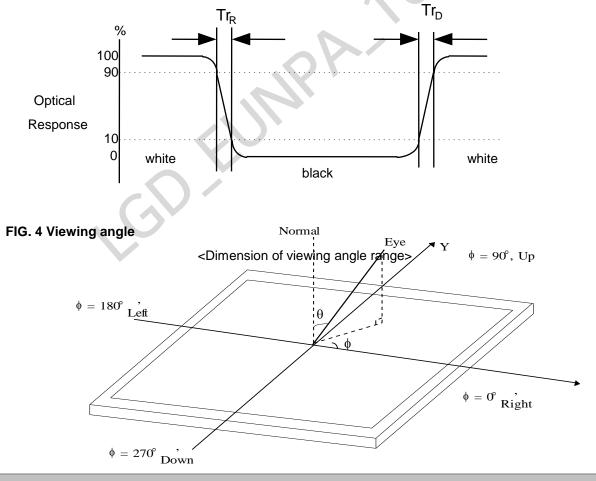


FIG. 3 Response Time

Active Area

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP140WF1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

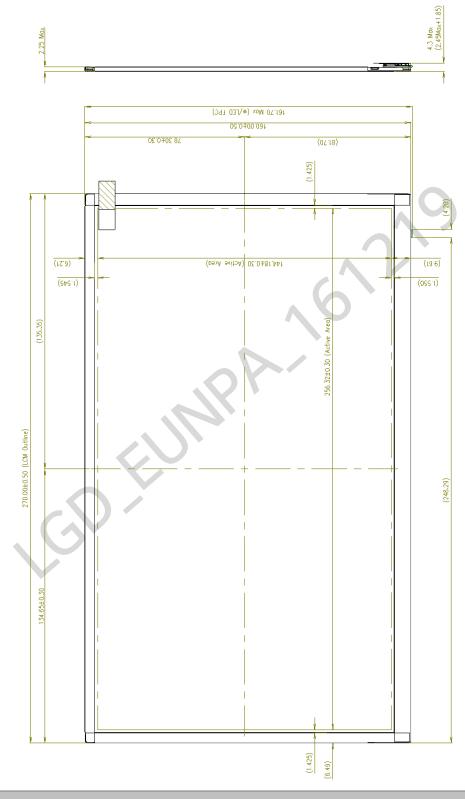
	Horizontal	270.00 ± 0.5mm
Outline Dimension	Vertical	$160.00\pm0.5\text{mm}$
	Thickness	2.25mm (max.) / 4.3mm (max) With PCB
	Horizontal	256.32 ± 0.3mm
Active Display Area	Vertical	$144.18\pm0.3\text{mm}$
Weight	147 g (Max.) / 137 g (Typ.)	
Surface Treatment	Glare treatment of the fro	ont Polarizer

GDEUNIPA



<FRONT VIEW>

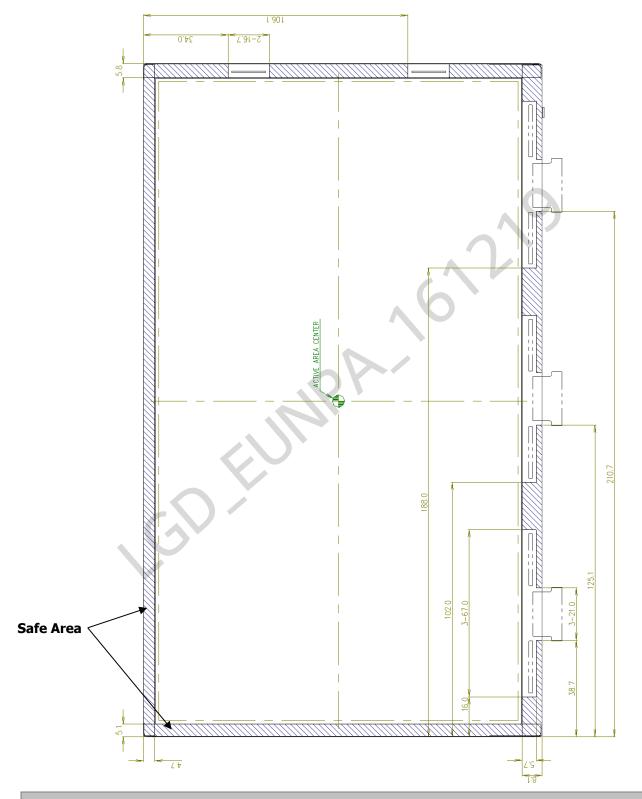
Note) Unit:[mm], General tolerance: \pm 0.5mm



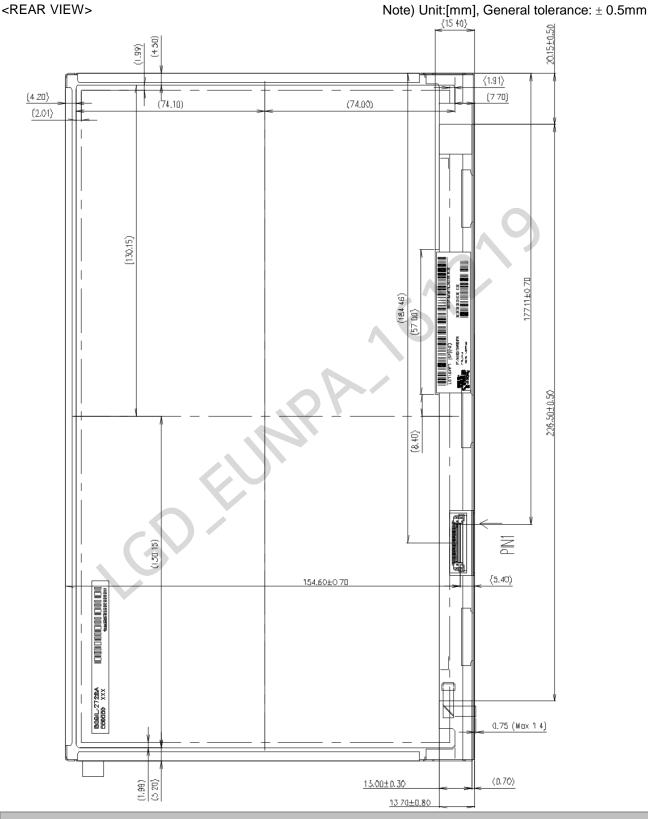


<FRONT VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



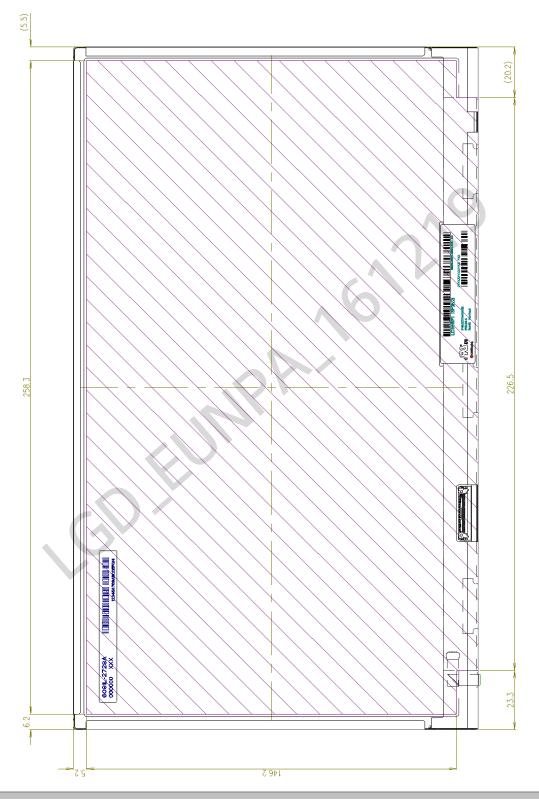






<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





6. Reliability

Environment test condition

No.	Test Item	Conditions	Note
1	High temperature storage test	Ta= 60°C, 240h	1,2,3
2	Low temperature storage test	Ta= -20°C, 240h	1,2,3
3	High temperature operation test	Ta= 50°C, 50%RH, 240h	1,2,3
4	Low temperature operation test	Ta= 0°C, 240h	1,2,3
5	Vibration test (non-operating)	Random, 1.0Grms, X,Y,Z Direction Test time : each direction 1hour	
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces	
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr	

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

[Note 1] Ta = Ambient Temperature

[Note 2] After reliability test is finished, Confirm performance after leaving in room temp.

[Note 3] In the standard condition, there shall be no practical problems that may affect the display function 24 hours later after reliability test. After the reliability test is finished, we can guarantee the product only when the corrosion is causing its malfunction. The corrosion causing no functional defect can not be guaranteed.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc. Information Technology Equipment - Safety - Part 1 : General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1 : General Requirements.

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH) E : MONTH D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	Е	F	G	H	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

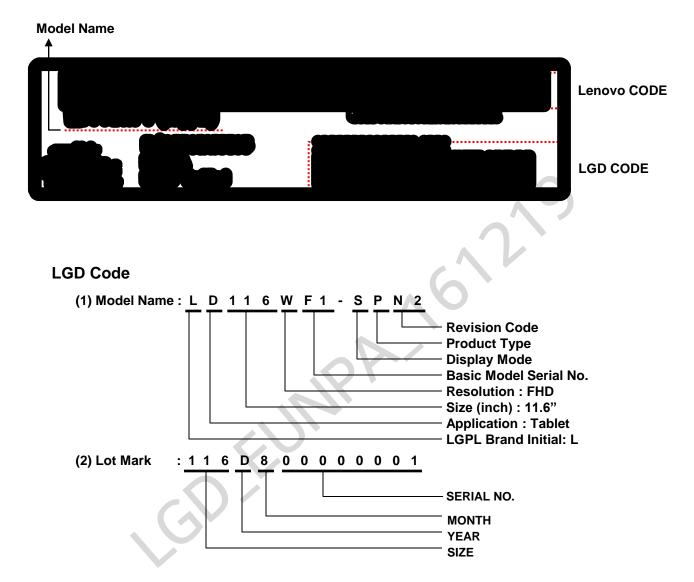
Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box :30ea
- b) Box Size : 468 \times 355 \times 226 mm



8-3. Label Description



Lenovo Code

1)P/N : SD10A09815 2)FRU : N/A



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for here hand or greased sloth (Some cosmetics are detrimental).

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) When handling the LCD module, it needs to handle with care not to give mechanical stress to the PCB and Mounting Hole area."

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.

Please carefully peel off the protection film without rubbing it against the polarizer.

- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 1/3

	Byte	Byte	Field Name and Comments	Value	Value
	(Dec)	(Hex)		(Hex)	(Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	11111111
er	2	02	Header Header	FF FF	11111111
Header	4	03	Header	FF	11111111
He	5	04	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	ID Manufacture Name LGD	30	00110000
	9	09	ID Manufacture Name	E4	11100100
3	10	0A	ID Product Code 0461h	61	01100001
Vendor / Product EDID Version	11	0B	(Hex. LSB first)	04	00000100
roc	12	0C	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
/P	13 14	0D 0E	ID Serial No Optional ("00h" If not used, Number Only and LSB First) ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00 00	00000000
E Q	15	0E 0F	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
DI DI	16	10	Week of Manufacture - Optinal 00 weeks	00	00000000
Ve E	17	11	Year of Manufacture 2014 years	18	00011000
	18	12	EDID structure version $\# = 1$	01	00000001
	19	13	EDID revision # = 4	04	00000100
			Video input Definition = Input is a Digital Video signal Interface, Colo Bit Depth : 8 Bits per Primary Color,		
	20	14	Digital Video Interface Standard Supported: DisplayPort is supported	A5	10100101
2	21	15	Horizontal Screen Size (Rounded cm) = 26 cm	1A	00011010
ay ete	22	16	Vertical Screen Size (Rounded cm) = 14 cm	0E	00001110
spl s	23	17	Display Transfer Characteristic (Gamma) = (gamma*100)-100 = Example:(2.2*100)-100=120	78	01111000
Display Parameters			Feature Support [Display Power Management(DPM) : Standby Mode is not supported, Suspend Mode is not		
Р	24	18	supported, Active Off = Very Low Power is not supported ,Supported Color Encoding Formats : RGB 4:4:4 &	0 A	00001010
	24	10	YCrCb 4:4:4 ,Other Feature Support Flags : No_sRGB, Preferred Timing Mode, No_Display is continuous	VA	00001010
	25	10	frequency (Multi-mode_Base EDID and Extension Block).]	07	00000111
	25 26	19 1A	Red/Green Low Bits (RxRy/GxGy) Blue/White Low Bits (BxBy/WxWy)	07 B5	10110101
	20	1B	Red X $Rx = 0.598$	<u>99</u>	10011001
SS SS	27	1B 1C	Red Y $Ry = 0.352$	5A	01011010
Panel Color Coordinates	29	10 1D	Green X Gx = 0.337	5A	01011010
l C din	30	1D 1E	Green Y Gy = 0.569	<u> </u>	10010001
ne		1E 1F		27	00100111
Pa Co	31		Blue X $Bx = 0.154$		00100111
	32	20	Blue Y $By = 0.120$	1E	
	33	21	White X $Wx = 0.313$	50	01010000
	34	22	White Y $Wy = 0.329$	54	01010100
ed	35	23	Established timing 1 (Optional_00h if not used)	00	00000000
ish ng					
Established Timings	36	24	Established timing 2 (Optional_00h if not used)	00	00000000
Sta	37	25	Manufacturer's timings (Optional_00h if not used)	00	00000000
E					
	38	26	Standard timing ID1 (Optional_01h if not used)	01	00000001
	39 40	27	Standard timing ID1 (Optional_01h if not used) Standard timing ID2 (Optional_01h if not used)	01	00000001
	40	28 29	Standard timing ID2 (Optional_01h if not used) Standard timing ID2 (Optional_01h if not used)	01 01	00000001 00000001
8	41	29 2A	Standard timing ID2 (Optional_01h if not used) Standard timing ID3 (Optional_01h if not used)	01	00000001
8	43	2A 2B	Standard timing ID3 (Optional_01h if not used)	01	00000001
Standard Timing ID	44	2C	Standard timing ID4 (Optional_01h if not used)	01	00000001
Tin	45	2D	Standard timing ID4 (Optional_01h if not used)	01	00000001
ŗ p	46	2E	Standard timing ID5 (Optional_01h if not used)	01	00000001
lar	47	2F	Standard timing ID5 (Optional_01h if not used)	01	00000001
m	48	30	Standard timing ID6 (Optional_01h if not used)	01	00000001
Sto	49	31	Standard timing ID6 (Optional_01h if not used)	01	00000001
	50 51	32 33	Standard timing ID7 (Optional_01h if not used) Standard timing ID7 (Optional_01h if not used)	01 01	00000001 00000001
	52	33	Standard timing ID7 (Optional_01h if not used) Standard timing ID8 (Optional_01h if not used)	01	00000001
	53	35	Standard timing ID8 (Optional_01h if not used)	01	00000001
			6 · (· F · · · · · · · · · · · · · · · ·	V1	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 2/3

	Byte	Byte	Field Name and Comments			
	(Dec)	(Hex)		(Hex)	(Bin)	
Timing Descriptor #1	54	36	Pixel Clock/10,000 (LSB) 138.1 MHz @ 60 Hz	F4	11110100	
	55	37	Pixel Clock/10,000 (MSB)	35	00110101	
	56	38	Horizontal Active (HA) (lower 8 bits) 1920 pixels	80	10000000	
	57	39	Horizontal Blanking (HB) (lower 8 bits) 132 pixels	84	10000100	
	58	3A	Horizontal Active (HA) / Horizontal Blanking (HB) (upper 4:4bits)	70	01110000	
	59	3B	Vertical Avtive (VA) 1080 lines	38	00111000	
	60	3C	Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 42 lines	2A	00101010	
	61	3D	Vertical Active (VA) / Vertical Blanking (VB) (upper 4:4bits)	40	01000000	
	62 63	3E	Horizontal Front Porch in pixels (HF) (lower 8 bits) 48 pixels Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32 pixels	30 20	00110000	
		3F		20 36	00100000	
	64 65	40 41	Vertical Front Porch in lines (VF) : Vertical Sync Pluse Width in lines (VS) (lower 4 bits) 3 lines : 6 lines Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)		00110110	
imi				00		
T	66	42		00	00000000 10010000	
	67 68	43 44	Vertical Vedio Image Size (mm) (lower 8 bits) 144 mm	90 10	00010000	
	69		Horizontal Image Size / Vertical Image Size (upper 4 bits) Horizontal Border = 0 (Zero for Notebook LCD)		00000000	
	70	45 46	Vertical Border = 0 (Zero for Notebook LCD)	00 00	00000000	
	70	40		1B	00000000	
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)] Flag	IB 00	00000000	
	72	40	Flag	00	00000000	
	74	4A	Flag	00	00000000	
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000	
	76	4C	Flag	00	00000000	
2	77	4D	Descriptor Defined by manufacturer	00	00000000	
r #	78	4E	Descriptor Defined by manufacturer	00	00000000	
oto	79	4F	Descriptor Defined by manufacturer	00	00000000	
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000	
	81	51	Descriptor Defined by manufacturer	00	00000000	
	82	52	Descriptor Defined by manufacturer	00	00000000	
	83	53	Descriptor Defined by manufacturer	00	00000000	
î.	84	54	Descriptor Defined by manufacturer	00	00000000	
L	85	55	Descriptor Defined by manufacturer	00	00000000	
	86	56	Descriptor Defined by manufacturer	00	00000000	
	87	57	Descriptor Defined by manufacturer	00	00000000	
	88	58	Descriptor Defined by manufacturer	00	00000000	
	89	59	Descriptor Defined by manufacturer	00	00000000	
	90	5A	Flag	00	00000000	
	91	5B	Flag	00	00000000	
	92	5C	Flag	00	00000000	
	93	5D	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110	
	94	5E	Flag	00	00000000	
#3	95	5F	Alphanumeric Data String (ASCII String) L	4 C	01001100	
Timing Descriptor #3	96	60	Alphanumeric Data String (ASCII String) G	47	01000111	
	97	61	Alphanumeric Data String (ASCII String)	20	00100000	
SCL	98	62	Alphanumeric Data String (ASCII String) D	44	01000100	
Sec	99	63	Alphanumeric Data String (ASCII String) i	69	01101001	
50	100	64	Alphanumeric Data String (ASCII String) s	73	01110011	
Timin	101	65	Alphanumeric Data String (ASCII String) p	70	01110000	
	102	66	Alphanumeric Data String (ASCII String)	6C	01101100	
	103	67	Alphanumeric Data String (ASCII String) a	61	01100001	
	104	68	Alphanumeric Data String (ASCII String) y	79	01111001	
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC Π code 0Ah,set remaining char = 20h)	0 A	00001010	
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	20	00100000	
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC Π code 0Ah,set remaining char = 20h)	20	00100000	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 3/3

	Byte	Byte	Field Name and Comments		Value (Hex)	Value
	(Dec)	(Hex)				(Bin)
	108	6C	Flag		00	00000000
	109	6D	Flag		00	00000000
	110	6E	Flag		00	00000000
	111	6F	Data Type Tag (Alphanumeric Data String (ASCII String))		FE	11111110
	112	70	Flag		00	00000000
#4	113	71	Alphanumeric Data String (ASCII String)	L	4 C	01001100
Timing Descriptor #4	114	72	Alphanumeric Data String (ASCII String)	D	44	01000100
	115	73	Alphanumeric Data String (ASCII String)	1	31	00110001
	116	74	Alphanumeric Data String (ASCII String)	1	31	00110001
	117	75	Alphanumeric Data String (ASCII String)	6	36	00110110
	118	76	Alphanumeric Data String (ASCII String)	W	57	01010111
	119	77	Alphanumeric Data String (ASCII String)	F	46	01000110
	120	78	Alphanumeric Data String (ASCII String)	1	31	00110001
	121	79	Alphanumeric Data String (ASCII String)	-	2D	00101101
	122	7A	Alphanumeric Data String (ASCII String)	S	53	01010011
	123	7B	Alphanumeric Data String (ASCII String)	Р	50	01010000
	124	7C	Alphanumeric Data String (ASCII String)	N	4E	01001110
	125	7D	Alphanumeric Data String (ASCII String)	2	32	00110010
Checksum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)		00	00000000
	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)		FD	11111101