

SPECIFICATION FOR APPROVAL

() Pre	liminary	y Spec	cification
١	,		,	

(•) Final Specification

Title	10.1" HD TFT LCD
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Customer	DELL
MODEL	

SUPPLIER	LG Display Co., Ltd.	
*MODEL	LP101WH1	
Suffix	TLA3	

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
Please return 1 copy for you	r confirmation with

your signature and comments.

APPROVED BY	SIGNATURE
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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Aug. 13. 2009	All	First Draft (Preliminary Specification)	-
0.1	Oct.30.2009	13	Color Coordinates is changed.	-
1.0	Nov. 5.2009	all	Final	
[

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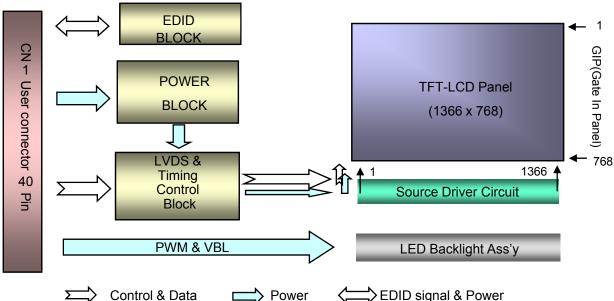


1. General Description

The LP101WH1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 10.1inches diagonally measured active display area with HD resolution(1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP101WH1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP101WH1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP101WH1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	10.1 inches diagonal	
Outline Dimension	235(H) × 143(V) × .5.2(D,Max.) [mm]	
Pixel Pitch	0.16305mm × 0.16305 mm	
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement	
Color Depth	6-bit, 262,144 colors	
Luminance, White	250 cd/m ² (Typ.5 point)	
Power Consumption	Total 3.46 Watt(Typ.) @ LCM circuit 1.16 Watt(Typ.), B/L input 2.3 Watt(T (W/O LED Driver)	yp.)
Weight	200g (Max.)	
Display Operating Mode	Transmissive mode, normally white	
Surface Treatment	Glare treatment of the front polarizer	
RoHS Comply	Yes	
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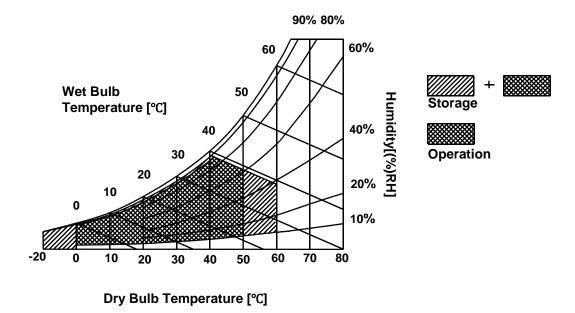
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Office		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

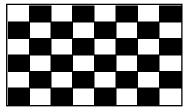
The LP101WH1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Cymhol	Values			Unit	Notes	
Parameter	Symbol	Min	Тур	Max	Offic	notes	
MODULE :							
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V_{DC}		
Power Supply Input Current	I _{cc}	-	350	402	mA	1	
Power Consumption	Pc	-	1.16	1.33	Watt	1	
Differential Impedance	Zm	90	100	110	Ohm	2	
LED Backlight (With LED Driver):							
LED Driver (@12V)	P_{DRIVER}	-	0.21	0.25	Watt		
Operating Voltage	V_{LED}	-	28.85	32.4	V		
Operating Current per string	I _{LED}	-	20		mA	3	
Power Consumption	P _{BL}	-	2.3	2.592	Watt	4	
Life Time		10,000			Hrs	5	

Note)

1. The specified current and power consumption are under the Vcc = 3.3V , 25°C, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics. I_{LED} is the current of each LED's string, LED backlight has 4 strings on it.
- 4. The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
- 5. The life time is determined as the time at which brightness of LED is 50% compare to that of initial value at the typical LED current.

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3-2. Interface Connection

This LCD employs one interface connection, a 40 pin connector is used for the module electronics interface.

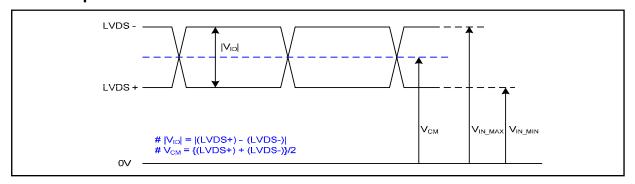
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Din	Cumbal	Description	Notes
Pin	Symbol	Description	Notes
12	CT1/NC	Connector Test/No Connection(Reserved)	
	VDD	+3.3V Power Supply	
3	VDD	+3.3V Power Supply	
4 <u>-</u>	V _{EDID}	+3.3V EDID Power	1, Interface chips
5	Test	Panel Self Test	1.1 LCD : SiW, 1port including LVDS Receiver
6 <u>-</u>	CLK _{EDID}	EDID Clock Input	1.2 System :
7	DATA _{EDID}	EDID Data Input	* Pin to Pin compatible with LVDS
8	RxIN0-	LVDS differential data input	2. Connector
9	RxIN0+	LVDS differential data input	2.1 LCD :I-PEX 20455-040E-0*
10	GND	Ground	(Locking type)
11	RxIN1-	LVDS differential data input	or equivalent
12	RxIN1+	LVDS differential data input	2.2 Mating :
13	GND	Ground	2.3 Connector pin arrangement
14	RxIN2-	LVDS differential data input	40 1
15	RxIN2+	LVDS differential data input	П ПП П
16	GND	Ground	
17	RxCLKIN-	LVDS differential clock input	
18	RxCLKIN+	LVDS differential clock input	[LCD Module Rear View]
19	GND	Ground	, , , , , , , , , , , , , , , , , , , ,
20	NC	No Connection	
21	NC	No Connection	
22	GND	Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	CT2/NC	Connector Test/No Connection(Reserved)	
35	S_PWMIN	System PWM signal input(+3.3V swing)	
36	BL_ON	LED Enable(3.3V Input) [Note 1]	
37	NC	No Connection	
38	VLED	7~21V LED Power Supply	
39	VLED	7~21V LED Power Supply	[Note 1]
40	VLED	7~21V LED Power Supply	On: 2.0V↑,Off:0~0.4V



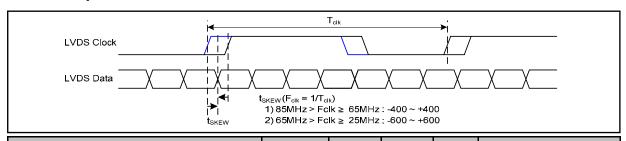
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



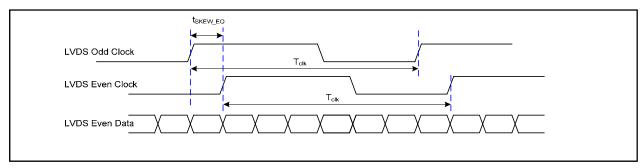
Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	$ V_{ID} $	100	600	mV	-
LVDS Common mode Voltage	V_{CM}	0.6	1.8	٧	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

3-3-2. AC Specification

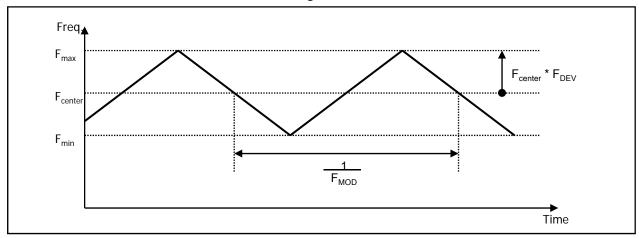


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-





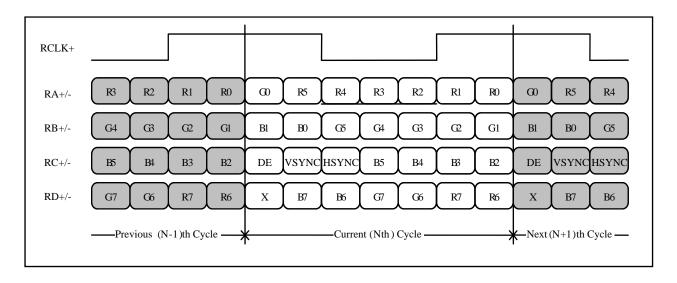
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

- LVDS 1 Port



< LVDS Data Format >

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Condition: VCC =3.3V

Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. TIMING TABLE

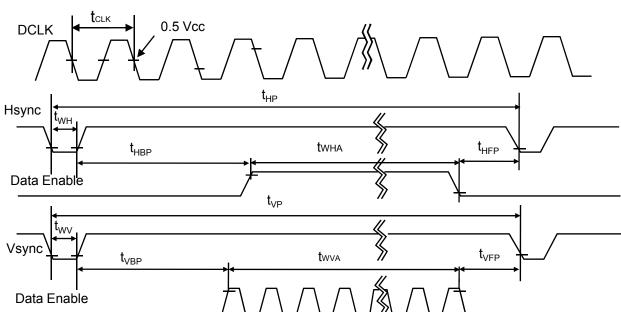
ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency f _{CLK}		-	72.3	-	MHz	
	Period	Thp	1470	1526	1586		
Hsync	Width	t_{WH}	23	32	40	tCLK	
	Width-Active	t _{WHA}	1366 1366		1366		
	Period	t _{VP}	779	790	801		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	72	80	124	+C1 V	
Data	Horizontal front porch	t _{HFP}	8	48	48	tCLK	
Enable	Vertical back porch	t_{VBP}	8	14	20	tHP	
	Vertical front porch	t _{VFP}	1	3	5	IMP	



Data Enable, Hsync, Vsync

Low: 0.3VCC

Low: 0.5VCC



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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

								_	Inp	out Co	olor D	ata							
	Color			RE	ΞD					GRE	EEN					BL	UE		
		MSE					LSB						LSB	MSE					LSB
	1	R 5	R 4	R 3	R 2	R 1		G 5		G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	1	0	0				0	0 	0		0	0		0	0			0	0
	Red	1	1	1		1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0			0	0	1 		. 1 		1	1	0	0		0	0	0
Basic	Blue	0	0			0	0	0	0	0	0	0	0	1 	1		. 1 		1
Color	Cyan	0	0			0	0	1 	1				1	1	1	.1			
	Magenta	1	1	.1	. 1	1		0	0	0	0	0	0	1	1	.1	1		1
	Yellow	1	1	1	. 1	1		1	1	1	1	1		0	0	0	0	0	0
W	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED					· · · · · ·												·····		
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					 												 		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					 												 		
	BLUE (62)	0	0	0	0		0	 0	0	0	 0	0	0	1	1	1			0
	BLUE (63)	0	0	0	0		0	0	0	0	 0	0	0	1	1	1		1	1



3-7. Power Sequence

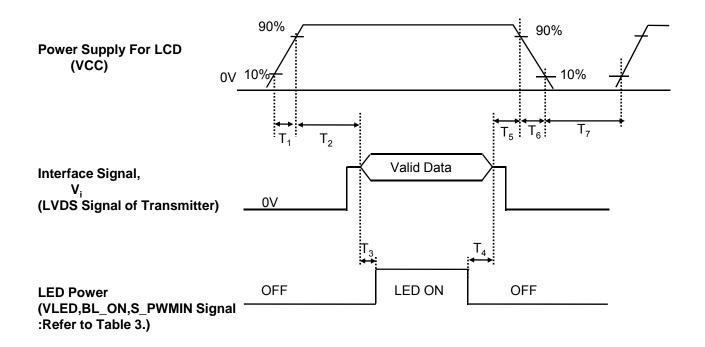


Table 7. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	3	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

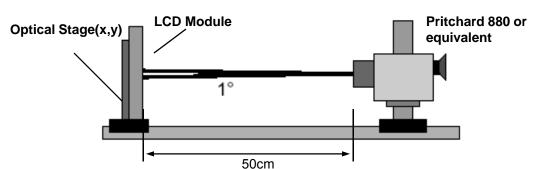


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 72.3MHz, I_{BL} = 20 mA

Darameter	Cymbal		Values		Units	Notes
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	500	-	-		1
Surface Luminance, white	L _{WH}	230	250	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	Tr_{R} + Tr_{D}	-	16	25	ms	4
Color Coordinates]		
RED	RX	0.568	0.598	0.628		
	RY	0.312	0.342	0.372		
GREEN	GX	0.281	0.311	0.341		
	GY	0.569	0.599	0.629		
BLUE	BX	0.123	0.153	0.183		
	BY	0.089	0.119	0.149		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	30	-		degree	
x axis, left (Φ=180°)	Θl	30	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	20	-	[.	degree	
Gray Scale			2.2			6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, ... L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}$$

- Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_{V} = 60$$
Hz

Gray Level	Luminance [%] (Typ)
LO	0.24
L7	0.74
L15	2.57
L23	6.75
L31	15.48
L39	32.89
L47	56.63
L55	76.95
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

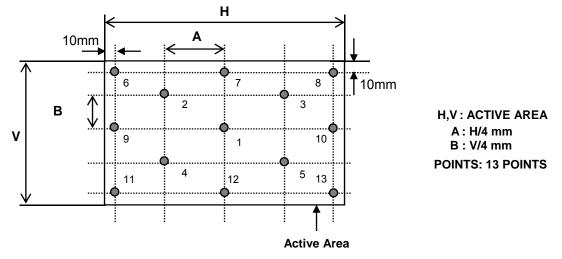


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

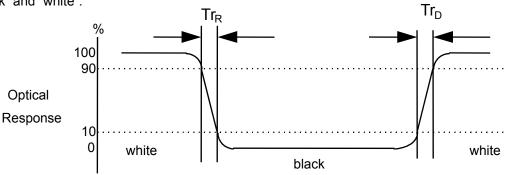
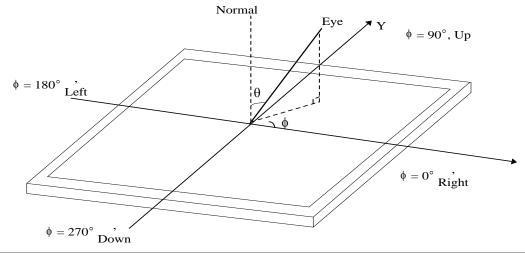


FIG. 4 Viewing angle



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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP101WH1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

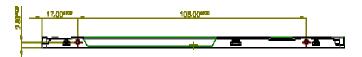
	Horizontal	235.0 ± 0.5 mm				
Outline Dimension	Vertical	143.0 ± 0.5 mm				
	Thickness	5.2mm (max)				
Bezel Area	Horizontal	226.00 mm				
bezei Alea	Vertical	128.70 mm				
Active Display Area	Horizontal	222.73 mm				
Active Display Area	Vertical	125.22 mm				
Weight	200g (Max.)					
Surface Treatment	Glare treatment of the front polarizer					

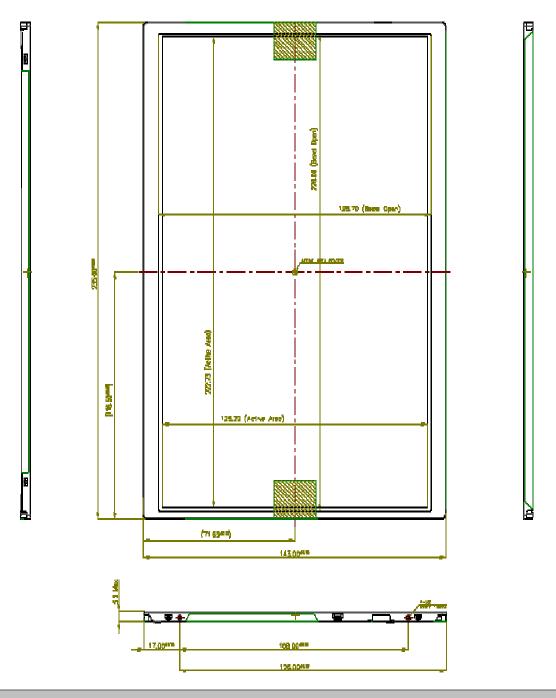
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<FRONT VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm

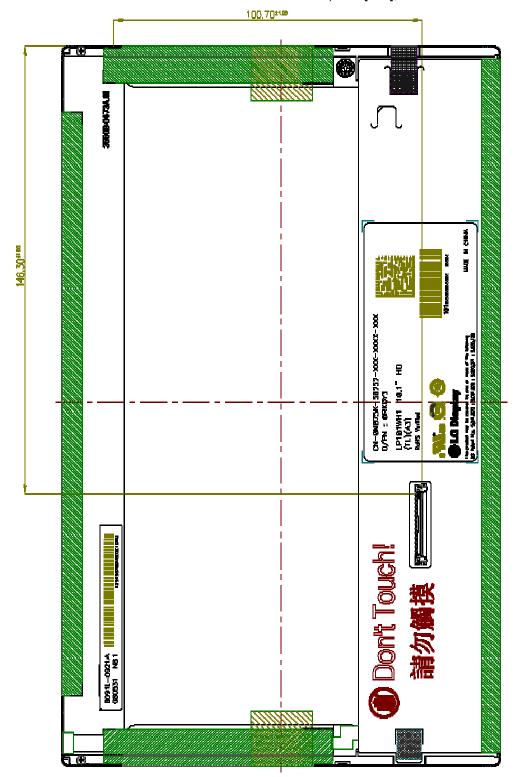






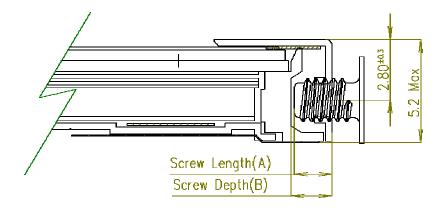
<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



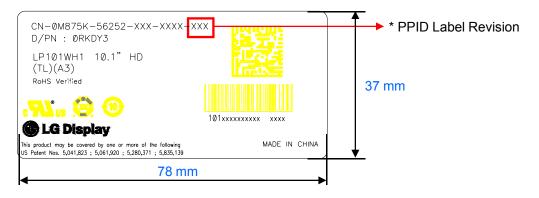
- * Mounting Screw Length (A) = 1.5(Min) /1.8(Max)
- * Mounting Screw Hole Depth (B) = 1.8(Min)
- * Mounting hole location : 2.8(typ.)
- * Torque : 2.0 kgf.cm(Max)

(Measurement gauge: torque meter)

Section A-A

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

[DETAIL INFORMATION OF PPID LABEL AND REVISION CODE]



* PPID Label Revision:

It is subject to change with Dell event. Please refer to the below table for detail.

Classification	No Change	1st Revision	2nd Revision	•••	9th Revision	
SST(WS)	X00	X01	X02	•••	A09	•••
PT(ES)	X10	X11	X12	•••	A19	
ST(CS)	X20	X21	X22	•••	A29	
XB(MP)	A00	A01	A02	•••	A09	

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6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Random, 1.0Grms, X,Y,Z Direction Test time: each direction 1hour					
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 40 pcs

b) Box Size : 395mm × 390mm × 309mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the structure of t
 - module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	11111111
	2	02	Header	FF	11111111
qe	3	03	Header	FF	11111111
Header	4	04	Header	FF	11111111
I	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	ID Manufacture Name LGD	30	00110000
	9	09	ID Manufacture Name	E4	11100100
	10	0A	ID Product Code 0269h	69	01101001
uct	11	0В	(Hex. LSB first)	02	00000010
p_{α}	12	0C	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
'n	13	0D	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
Jr./	14	0E	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
Vendor / Product	15	0F	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000
Ve	16	10	Week of Manufacture - Optinal 00 weeks	00	00000000
	17	11	Year of Manufacture 2009 years	13	00010011
	18	12	EDID structure version # = 1	01	00000001
	19	13	EDID revision # = 4	04	00000100
	20	14	Video input Definition = Input is a Digital Video signal Interface, Colo Bit Depth: 6 Bits per Primary Color, Digital Video Interface Standard Supported: Digital Interface is not defined	90	10010000
	21	15	Horizontal Screen Size (Rounded cm) = 22 cm	16	00010110
â	22	16	Vertical Screen Size (Rounded cm) = 13 cm	0D	00001101
Display	23	17	Display Transfer Characteristic (Gamma) = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
D	24	18	Feature Support [Display Power Management(DPM) : Standby Mode is not supported, Suspend Mode is not supported, Active Off = Very Low Power is not supported ,Supported Color Encoding Formats : RGB 4:4:4 & YCrCb 4:4:4 ,Other Feature Support Flags : No_sRGB, Preferred Timing Mode, No_Display is continuous frequency (Multi-mode_Base EDID and Extension Block).]	0A	00001010
	25	19	Red/Green Low Bits (RxRy/GxGy)	BF	10111111
4.	26	1A	Blue/White Low Bits (BxBy/WxWy)	45	01000101
luci	27	1B	Red X Rx	95	10010101
pa	28	1C	Red Y Ry	58	01011000
/P	29	1D	Green X Gx	52	01010010
or.	30	1E	Green Y Gy	8A	10001010
Vendor / Product	31	1F	Blue X Bx	28	00101000
Z	32	20	Blue Y By White X $Wx = 0.313$	25 50	00100101 01010000
	34	22	White Y Wy = 0.329	54	01010000
t t	35	23	Established timing 1 (Optional_00h if not used)	00	00000000
Establ ished	36	24	Established timing 2 (Optional_00h if not used)	00	00000000
Es is.	37	25	Manufacturer's timings (Optional_00h if not used)	00	00000000
	38	26	Standard timing ID1 (Optional_01h if not used)	01	00000001
	39	27	Standard timing ID1 (Optional_01h if not used)	01	00000001
	40	28	Standard timing ID2 (Optional_01h if not used)	01	00000001
	41	29	Standard timing ID2 (Optional_01h if not used)	01	00000001
E C	42	2A	Standard timing ID3 (Optional_01h if not used)	01	00000001
gu.	43	2B 2C	Standard timing ID3 (Optional_01h if not used) Standard timing ID4 (Optional_01h if not used)	01	00000001
īm	45	2D	Standard timing ID4 (Optional_01h if not used) Standard timing ID4 (Optional_01h if not used)	01 01	00000001
Standard Timing ID	46	2E	Standard timing ID5 (Optional_01h if not used)	01	00000001
tan	47	2F	Standard timing ID5 (Optional_01h if not used)	01	00000001
ж	48	30	Standard timing ID6 (Optional_01h if not used)	01	00000001
×	49	31	Standard timing ID6 (Optional_01h if not used)	01	00000001
	50	32	Standard timing ID7 (Optional_01h if not used)	01	00000001
	51	33	Standard timing ID7 (Optional_01h if not used)	01	00000001
	52	34	Standard timing ID8 (Optional_01h if not used)	01	00000001
	53	35	Standard timing ID8 (Optional_01h if not used)	01	00000001



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	PIXEI CIOCK/10,000 (LSB) /1 MHZ @ 60Hz	BC	10111100
	55	37	Pixel Clock/10,000 (MSB)	1B	00011011
	56	38	Horizontal Active (HA) (lower 8 bits) 1366	56	01010110
	57	39	Horizontal Blanking (HB) (lower 8 bits) 132	84	10000100
	58	3A	Horizontal Active / Horizontal Blanking(HA HB) (upper 4:4bits)	50	01010000
	59	3В	Vertical Avtive (VA) /68	00	00000000
<i>I#</i>	60	3C	Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 22	16	00010110
ä	61	3D	Vertical Active / Vertical Blanking (VA VB) (upper 4:4bits)	30	00110000
ήr	62	3E	Horizontal Front Porch in pixels (HF) (lower 8 bits) 48 Pixels	30	00110000
282	63	3F	Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32	20	00100000
βD	64	40	Vertical Front Porch in lines (VF) (lower 4 bits): Vertical Sync Pluse Width in lines (VS) (lower 4 bits) 3 Lines: 5 Lines	35	00110101
Timing Descriptor #1	65	41	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
7	66	42	Horizontal Vedio Image Size (mm) (lower 8 bits) 223	DF	11011111
	67	43	Vertical Vedio Image Size (mm) (lower 8 bits) 125	7D	01111101
	68	44	Horizontal Image Size / Vertical Image Size (upper 4 bits)	00	00000000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)]	1B	00011011
	72	48	Pixel Clock/10,000 (LSB) 71 MHz @ 60Hz	BC	10111100
	73	49	Pixel Clock/10,000 (MSB)	1B	00011011
	74	4A	Horizontal Active (HA) (lower 8 bits) 1366	56	01010110
	75	4B	Pixels Horizontal Blanking (HB) (lower 8 bits) 132 Pixels	84	10000100
	76	4C	Horizontal Active / Horizontal Blanking(HA HB) (upper 4:4bits)	50	01010000
	77	4D	Vertical Avtive (VA) 768	00	00000000
\$3		4E	Lines Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 22		
a.	78		Lines	16	00010110
ript	79	4F	Vertical Active / Vertical Blanking (VA VB) (upper 4:4bits) Horizontal Front Porch in pixels (HF) (lower 8 bits) 48	30	00110000
282	80	50	Pixels Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32	30	00110000
g	81	51	Pixels	20	00100000
Tining Descriptor #2	82	52	Vertical Front Porch in lines (VF) (lower 4 bits): Vertical Sync Pluse Width in lines (VS) (lower 4 bits) 3 Lines: 5 Lines	35	00110101
I	83	53	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
	84	54	Horizontal Vedio Image Size (mm) (lower 8 bits) 223	DF	11011111
	85	55	Vertical Vedio Image Size (mm) (lower 8 bits) 125	7D	01111101
	86	56	Horizontal Image Size / Vertical Image Size (upper 4 bits)	00	00000000
	87	57	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	88	58	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	89	59	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)]	1B	00011011
	90	5A	Flag	00	00000000
	91 92	5B 5C	Flag	00	00000000
	92	5D	Flag Data Type Tag: Alphanumeric Data String (ASCII String)	FE	11111110
	94	5E	Flag	00	00000000
8	95	5F	Dell P/N 1st Character = R	52	01010010
₩ .†)	96	60	Dell P/N 2nd Character = K	4B	01001011
interest	97	61	Dell P/N 3rd Character = D	44	01000100
SC	98	62	Dell P/N 4th Character = Y	59	01011001
ä	99	63	Dell P/N 5th Character = 3 EDID Pavision Pavil Name = MD(V Pavild) Pavision # = 400	33	00110011
Timing Descriptor#3	100 101	64 65	EDID Revision Build Name = MP(X-Build) , Revision # = A00 Manufacturer P/N = 1	80 31	10000000
т	102	66	Manufacturer $P/N = 0$	30	00110001
7	103	67	Manufacturer P/N = 1	31	00110001
	104	68	Manufacturer P/N = W	57	01010111
	105	69	Manufacturer P/N = H	48	01001000
	106	6A	Manufacturer P/N = 1	31	00110001
	107	6B	Manufacturer P/N (If < 13 char, then terminate with ASC code 0Ah,set remaining char = 20h)	0A	00001010



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag: Descriptor Defined by manufacturer	00	00000000
	112	70	Flag	00	00000000
	113	71	Color Management [No +2 FRC Support, True Color Depth : 6 bit]	00	00000000
24	114	72	Panel Type [WLED] , Configuration [Single light bar], Number Lamp or LED Light Bar [one]	41	01000001
Timing Descriptor #4	115	73	Frame Rate Details [Minimum Frame Rate : 60Hz, Maximum Frame Rate : 65Hz , Tcon provides native Intel DRRS / sDRRS support]	33	00110011
ψ	116	74	Controller Interface and Maximum Luminance [PWM type, 250 nit]	99	10011001
esci	117	75	Front Surface / Polarizer [Glossy/True-life, No Transflective] , Pixel Structure [RGB v-stripe]	01	00000001
g	118	76	Multi-Media Features [Color Management : NTSC, Dynamic Backlight Control : No]	00	00000000
rain	119	77	Multi-Media Features [Motion Blur : No support , Active Gamma Control : No support]	00	00000000
Tu	120	78	Special Features [Wireless Enhancement Hardware : No support , In-Cell Scanner : No support]	00	00000000
	121	79	Special Features [Number of LVDS channels or eDP lanes : one , Overdrive : No ,Interface : LVDS , In-Cell Touch Support : No]	01	00000001
	122	7A	Special Features [BIST Support : yes , Electronic Privacy : No electronic privacy hardware support , 3-D Support : No]	01	00000001
	123	7B	(If<13 char> 0Ah, then terminate with ASC code 0Ah,set remaining char = 20h)	0A	00001010
	124	7C	(If<13 char> 0Ah, then terminate with ASC _{II} code 0Ah,set remaining char = 20h)	20	00100000
	125	7D	(If<13 char> 0Ah, then terminate with ASC code 0Ah,set remaining char = 20h)	20	00100000
Check sum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
Ch _n	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	35	00110101

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