

Product Specification

SPECIFICATION FOR APPROVAL

(•) Preliminary	Specification
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() Final Specification

Title		13.3" WHD TFT LCD							
BUYER	HP	I	SUPPLIER	LG Display Co., Ltd.					
MODEL			*MODEL	LP133WH1					
			Suffix	TLA2					

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE						
/							
/							
/							
Please return 1 copy for your confirmation with your signature and comments.							

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	29. Apr. 2009	-	First Draft (Preliminary Specification)	0.0

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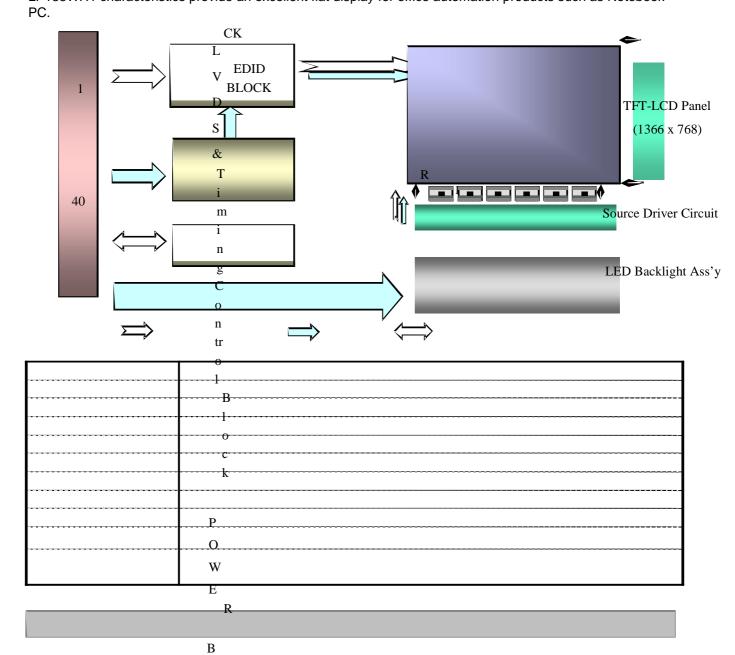
Product Specification

1. General Description

The LP133WH1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 13.3 inches diagonally measured active display area with WHD resolution(1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors

The LP133WH1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP133WH1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP133WH1 characteristics provide an excellent flat display for office automation products such as Notebook



L O 1

1366 768

VBL+ & PWM

42ea

Control & Data Power EDID signal & Power

General Features

Active Screen Size 13.3 inches diagonal

Outline Dimension 308.1 (H) Y 183.6 (V) Y 5.5(D, max.) mm

Pixel Pitch 0.2148 mm 4 0.2148 mm

Pixel Format 1366 horiz. by 768 vert. Pixels RGB strip arrangement

Color Depth 6-bit, 262,144 colors Luminance, White 200 cd/m₂(Typ., 5 points)

Power Consumption Total 4.5 Watt(Max.) @ LCM circuit 1.2 Watt(Max.), B/L input 3.3Watt(Max. with Driver)

Weight 350g(Max.)

Display Operating Mode Transmissive mode, normally white

Surface Treatment Hard Coating(3H), Glare treatment of the front polarizer

RoHS Comply Yes

&Halogen Free

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Gate In Panel

or Pin



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2. Absolute Maximum Ratings

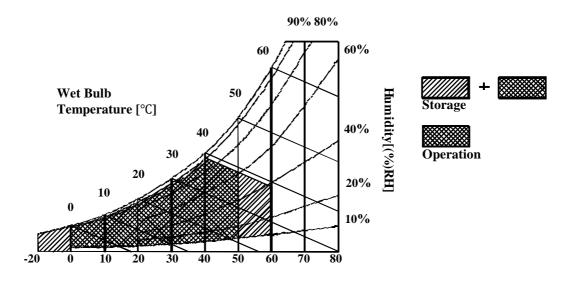
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes		
Farameter	Symbol	Min	Max	Ullits	Notes		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C		
Operating Temperature	Тор	0	50	°C	1		
Storage Temperature	Hst	-20	60	°C	1		
Operating Ambient Humidity	Нор	10	90	%RH	1		
Storage Humidity	Hst	10	90	%RH	1		

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Dry Bulb Temperature [°C]

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V

V

V

Hrs

6

8.0

5

0.8



LP133WH1 Liquid Crystal Display

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3. Electrical Specifications

3-1. Electrical Characteristics

The LP133WH1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED Backlight.

Values Parameter Symbol Unit Notes Min Max Тур LOGIC: Power Supply Input Voltage Vcc 3.0 3.3 3.6 Mosaic 315 365 mΑ 1 Power Supply Input Current Black 360 410 mΑ Pcc 1.0 1.2 W 1 **Power Consumption** Power Supply Inrush Current ICC P 1500 mΑ LVDS Impedance **Z**LVDS 90 100 110 2 BACKLIGHT: (with LED Driver) LED Power Input Voltage 12.0 20.0 VLED 7.0 ٧ LED Power Input Current 260 ILED mΑ 3 **LED Power Consumption** 3.1 W 3 PLED 3.3 **LED Power Inrush Current** LED P -mΑ **PWM Dimming Ratio** 6 100 % 4 PWM Impedance **Z**PWM 20 40 60 k **PWM Frequency F**PWM 200 1000 Hz 5 PWM High Level Voltage 5 V 2.1 3.3 V_{PWM_H}

Table 2. ELECTRICAL CHARACTERISTICS

Note)

Life Time

PWM Low Level Voltage

LED_EN High Voltage

LED_EN Low Voltage

1. The specified Icc current and power consumption are under the Vcc = 3.3V , 25°C, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.

 V_{PWM_L}

VLED_EN_H

VLED_EN_L

is displayed and fv is the frame frequency.

2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.

0

2.1

0

12,000

3.3

- 3. The specified LED current and power consumption are under the Vled = 12.0V, 25°C, Dimming of Max luminance whereas White pattern is displayed and fv is the frame frequency.
- 4. There may be a flickering or some reliability issues when LED driver is operated by under condition of minimum.
- 5. The PWM Frequency has 0Hz, DC level for dimming ratio 100%. The PWM Frequency should be fixed and continue for stable luminance levels what you want.
- 6. The life time is determined as the time at which brightness of LCD is 50% compare to that of initial value at the typical LED current. These LED backlight has 6 strings on it and the typical current of LED's string is base on 20mA.



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3-2. Interface Connections

This LCD employs one interface connections, a 40 pin connector is used for the module electronics interface and LED Driver.

The electronics interface connector is a model 20455-040E-0x manufactured by I-PEX.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

1	Pin	Symbol	Description	Notes
3		NC	No connection	
1. Interface chips	2	VCC	Power Supply, 3.3V Typ.	
S	3	VCC		
5	4	V EEDID	DDC 3.3V power	1 Interface chins
7	5	NC	No Connection	
8	6		DDC Clock	
8	7	DATA EEDID	DDC Data	
9 Odd R ₂ 0+ Positive LVDS differential data input 10 GND Ground 2.1 LCD 20455-040E-0x, I-PEX or sequivalent. 2.2 Connector 2.1 LCD 20455-040E-0x, I-PEX or sequivalent. 2.2 Adding: 20455-040E-0x, I-PEX or sequivalent. 2.2 Adding: 20455-040E-0x, I-PEX or sequivalent. 2.2 Adding: 20455-040E-0x, I-PEX or sequivalent. 2.3 Connector prior arrangement 2.4 Odd R ₂ 2+ Positive LVDS differential data input 16 Odd R ₂ 2- Positive LVDS differential clock input 17 Odd CLKIN+ Positive LVDS differential clock input 18 Odd-CLKIN+ Positive LVDS differential clock input 20 NC No Connection 21 NC No Connection 22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 28 NC No Connection 29 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)	8	Odd_R _{IN} 0-	Negative LVDS differential data input	
11	9	Odd_R _№ 0+	Positive LVDS differential data input	·
11	10	ĞND	Ground	
12		Odd R _{IN} 1-	Negative LVDS differential data input	•
14 Odd R₃2- 15 Odd R₃2- 16 GND Ground 17 Odd_CLKIN- 18 Odd_CLKIN- 19 GND Ground 20 NC No Connection 21 NC No Connection 22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 28 NC No Connection 29 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)		Odd_R _⊪ 1+	Positive LVDS differential data input	2.2 Mating : 20453-040T-0x, I-PEX
14 Odd R _m 2+ Negative LVDS differential data input 15 Odd R _m 2+ Positive LVDS differential data input 16 GND Ground 17 Odd_CLKIN- Negative LVDS differential clock input 18 Odd_CLKIN- Positive LVDS differential clock input 19 GND Ground 20 NC No Connection 21 NC No Connection 22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control <td>13</td> <td>GND</td> <td>Ground</td> <td>· ·</td>	13	GND	Ground	· ·
15	14	Odd R _№ 2-	Negative LVDS differential data input	2.3 Connector pin arrangement
17		Odd_R _{ı₀} 2+	Positive LVDS differential data input	40 1
18				
19			,	
20 NC No Connection 21 NC No Connection 22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 28 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)		_	•	
21 NC No Connection 22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 28 NC No Connection 29 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				[LCD Module Rear View]
22 NC No Connection 23 NC No Connection 24 NC No Connection 25 NC No Connection 26 NC No Connection 27 NC No Connection 28 NC No Connection 29 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)		NC		
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27 NC No Connection 28 NC No Connection 29' NC No Connection 30' NC No Connection 31' VLED_GND LED Ground 32' VLED_GND LED Ground 34' NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37' NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
28 NC No Connection 29' NC No Connection 30' NC No Connection 31 VLED_GND LED Ground 32' VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37' NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
29 NC No Connection 30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
30 NC No Connection 31 VLED_GND LED Ground 32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
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32 VLED_GND LED Ground 33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
33 VLED_GND LED Ground 34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
34 NC No Connection 35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)	32	VLED_GND	LED Ground	
35 BLIM PWM for Luminance control 36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)	33	VLED_GND	LED Ground	
36 BL_On Backlight On/Off Control 37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)	34	····NC	No Connection	
37 NC No Connection 38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)				
38 VLED LED Power Supply (7V-20V) 39 VLED LED Power Supply (7V-20V)			· ·	
39 VLED LED Power Supply (7V-20V)	37			
40 VLED LED Power Supply (7V-20V)				
	40	VLED	LED Power Supply (7V-20V)	



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The LED backlight connector is a model TF12-9S-0.5H, manufactured by Hirose or equivalent.

 Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (CN2)

Pin	Symbol	Description	Notes
1	Vdc(1,2,3,4,5,6)	LED Anode(Positive)	
2	Vdc(1,2,3,4,5,6)	LED Anode(Positive)	
3	NC	No Connection	
4	Vdc1	LED Cathode (Negative)	
5	Vdc2	LED Cathode (Negative)	
6	Vdc3	LED Cathode (Negative)	
7	Vdc4	LED Cathode (Negative)	
8	Vdc5	LED Cathode (Negative)	
9	Vdc6	LED Cathode (Negative)	

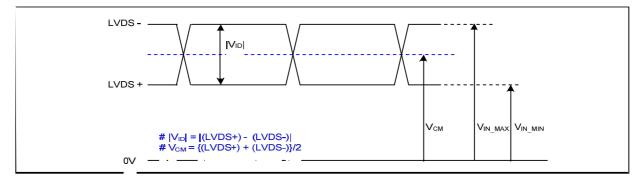
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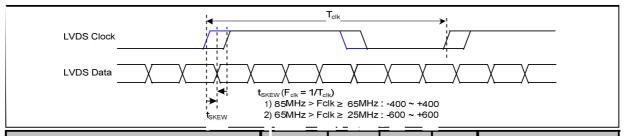
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

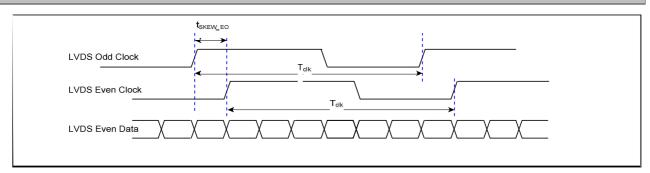
3-3-2. AC Specification



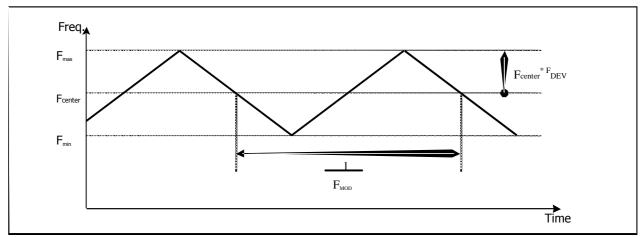
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	tskew	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
2.750 olook to Bala okow Wargin	tskew	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	tskew_eo	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV} -		± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD} -		200	KHz	-



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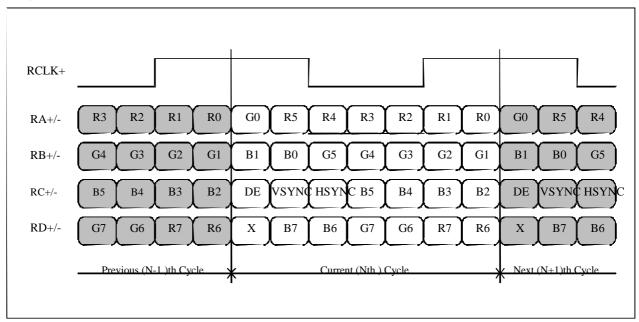
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >



Product Specification

3-4. Signal Timing Specifications

Vertical front porch

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

ITEM **Symbol** Min Max Unit Note Typ DCLK 69.3 MHz Frequency fclk Thp 1406 1462 1518 Period Hsync Width $t_{wH}8$ 16 24 tCLK 1366 Width-Active t_{wha}1366 1366 790 800 Period t₁₀780 Vsync Width 6 9 $t_{wv}3$ tHP Width-Active 768 768 twva768 Horizontal back porch t_{HBP}24 48 72 tCLK Horizontal front porch 32 56 $t_{HFP}8$ Data Enable 13 Vertical back porch $t_{\scriptscriptstyle VBP}8$ 18 tHP

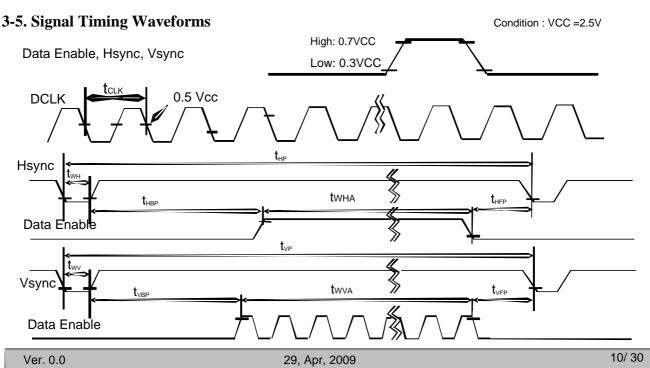
Table 5. TIMING TABLE

Note)

 $t_{vFP}1$

3

5



^{1.} In this documentation, all reliabilities are specified for timing specification based on refresh rate of 60Hz. However, LP133WH1 has a good actual performance even at lower refresh rate(eg. 40Hz or 50Hz) for power saving mode, whereas LP133WH1 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz ,40 Hz at Power save mode. Don't care Flicker level (power save mode).



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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

			Input Color Data																
ے ا	Color			RE	D					GRE	ΕN					BL	JΕ		
F		MSI	В				LSB							MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0		G 4	G 3		G 1			B 4	В3	B 2	B 1	B 0
	Black	0	0	0	0	0	0		0	0	0	0	0		0	0	0	0	0
	Red	1	1	1	1	1	1)	0	0	0	0	0) 	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	D	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	D	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	D	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	D	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0)	0	0	0	0	0)	0	0	0	0	0
	RED (01)	Ö	0	0	0	0	1	j	0	0	0	0	0	<u> </u>	0	0	0	0	0
RED		····	• • • • • • • • • • • • • • • • • • • •	•••••		•••••		••••											
	RED (62)	1	1	1	1	1	0	<u> </u>	0	0	0	0	0	<u> </u>	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	Ď	0	0	0	0	0	D	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0)	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0)	0	0	0	0	1)	0	0	0	0	0
GREEN				•••••	·····	•••••													
	GREEN (62)	0	0	0	0	0	0	 1	1	1	1	1	0) D	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1) D	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0		0	0	0	0	0		0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0)	0	0	0	0	0)	0	0	0	0	1
BLUE		 							•••••		•					••••			
	BLUE (62)	0	0	0	0	0	0	b	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0		0	0	0	0	0	1	1	1	1	1	1
	- (/	┖—																	



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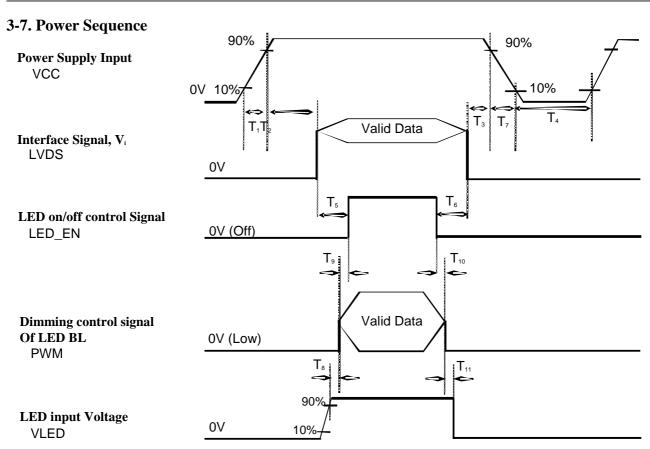


Table 6. POWER SEQUENCE TABLE

		•				
Doromotor		Units				
Parameter	Min.	Тур.	Max.			
T ₁	0.5	-	10	ms		
T ₂	0	-	50	ms		
T ₃	200	-	-	ms		
T₄	200	-	-	ms		
T _s	0	-	50	ms		
T ₆	3	-	10	ms		
Т,	400	-	-	ms		
T ₈	10	-	100	ms		
T,	0	-	100	ms		
T ₁₀	0	-	100	ms		
T,,	10	-	100	ms		

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of $\sqrt{\ }$ and \bigcirc equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

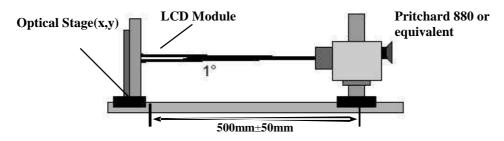


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK} = 69.3 MHz, I_{LED} = 20 mA

	Devementer	Curah al	Symbol Values Ture May				Notes
	Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ra	atio	CR	300	-	-		1
Surface Lui	minance, white	L _{wH}	170	200	-	cd/m ₂	2
Luminance	Variation(13points)	TM WHITE		1.4	1.6		3
Response	Time	Tr _R + Tr _D		16	25	ms	4
Color Coord	dinates						
	RED	RX	0.559	0.589	0.619		
		RY	0.319	0.349	0.379		
	GREEN	GX	0.307	0.337	0.367		
		GY	0.518	0.548	0.578		
	BLUE	ВХ	0.125	0.155	0.185		
		BY	0.092	0.122	0.152		
	WHITE	WX	0.283	0.313	0.343		
		WY	0.299	0.329	0.359		
Viewing An	gle						5
	x axis, right(√=0°)	∪r	40	-	-	degree	
	x axis, left (√=180°)	Ul	40	-	-	degree	
	y axis, up (√=90°)	∪u	10	-	-	degree	
	y axis, down (√=270°)	∪d	30	-	-	degree	
Gray Scale	Gray Scale					1	6

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Product Specification

Notes)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

 Surface luminance is the 5point (1~5)average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.

When I_{LED} = TBD mA, L_{WH} =200cd/m₂(Typ.)

3. The variation in surface luminance, The panel total variation (™ WHITE) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\label{eq:maximum} \begin{array}{c} & & & \\ & & \\ \text{TM} & \\ & & \\ \text{WHITE}^{=} & \\ & & \\$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

 $f_v=60Hz$

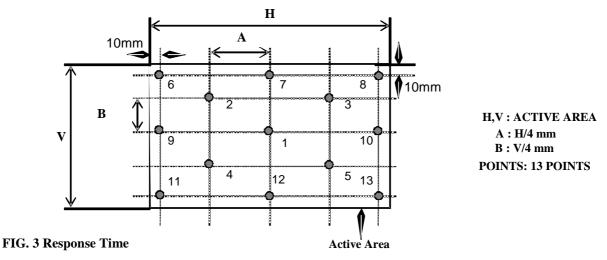
Gray Level	Luminance [%] (Typ)
L0	0.16
L7	1.45
L15	5.36
L23	12.21
L31	21.01
L39	34.82
L47	52.49
L55	74.17
L63	100



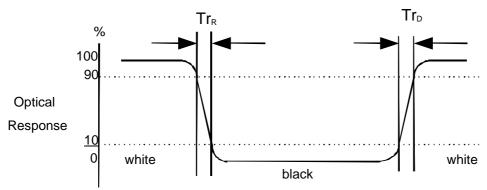
Product Specification

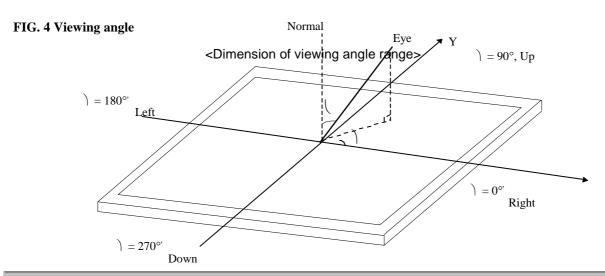
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





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Product Specification

5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP133WH1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	307.6 ± 0.50mm		
Outline Dimension	Vertical	183.1 ± 0.50mm		
	Depth	5.5mm(Max.)		
Bezel Area	Horizontal	299.5 mm		
bezei Alea	Vertical	168.4 mm		
Active Display Area	Horizontal	293.42mm		
Active Display Area	Vertical	164.97mm		
Weight	350g (Max.)			
Surface Treatment	Hard Coating(3H), Glare treatment of	of the front polarizer		

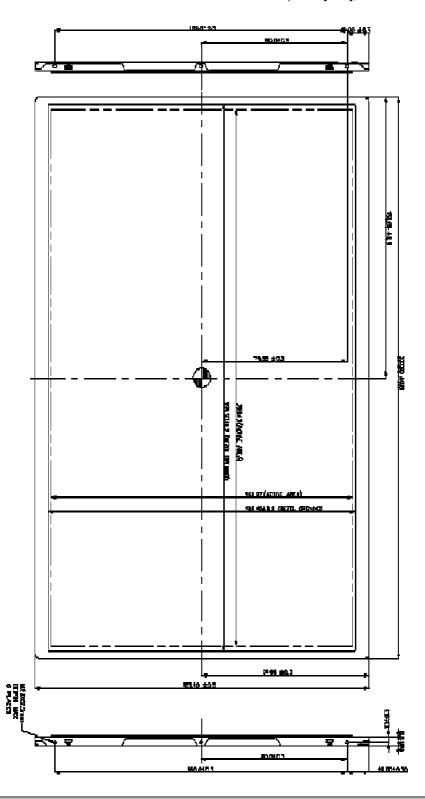


Product Specification

<FRONT VIEW>

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Note) Unit:[mm], General tolerance: ± 0.5 mm

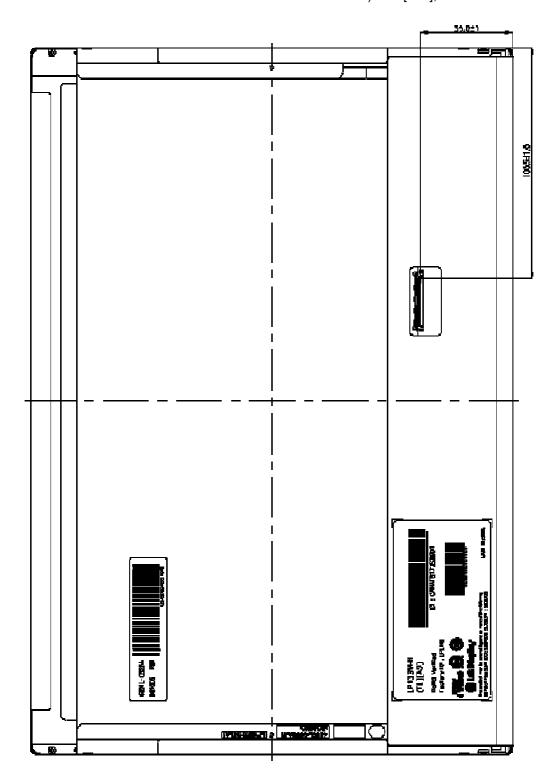




Product Specification

<REAR VIEW>

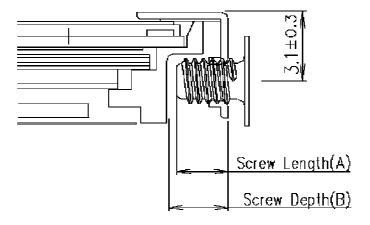
Note) Unit:[mm], General tolerance: ± 0.5 mm





Product Specification

[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



Screw Length (A): Max: 2.5mm / Min: 2.0mm

Screw Depth (B): Min 2.5mm

Screw Torque: Max 2.5kgf.cm (Measurement Gauge: Torque meter)

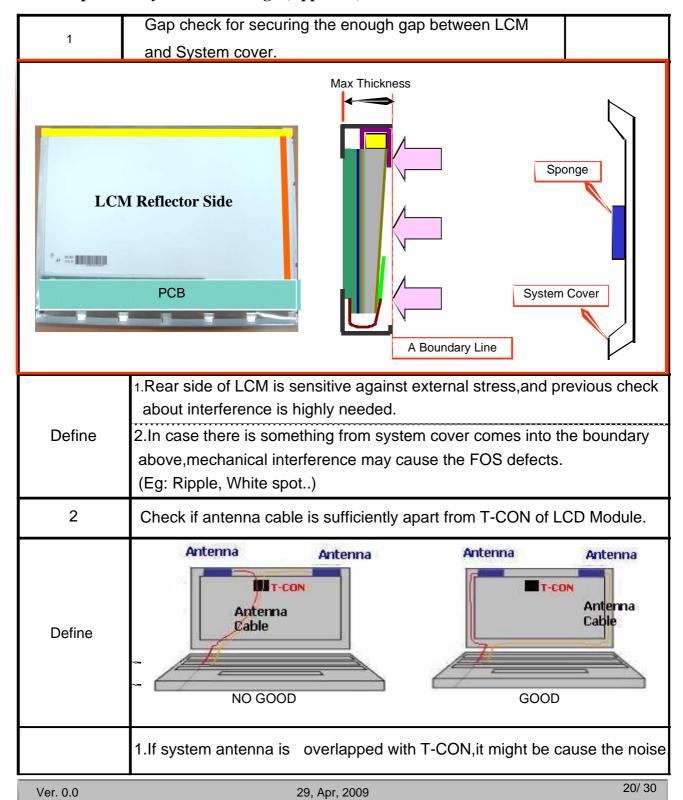
Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

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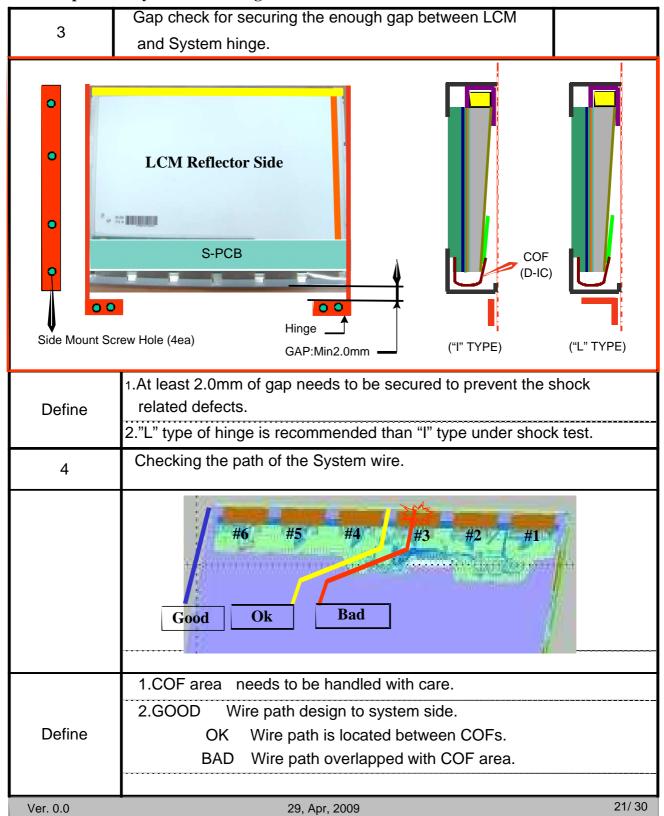
LGD Proposal for system cover design.(Appendix)





Product Specification

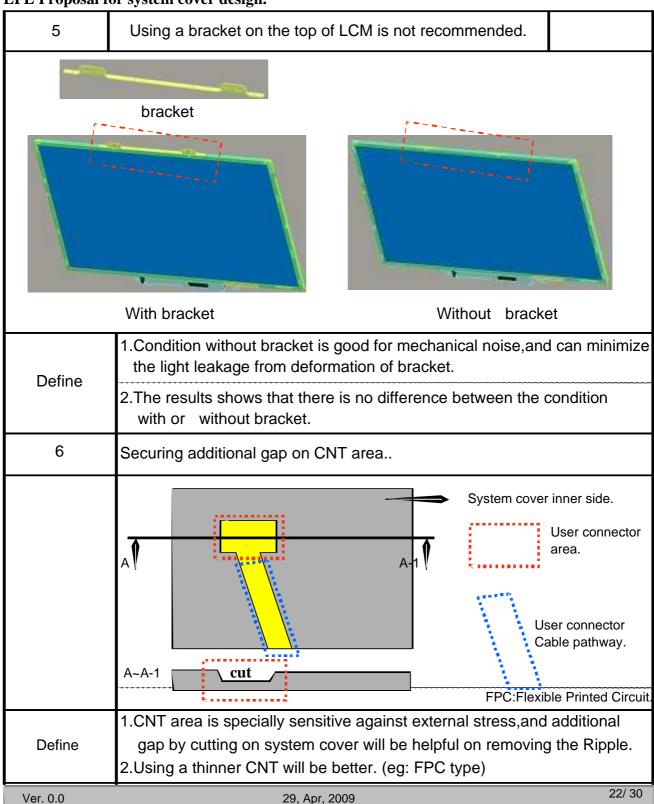
LGD Proposal for system cover design.





Product Specification

LPL Proposal for system cover design.





Product Specification

6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)
7 oper	Altitude ating	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

storage / shipment

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



Product Specification

7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information
 Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998
 (Including A1: 2000)



Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C: SIZE(INCH) D:

YEAR E: MONTH

F~ M: SERIAL NO.

Note										2010		
1. YEAR												
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009		•	
Mark	1	2	3	4	5	6	7	8	9	0		
2. MONTH												
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30 pcs

b) Box Size: 430X378X268mm



Product Specification

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external
 - module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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Product Specification

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDIDTM) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	11111111
ler	2	02	Header	FF	11111111
Header	3	03	Header	FF	11111111
H	4	04	Header	FF	11111111
	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	EISA manufacture code (3 Character ID)	30	00110000
*	9	09	EISA manufacture code (Compressed ASC II)	E4	11100100
Vendo r /Product EDID Version	10	0A	Panel Supplier Reserved - Product Code 022Ch	2C 02	00101100
endo r /Produ EDID Version	11	0B 0C	(Hex. LSB first)	00	00000010
/Pr	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used) LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
r. 70	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	
do II	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
en EE	16	10	Week of Manufacture 0	00	00000000
7	17	weeks		13	00010011
	18	11	Year of Manufacture 2009years	01	00010011
	19	12	EDID structure version # = 1	03	
		13	EDID revision # = 3		00000011
v ers	20			80	10000000
Display Parameters	21	14	Video input Definition = Digital signal	1D	00011101
ds:	22	15	Max H image size (Rounded cm) = 29 cm	11	00010001
ar D	23	16	Max V image size (Rounded cm) = 17 cm	78	01111000
P.	24	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma		
	24		Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK	0A	00001010
te	25	18	l no GTF)	D5	11010101
ıa	26	19	Red/Green Low Bits (RxRy/GxGy)	D5	11010101
đị.	27	1A	Blue/White Low Bits (BxBy/WxWy)	96	10010110
100	28	IR	Red X Rx = 0.589	59	01011001
Ç	29	1C	Red Y Ry =0.349	56	01010110
0 1	30	1D	Green X $Gx = 0.337$	8C	10001100
ρο	31	1E	Green Y Gy =0.548	27	
Pane lColo rCoordinate	32	1F	Blue X Bx = 0.155	1F	00100111
ne		20	Blue Y By = 0.122		00011111
Pa	33		•	50	01010000
~	34	21	White X Wx =0.313	54	01010100
i e IS	35	22	White Y Wy =0.329	00	00000000
lisl 'ng		23	Established timing 1 (00h if not used)		
stablish e Timin gs	36			00	00000000
Establish e. Timings		24	Established timing 2 (00h if not used)	0.0	
7	37			00	00000000
	38	25	Manufacturer's timings (00h if not used)	01	00000001
	39	26	Standard timing ID1 (01h if not used)	01	00000001
	40	27	Standard timing ID1 (01h if not used)	01	00000001
0	41	28	Standard timing ID2 (01h if not used)	01	00000001
s l	42	29	Standard timing ID2 (01h if not used)	01	00000001
in	43	2A	Standard timing ID3 (01h if not used)	01	00000001
ïm	44	2B	Standard timing ID3 (01h if not used)	01	00000001
1 I	45	2C	Standard timing ID4 (01h if not used)	01	00000001
xr	46	2D	Standard timing ID4 (01h if not used)	01	00000001
nd	47	2E	Standard timing ID5 (01h if not used)	01	00000001
Standard Timing ID	48	2F	Standard timing ID5 (01h if not used)	01	00000001
S	49	30	Standard timing ID6 (01h if not used) Standard timing ID6 (01h if not used)	01	00000001
	50	31	Standard timing ID6 (01n if not used) Standard timing ID7 (01h if not used)	01	00000001
	51 52	33	Standard timing ID7 (01h if not used) Standard timing ID7 (01h if not used)	01	00000001
	53	34	Standard timing ID8 (01h if not used) Standard timing ID8 (01h if not used)	01	00000001 00000001
	55	35	Standard timing ID8 (01h ir not used) Standard timing ID8 (01h ir not used)	VI	10000001
		55			



Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

Section Sect		Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
15				Pixel Clock/10.000 (LSB) 69.3 MHz @ 60Hz		00010010
100 100						
190 190		56	38			
Section		57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 96 Pixels		
Process		58	3A			
1	#1	59	3B			
1	to 1	60				
1	rip.	61		Vertical Blanking (Typ-HA) (DE Blanking typ.for DE only panels) 22 Lines		
1	ssci	62	3D			
1	Dε	63	3E			
1	Bu	64				
1	mi	65	Pixels			
1	Ti	66	40	Vertical Sync Offset(Tvfp) : Sync Width (VSPW) 3 Lines : 6 Lines	26	
Page 14		67	41		_	
Page 13		68	42			
10		69	43			
1		70	44			
1				<u> </u>		
1			46	Vertical Border = 0 (Zero for Notebook LCD)		
The color The		73	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only		
1		74	48	note: LSB is set to 'I' if panel is DE-timing only. H/V can be ignored.	00	00000000
77		75			00	00000000
Secretary Secr	0)	76	4A	Flag	00	00000000
Secretary Secr	#	77	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
Secretary Secr	to	78	4C	Flag	00	00000000
Secretary Secr	rip	79	4D	Descriptor Defined by manufacturer	00	00000000
Secretary Secr	esc	80	4E	Descriptor Defined by manufacturer	00	00000000
Secretary Secr	Ď	81	4F	Descriptor Defined by manufacturer	00	00000000
Secretary Secr	ng	82	50	Descriptor Defined by manufacturer	00	00000000
Secretary Secr	imi	83	51	Descriptor Defined by manufacturer	00	00000000
Second S	I	84	52	Descriptor Defined by manufacturer	00	00000000
ST 55 Descriptor Defined by manufacturer 00 00000000000000000000000000000		85	53	Descriptor Defined by manufacturer	00	00000000
SECULAR Security		86	54	Descriptor Defined by manufacturer	00	00000000
Section Sect		87	55	Descriptor Defined by manufacturer	00	00000000
Parameter Para		88	56	Descriptor Defined by manufacturer	00	00000000
STATEST STAT		89	57	Descriptor Defined by manufacturer	00	00000000
Parameter Para		90	58	Descriptor Defined by manufacturer	00	00000000
Page		91	59	Descriptor Defined by manufacturer	00	00000000
94 5C Flag 90 9000000000000000000000000000000		92		· ·	00	00000000
95 5D Data Type Tag (ASCII String) 4C 01001100 96 5E Flag 47 01000111 97 5F ASCII String L 20 00100000 98 60 ASCII String 44 01000100 99 G 69 01101001 100 61 ASCII String 73 01110011 101 62 ASCII String D 70 01110000 102 63 ASCII String i 6CC 01101100 103 64 ASCII String s 61 01100001 104 65 ASCII String s 61 01100001 105 66 ASCII String s 61 01100001 106 67 ASCII String s 61 0100001 106 67 ASCII String s 61 01000000 107 108 109 109 109 109 108 68 ASCII String s 20 001000000 109 68 ASCII String s 20 00100000000000000000000000000000					FE	11111110
103 64 ASCII String 8 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y		94				00000000
103 64 ASCII String 8 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	#					01001100
103 64 ASCII String S 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	ato					01000111
103 64 ASCII String S 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	cri			*		00100000
103 64 ASCII String S 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	sə			ASCII String		01000100
103 64 ASCII String S 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	g L					01101001
103 64 ASCII String S 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	ini			-		01110011
103 64 ASCII String 8 61 01100001 104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	'in			· · · · · · · · · · · · · · · · · · ·		
104 65 ASCII String P 79 01111001 105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y	I			-	6C	01101100
105 66 ASCII String 1 0A 00001010 106 67 ASCII String a 20 00100000 68 ASCII String y -						01100001
106 67 ASCII String a 20 00100000 68 ASCII String y						01111001
68 ASCII String y				·	0A	00001010
· · ·		106		-	20	00100000
69 Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II 6A Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II			69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II		

6A Manufacturer P/N(If<13 char--> 0Ah, then terminate with ASC II

107 6B Manufacturer P/N(If<13 char--> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h) 20 00100000



Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDIDTM) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
4	112	70	Flag	00	00000000
r#	113	71	ASCII String L	4C	01001100
#o	114	72	ASCII String P	50	01010000
Timing Descripto r#4	115	73	ASCII String 1	31	00110001
esc	116	74	ASCII String 3	33	00110011
D	117	75	ASCII String 3	33	00110011
ing	118	76	ASCII String W	57	01010111
im	119	77	ASCII String H	48	01001000
I	120	78	ASCII String 1	31	00110001
	121	79	ASCII String -	2D	00101101
	122	7A	ASCII String T	54	01010100
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String A	41	01000001
~	125	7D	ASCII String 2	32	00110010
Checksun	126	7 E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	0000000
Che	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	02	00000010

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