

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(•) Final Specification

Title

20.1" VGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC201V02
SUFFIX	SDB1

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE	APPROVED BY	SIGNATURE DATE
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Please return 1 copy for your confirmation with your signature and comments.		TV Product Developm LG. Philips LCD Co	

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
1.0	April. 07, 2006	-	Final Specification

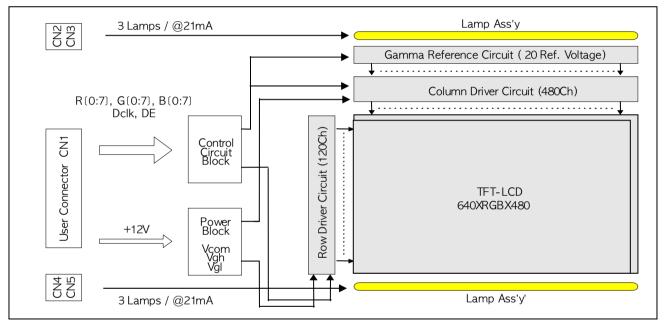


1. General Description

The LC201V02 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 20.1 inch diagonally measured active display area with VGA resolution (480 vertical by 640 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit parallel CMOS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	20.1 inches(510.54mm) diagonal
Outline Dimension	432 mm(H) x 331.5 mm(V) x 25.0 mm(D) (Typ.)
Pixel Pitch	212.5 μm x 637.5 μm x RGB
Pixel Format	640 horiz. By 480 vert Pixels RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	450 cd/m ² (Center 1-point)(Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total 32.24 W(Typ.) (Logic= 2.00W, Lamp= 30.24W [I _{BL} =7mA])
Weight	3,500g (Тур.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H) & anti-glare treatment of the front polarizer

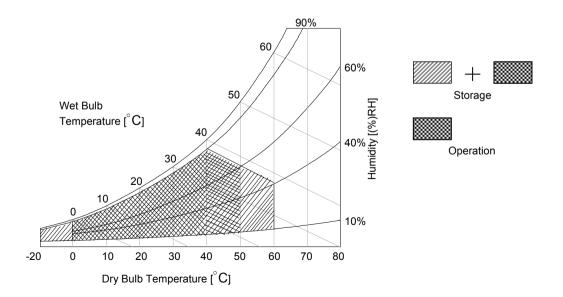
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ue	Unit	Remark	
i arameter	Symbol	Min	Max	Ont	INCIDAIN	
Power Input Voltage	VLCD	-0.3	+14.0	VDC	at 25 \pm 2 $^\circ\text{C}$	
Operating Temperature	Тор	0	50	°C		
Storage Temperature	Тѕт	-20	60	°C	Noto 1	
Operating Ambient Humidity	Нор	10	90	%RH	Note 1	
Storage Humidity	Нѕт	10	90	%RH		

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL /Backlight, is typically generated by an inverter. The inverter is an external unit to the LCDs.

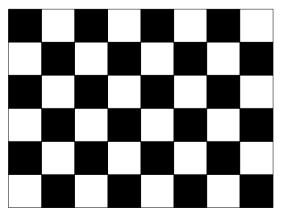
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note	
	Gymbol	Min	Тур	Max	Offic	Note	
Circuit :							
Power Supply Input Voltage	VLCD	11.4	12.0	12.6	VDC		
Power Supply Input Current	ILCD	-	166	191	mA	1	
Power Supply Input Current		-	178	232	mA	2	
Power Consumption	PLCD	-	2.00	2.30	W	1	
Rush current	Irush	-	-	3.0	А	3	

Note :

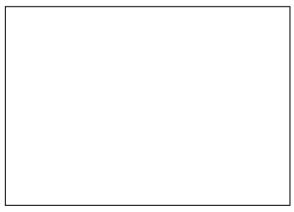
- 1. The specified current and power consumption are under the V_{LCD}=12.0V, $25 \pm 2^{\circ}C$,f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power Input is 1ms (min.).

White : 255Gray Black : 0Gray



Mosaic Pattern(8 x 6)

Maximum current pattern



Full white pattern

Table 2. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol	Value			Unit	Note
		Gymbol	Min	Тур	Max	Onit	Note
Inverter :							
Operating Voltage		VBL	700(7.5mA)	720(7mA)	825(3mA)	V _{RMS}	1, 2
Operating Current		IBL	3.0	7.0	7.5	mA _{RMS}	1
Established Startir	Established Starting Voltage						1, 3
	at 25 $^\circ$ C				1,150	V_{RMS}	
	at 0 °C				1,450	V _{RMS}	
Operating Frequ	ency	fBL	40	50	60	kHz	4
Discharge Stabilization Time		Ts			3	Min	1, 5
Power Consumption		Pbl		30.24	33.27	W	6
Life Time			50,000			Hrs	1, 7

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

- The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.
- Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.
- 1. Specified values are for a single lamp.
- 2. Operating voltage is measured at $25 \pm 2^{\circ}$ C. The variance of the voltage is $\pm 10\%$.
- The voltage above V_S should be applied to the lamps for more than 1 second for start-up. (Inverter open voltage must be more than lamp starting voltage.)
 Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 4. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 5. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.

 T_{S} is the time required for the brightness of the center of the lamp to be not less than 95%.

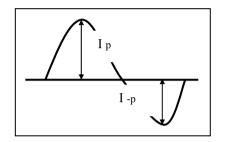
- 6. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current. $(P_{BL} = V_{BL} \times I_{BL} \times N_{Lamp})$
- 7. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^{\circ}$ C.

8. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.

Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.

It shall help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2}$ ±10%.
 - * Inverter output waveform had better be more similar to ideal sine wave.



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* Asymmetry rate:
| I_p - I_{-p} | / I_{rms} x 100%
* Distortion rate
I_p (or I_{-p}) / I_{rms}

- 9. The inverter which is combined with this LCM, is highly recommended to connect coupling(ballast) condenser at the high voltage output side. When you use the inverter which has not coupling(ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.
- 10.In case of edgy type back light with over 4 parallel lamps, input current and voltage wave form should be synchronized

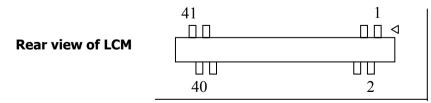


3-2. Interface Connections

- -LCD Connector(CN1):DF9B-41P-1V (Manufactured by Hirose) or Equivalent
- ---Mating Connector : DF9B-41S-1V (Manufactured by Hirose) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	RBF	NC 1)	21	G4	
2	DCLK	Dot Clock	22	GND	System Ground
3	Interlace	Interlace mode 2)	23	G5	
4	Don't care	Don't care	24	G6	
5	Don't care	Don't care	25	G7	Green Data(MSB)
6	GND	System Ground 3)	26	BO	Blue Data (LSB)
7	RO	Red Data(LSB)	27	GND	System Ground
8	R1		28	B1	
9	R2		29	B2	
10	R3		30	B3	
11	R4		31	B4	
12	GND	System Ground	32	GND	System Ground
13	R5		33	B5	
14	R6		34	B6	
15	R7	Red Data (MSB)	35	B7	Blue Data (MSB)
16	GO	Green Data(LSB)	36	GND	System Ground
17	GND	System Ground	37	DE	Data Enable
18	G1		38	GND	System Ground
19	G2		39	VLCD	Power input (+12.0V)
20	G3		40	VLCD	Power input (+12.0V)
			41	VLCD	Power input(+12.0V)



Note: 1. NC: No Connection.

- 2. Interlace mode (NC or GND = Interlace, VCC = Non Interlace).
- 3. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 4. All VLCD (power input) pins should be connected together.



Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3,CN4,CN5)

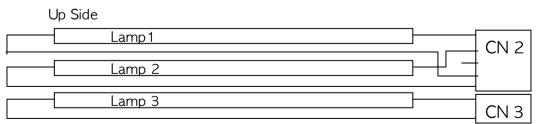
The backlight interface connector is a model BHSR-02VS-2(CN3/CN4) and BHR-05VS-4(CN2/CN5) manufactured by JST. The mating connector part number are SM02B-BHSS-1-TB(2pin), SM04(9-E2)B-BHS-1-TB(5pin) or equivalent. The pin configuration for the connector is shown in the table below.

No.	Pin No.	Symbol	Description	Note
CNID	CNID 1 HV		Power supply for lamp 1(High voltage side) - Gray	1
CN2	2	HV	Power supply for lamp 2(High voltage side) – Sky Blue	1
	3	NC	NC	
	4	LV	Power supply for lamp 1(Low voltage side) - Black	2
	5	LV	Power supply for lamp 2(Low voltage side) – Dark Blue	2
CNID	1	HV	Power supply for lamp 3(High voltage side) - White	1
CN3	2	LV	Power supply for lamp 3(Low voltage side) - White	2
CNI4	1	HV	Power supply for lamp 4(High voltage side) - White	1
CN4	2	LV	Power supply for lamp 4(Low voltage side) - White	2
CN5	1	HV	Power supply for lamp 6(High voltage side) - Gray	1
CINS	2	HV	Power supply for lamp 5(High voltage side) - Sky Blue	1
	3	NC	NC	
	4	LV	Power supply for lamp 6(Low voltage side) - Black	2
	5	LV	Power supply for lamp 5(Low voltage side) - Dark Blue	2

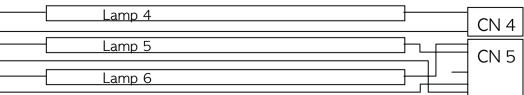
Notes: 1. The high voltage power terminal is colored white, sky blue, gray.

2. The low voltage pin color is white, dark blue, black .

<BACKLIGHT CONNECTOR DIAGRAM>



Down Side





3-3. Signal Timing Specifications

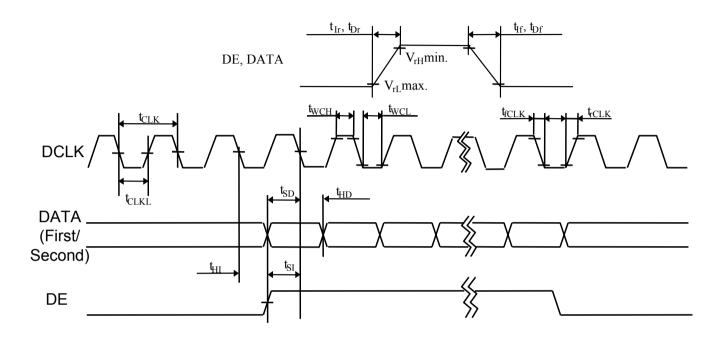
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

ITEM	Symbol		Min	Тур	Max	Unit	Note
	Period	tclk	33.3	40	55	ns	
DCLK	Frequency	-	18	25	30	MHz	
	High Duration	twcн	10	-	-	ns	
	Low Duration	twcL	10	-	-	ns	
DE	Horizontal Valid	tн∨	640	640	640	tclk	
(Data Enable)	Horizontal Blank	-	150	160	210	tclk	
	Vertical Valid	tvv	480	480	480	thp	
	Vertical Blank	-	10	45		tнр	
	DE SET UP TIME	tsı	3	-	-	ns	
	DE HOLD TIME	tнı	3	-	-	115	
Input	High	VrH	0.7Vcc	-	-	v	
Voltage	Low	VrL	-	-	0.3Vcc	v	
Data	Set-up time	tsd	3			20	For DCLK
	Hold time	thd	3			ns	For DCLK

Note: 1. PAL: 47~53Hz, NTSC:57~63Hz



3-4. Signal Timing Waveforms



3-5. Color Input Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The below table provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

												Inpu	ut Co	olor	Data	а									
Color					RE	Ð							GRE	EEN	I						BL	UE			
	00101		BB							MS								MS							.SB
	[<u> </u>			B4				B0
	Black	0	0	0	0		0	0	0	0	0	0			0		0	0	0			0		0	0
	Red (255)	1 	1	1 	1 	1 	1 	1 	1 	0		0				0 	0	0		0 		0		0	0
	Green (255)	0	0		0	0	0 	0	0	1 	1 	1 	1 	1 	1 	1 	1	0		. 0 		0		0	0
Basic	Blue (255)	0	0	0	. 0	. 0	0	0	0	0	0	0	0	0	0	0	0	1	1 	1 	. 1	1	1	1	1 1
Color	Cyan	0	0	0	0	0	.0	0	0	1			1	. 1	1 	1 	. 1	1	1	. 1	. 1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	. 1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	. 1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED					· · · ·									•••• ··			••••	1				••••			
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN			•••		· · · ·		•••					••••	•••••	••••		••••	••••	1		••••	••••	••••		•••	••••
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1		 1				1	0	0	0	0	0	0	0	0
	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	 0	0	0	 0	0	 0	 0	 0	0	0	 0	 0	 0	 0	 0	0	0	0	 0	0	0		0	 1
BLUE	· · · · · · · · · · · · · · · · · · ·				• • • • 		•••				••••		····	• • • • 			•••	1			••••	••••	••••	•••	
	BLUE (254)	 0	0	 0	 0	0	 0	 0	 0	 0	0	 0	 0	 0	 0		0	 1	 1	 1	 1		 1		 0
	BLUE (255)	 0	0	 0	 0	 0	 0	 0	 0	 0	0	 0	 0	 0	 0	 0		 1	 1	 1	 1	 1	 1	 1	 1



3-6. Power Sequence

3-6-1. LCD Driving circuit

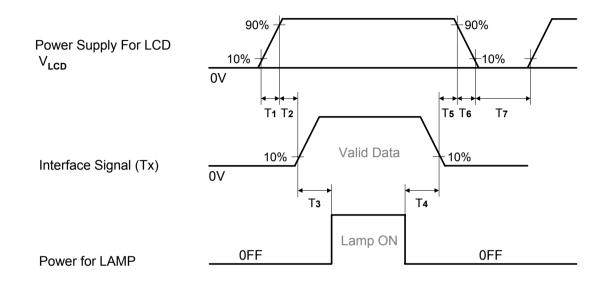


Table 7. POWER SEQUENCE FOR LCD DRIVING CIRCUIT

Deremeter		Value		Linit
Parameter	Min	Тур	Max	Unit
T1	0.5	-	10	ms
T2	0.5	-	50	ms
Т3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	-	-	100	ms
Τ7	1	-	-	S

- Notes : 1. Please avoid floating state of interface signal at invalid period.
 - 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
 - 3. Lamp power must be turn on after power supply for LCD an interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30Min in a dark environment at $25\pm2^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0[°].

It is presented additional information concerning the measurement equipment and method in FIG. 1.

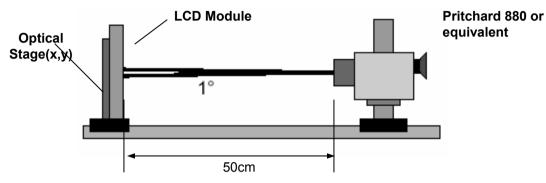


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 8. OPTICAL CHARACTERISTICS

Ta= 25 \pm 2°C, V_{LCD}=12.0V, fv=60HzDclk=25MHz, I_{BL}=7mA

Param	otor	Sumb			Value		Unit	Note
Palali	leter	Symbo	וכ	Min	Тур	Max	Unit	Note
Contrast Ratio		CR		500	800			1
Surface Luminance, white		L _{WH}		380	450		cd/m ²	2
Luminance Variat	ion	δ_{WHITE}	5P			1.3		3
Response Time	Rise Time	Tr _R		-	12		ms	4
Response nine	Decay Time	Tr _D		-	13		1115	4
	RED	Rx			0.632			
	RED	Ry			0.347			
Color Coordinates	ODEEN	Gx]	0.281	Тур		
	GREEN	Gy		Тур -0.03	0.611			
[CIE1931]	BLUE	Bx			0.146	+0.03		
	BLUE	Ву			0.071			
		Wx		1	0.289			
	WHITE	Wy		1	0.304			
Viewing Angle (Cl	R>10)							
x ax	s, right(φ=0°)	θr		85	89	-		
x ax	s, left (ϕ =180°)	θΙ		85	89	-		_
y ax	y axis, up (∳=90°)			85	89	-	degree	5
y ax	s, down (ϕ =270°)	θd		85	89	-		
Gray Scale								6



Note :

1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$ It is measured at center 1-point.

- 2. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 1.
- 3. The variation in surface luminance , δ WHITE is defined as :

 $\delta \text{ WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}}) / \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}})$

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 9.

Gray Level	Luminance [%] (Typ.)
LO	0.29
L15	0.42
L31	0.95
L47	2.36
L63	4.72
L79	7.82
L95	11.7
L111	16.3
L127	21.8
L143	28.2
L159	35.6
L175	43.5
L191	52.4
L207	62.6
L223	74.1
L239	86.2
L255	100

Table 9. GRAY SCALE SPECIFICATION



Measuring point for surface luminance & measuring point for luminance variation

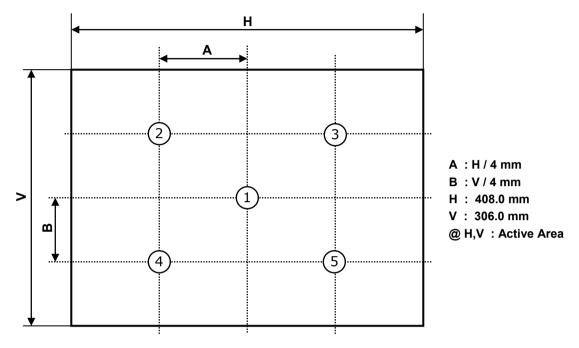


FIG. 2 Measure Point for Luminance

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

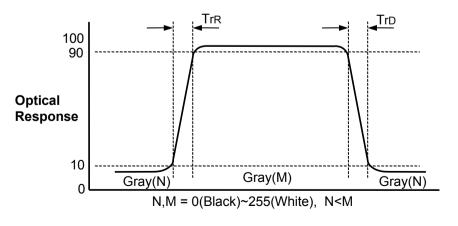


FIG. 3 Response Time



Dimension of viewing angle range

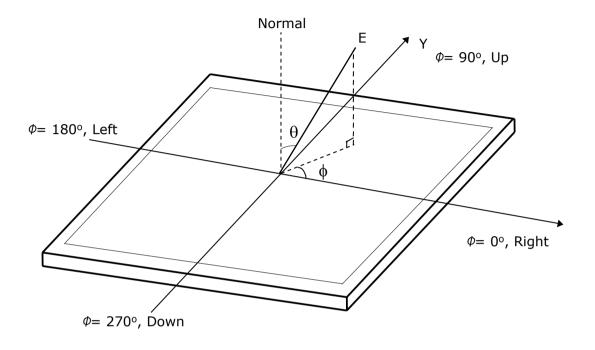


FIG. 4 Viewing angle

5. Mechanical Characteristics

The following items provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD module.

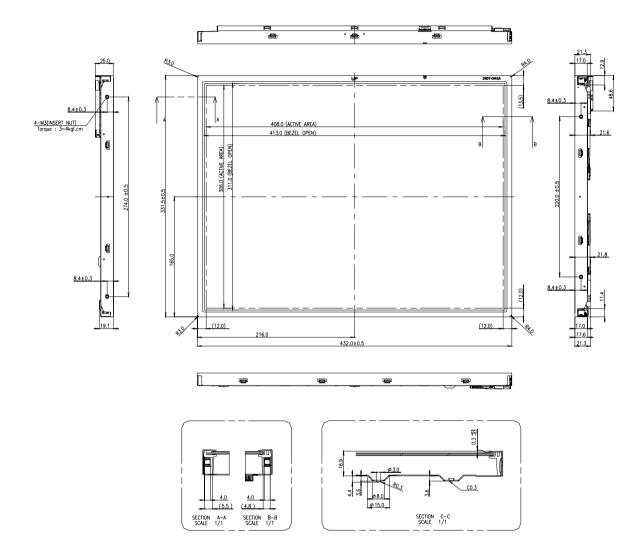
Table 10. MECHANICAL CHARACTERISTICS

Item	Va	lue			
	Horizontal	432.0 mm			
Outline Dimension	Vertical	331.5 mm			
	Depth	25.0 mm			
Dozel Aree	Horizontal	413.0 mm			
Bezel Area	Vertical	311.0 mm			
Active Dieplay Area	Horizontal	408.0 mm			
Active Display Area	Vertical	306.0 mm			
Weight	3500g (Typ.), 3675g(Max)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

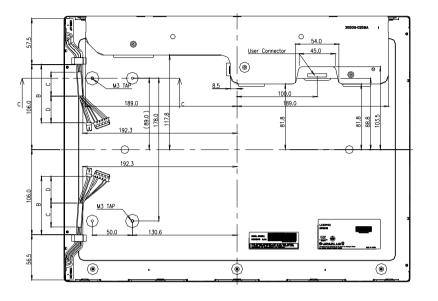


<FRONT VIEW>



<REAR VIEW>

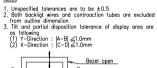
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* A. B. C. D - CONTENTS and User Connector type

-													
[LCM P/N	Suffix – A	Wire Length – B	Shrinkable Tube Length - C	Distance - D	User Connector Type							
[2436L-0515C/D	SDB1	74±10	15	45	DF9B-41P-1V (HIROSE)							

<u>Notes</u>





4. Lamp(CCFL) lot No. is marked at backlight connector

Mark M JST(BHR-05VS-4) JST(BHSR-02VS-2) SCALE 1/1

5. Do not wrap conductive tape around the backlight wires

6. Reliability

Table 11. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition						
1	High temperature storage test	Ta= 60°C 240h						
2	Low temperature storage test	Ta= -20°C 240h						
3	High temperature operation test	Ta= 50°C 50%RH 240h						
4	Low temperature operation test	Ta= 0°C 240h						
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-500Hz Duration : X,Y,Z, 20 min One time each direction						
6	Shock test (non-operating)	Shock level : 100Grms Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction						



7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992

b) C.I.S.P.R. "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.

c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)

- E : MONTH
- G : ASSEMBLY CODE

D : YEAR F : FACTORY CODE H ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG		
Mark	К	С	D		

4. SERIAL NO.

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Mark 100001~199999, 200001~299999, 300001~399999, ...., A00001~A99999, ...., Z00001~Z99999
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b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 6 pcs
- b) Box Size : 530mm imes 307mm imes453mm



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.