

Product Specification

SPECIFICATION FOR APPROVAL

- (●) Preliminary Specification
- () Final Specification

42.0j WUXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC420WU5
SUFFIX	SLA2

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
/	
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Please return 1 copy for your c	confirmation with
your signature and cor	nments.

APPROVED BY	SIGNATURE DATE
REVIEWED BY	
PREPARED BY	
TV Product Developm LG. Philips LCD Co	ent Dept. ., Ltd

LC420WU5

Product Specification

Number	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTERISTICS	6
3-2	INTERFACE CONNECTIONS	8
3-3	SIGNAL TIMING SPECIFICATIONS	10
3-4	SIGNAL TIMING WAVEFORMS	11
3-5	COLOR DATA REFERENCE	12
3-6	POWER SEQUENCE	13
4	OPTICAL SPECIFICATIONS	15
5	MECHANICAL CHARACTERISTICS	19
6	RELIABILITY	22
7	INTERNATIONAL STANDARDS	23
7-1	SAFETY	23
7-2	EMC	23
8	PACKING	24
8-1	DESIGNATION OF LOT MARK	24
8-2	PACKING FORM	24
9	PRECAUTIONS	25
9-1	MOUNTING PRECAUTIONS	25
9-2	OPERATING PRECAUTIONS	25
9-3	ELECTROSTATIC DISCHARGE CONTROL	26
9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	26
9-5	STORAGE	26
9-6	HANDLING PRECAUTIONS FOR PROTECTION FILM	26
pendix		27~31

CONTENTS

RECORD OF REVISIONS

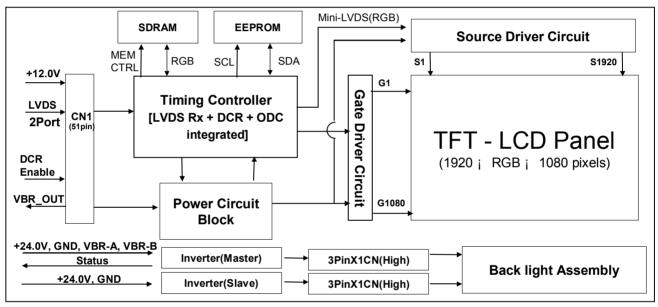
Revision No.	Revision Date	Page	Description
0.0	Apr, 19, 2007	-	Preliminary Specification(First Draft)
0.1	June, 25, 2007	4, 6	Update Current and Power Consumption (Not Fixed)
		7	Update Table 3(Not Fixed)
		9	Update Notes 5.
		15	Update Table 11(Color Coordinates_ Not Fixed)
		29	Update Pallet Assiy
		31	Update Pallet Label

1. General Description

The LC420WU5 is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 42.02 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.02 inches(1067.31mm) diagonal
Outline Dimension	983.0(H) x 576.0 (V) x 51.0 mm(D) (Typ.)
Pixel Pitch	0.4845 mm x 0.4845 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	500 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total 168.8 W (Typ.) (Logic = (8.8) W, Inverter = (160) W [VBR-A=1.65V])
Weight	(11.5Kg) (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 13%)

2. Absolute Maximum Ratings

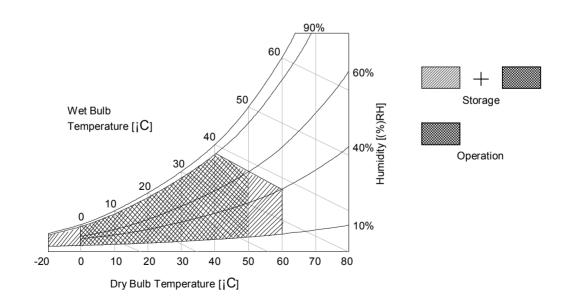
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Parameter		Symbol	Value		Unit	Remark
		Symbol	Min	Max	Offic	Remark
Power Input	Power Input LCM		+8.0	+14.0	VDC	at 25 \pm 2 $_{i}C$
Voltage	Backlight inverter	VBL	+21.6	+27.0	VDC	
ON/OFF Co	ON/OFF Control Voltage		-0.3	+5. 5	VDC	
Brightness C	Brightness Control Voltage		0	+5.0	VDC	
Operating T	Operating Temperature		0	+50	iC	
Storage Temperature		Тѕт	-20	+60	iC	Noto 1.2
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2
Storage Humidity		Нѕт	10	90	%RH	

Table 1. ABSOLUTE MAXIMUM RATINGS

Notes : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 ¡C Max, and no condensation.

2. Gravity mura can be guaranteed under 40 °C condition.



3. Electrical Specifications

3-1. Electrical Characteristics

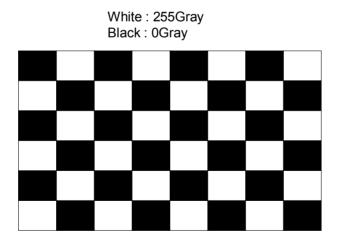
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the EEFL backlight and inverter circuit.

Table 2.	ELECTRICAL CHARACTERISTICS
----------	-----------------------------------

Parameter	Symbol		Value	Unit	Note	
i didifictor	Gymbol	Min	Тур	Max	Onit	NOLE
Circuit :						
Power Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Device legist Current	ILCD	-	(734)	(845)	mA	1
Power Input Current		-	(866)	(996)	mA	2
Power Consumption	PLCD	-	(8.8)	(10.14)	Watt	1
Rush current	Irush	-	-	(3)	А	3

Notes : 1. The specified current and power consumption are under the V_{LCD}=12.0V, $25 \pm 2_i$ C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 1ms (min)



Mosaic Pattern(8 x 6)

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol		Values			Notes	
			Symbol	Min	Тур	Max	Unit	NOLES
Inverter :	Inverter :							
Power Supply Inpu	t Voltage		VBL	22.8	24.0	25.2	Vdc	1
Power Supply Inpu	t Voltage Rip	ople		-	-	0.5	Vp-p	1
	After Aging		IBL A	-	6.7	(7.2)	А	V _{BR-A} = 1.65V j 1
Power Supply				-	7.5	(8)	A	V _{BR-A} = 3.3V j 1
Input Current	Before Aging		IBL B	-	7.2	(7.7)	A	V _{BR-A} = 1.65V j 2
		'9		-	8	(8.5)	A	V _{BR-A} = 3.3V j 2
Power Supply Inpu	Power Supply Input Current (In-Rush)		Irush	-	-	(9.6)	A	V _{BL} = 22.8V V _{BR-B} = 3.3V V _{BR-A} = 1.65V
Power Consumptio	n		PBL	-	160	(172)	W	V _{BR-A} = 1.65V j 1
	Brightness	Adjust	Vbr-a	0.0	1.65	3.3	Vdc	
Input Voltage for Control System	On/Off	On	V on	2.5	-	5.0	Vdc	
Signals		Off	V off	-0.3	0.0	0.8	Vdc	
	Brightness Adjust		Vbr-b	0	-	3.3	V	3
Lamp:	Lamp:							
Discharge Stabili	Discharge Stabilization Time		Ts			3	min	4
Life Time	-			50,000			Hrs	5

Notes :

Electrical characteristics are determined after the unit has been ¡ON¡ and stable for approximately 120 minutes at 25¡ 2¡C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A : 1.65V & VBR-B :3.3V), it is total power consumption.

The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is 24.0V j 5%.

- 2. Electrical characteristics are determined within 30 minutes at 25; 2;C. The specified currents are under the typical supply Input voltage 24V.
- 3. Brightness Control.

This VBR-B Voltage control brightness.

Vвк-в Voltage	Function	Vвк-в Voltage	Function
0V	Minimum Duty (20%)	3.3V	Maximum Duty (100%)

4. The brightness of the lamp after lighted for 5minutes is defined as 100%. T_S is the time required for the brightness of the center of the lamp to be not less than 95% at typical current. The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.

5. Specified Values are for a single lamp which is aligned horizontally. The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A : 1.65V & VBR-B :3.3V), on condition of continuous operating at 25 ± 2¡C

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and Master 14-pin and Slave 12-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose)

- Mating Connector : FI-R51HL(JAE) or compatible

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	Reserved	No connection or GND
2	NC	No Connection	28	RE0N	SECOND CHANNEL 0-
3	NC	No Connection	29	RE0P	SECOND CHANNEL 0+
4	NC	No Connection	30	RE1N	SECOND CHANNEL 1-
5	NC	No Connection	31	RE1P	SECOND CHANNEL 1+
6	NC	No Connection	32	RE2N	SECOND CHANNEL 2-
7	LVDS Select	iHi =JEIDA , iLi = VESA	33	RE2P	SECOND CHANNEL 2+
8	VBR_EXT	External VBR	34	GND	Ground
9	VBR_OUT	VBR output	35	RECLKN	SECOND CLOCK CHANNEL C-
10	DCR_Enable	¡H¡ = Enable , ¡L¡ = Disable	36	RECLKP	SECOND CLOCK CHANNEL C+
11	GND	Ground	37	GND	Ground
12	RO0N	FIRST CHANNEL 0-	38	RE3N	SECOND CHANNEL 3-
13	R00P	FIRST CHANNEL 0+	39	RE3P	SECOND CHANNEL 3+
14	RO1N	FIRST CHANNEL 1-	40	Reserved (NC)	No Connection
15	RO1P	FIRST CHANNEL 1+	41	Reserved (NC)	No Connection
16	RO2N	FIRST CHANNEL 2-	42	Reserved	No connection or GND
17	RO2P	FIRST CHANNEL 2+	43	Reserved	No connection or GND
18	GND	Ground	44	GND	Ground
19	ROCLKN	FIRST CLOCK CHANNEL C-	45	GND	Ground
20	ROCLKP	FIRST CLOCK CHANNEL C+	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	RO3N	FIRST CHANNEL 3-	48	VLCD	Power Supply +12.0V
23	RO3P	FIRST CHANNEL 3+	49	VLCD	Power Supply +12.0V
24	Reserved (NC)	No Connection	50	VLCD	Power Supply +12.0V
25	Reserved (NC)	No Connection	51	VLCD	Power Supply +12.0V
26	Reserved	No connection or GND	-	-	-

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Notes :

- 1. The pin no 44 is LCD Test option. ¡AGP; (Auto Generation LCM operates Pattern) or ¡NSB; (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 volt power supply. LPL recommends iNSBi. (AGP : iVCCi or iOPENi / NSB : iGNDi) 2. All GND(ground) pins should be connected together to the LCD module is metal frame.
- 3. All VLCD (power input) pins should be connected together.
- 4. All Input levels of LVDS signals are based on the IEA 664 Standard.
- 5. Specific pins(pin No. #1~#10) are used for internal data process of the LCD module. If not used, these pins are no connection.
- 6. If DCR function should be enable(¡Hi), 10th pin must be connected to serial resistor which value is under 1k ohm.

3-2-2. Backlight Inverter

Slave

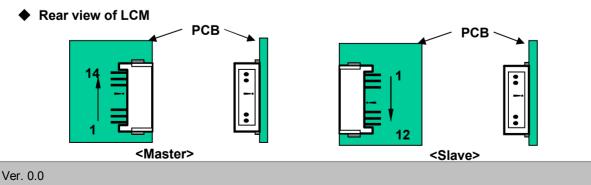
-Inverter Connector : S12B-PH-SMC (JST) or Equivalent -Mating Connector : PHR-12 or Equivalent

Table 5. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Master	Slave	Note
1	VBL	Power Supply +24.0V	VBL	VBL	
2	VBL	Power Supply +24.0V	VBL	VBL	
3	VBL	Power Supply +24.0V	VBL	VBL	
4	VBL	Power Supply +24.0V	VBL	VBL	
5	VBL	Power Supply +24.0V	VBL	VBL	
6	GND	Backlight Ground	GND	GND	
7	GND	Backlight Ground	GND	GND	
8	GND	Backlight Ground	GND	GND	1
9	GND	Backlight Ground	GND	GND	
10	GND	Backlight Ground	GND	GND	
11	VBR-A	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	VBR-A	Don _i t care	2, 3
12	VON/OFF	0.0V ~ 5.0V	On/Off	Don _i t care	
13	Vbr-B	Burst dimming voltage DC 0.0V ~ 3.3V	VBR-B	-	3
14	Status	Normal : Upper 3.0V Abnormal : Under 0.7V	Status	-	4

Notes : 1. GND should be connected to the LCD module is metal frame.

- If Pin #11 is open, VBR-A = 1.65V. When apply over 1.65V(~ 3.3V) continuously, its luminance is increasing however lamps life time is decreasing.
 It could be usable for boost up luminance when using DCR (=Dynamic contrast ratio) function only.
- 3. Minimum Brightness : VBR-B =0V Maximum Brightness : VBR-B = 3.3V
- 4. Even though Pin #14 is open, there is no effect on inverter operating. The output terminal of inverter.
- 5. Each impedance of pin #11,12 and 13 is $112[M\Omega]$, $\infty[M\Omega]$, $112[M\Omega]$



Product Specification

3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

I	ITEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	(100)	140	(320)	tclk	
	Total	tHP	(1060)	1100	(1280)	tclk	2200/2
	Display Period	t∨∨	-	1080	-	Lines	
Vertical	Blank	tvв	(11)	45	(69)	Lines	
	Total	tvp	(1091)	1125	(1149)	Lines	
	TEM	Symbol	Min	Тур	Max	Unit	Note

Table 6. TIMING TABLE for NTSC (DE Only Mode)

	Total	tvp	(1091)	1125	(1149)	Lines	
п	EM	Symbol	Min	Тур	Мах	Unit	Note
	DCLK	fclk	(71.55)	74.25	(77)	MHz	148.5/2
Frequency	Horizontal	fн	(65.5)	67.5	(68.9)	KHz	
	Vertical	f∨	(57)	60	(63)	Hz	

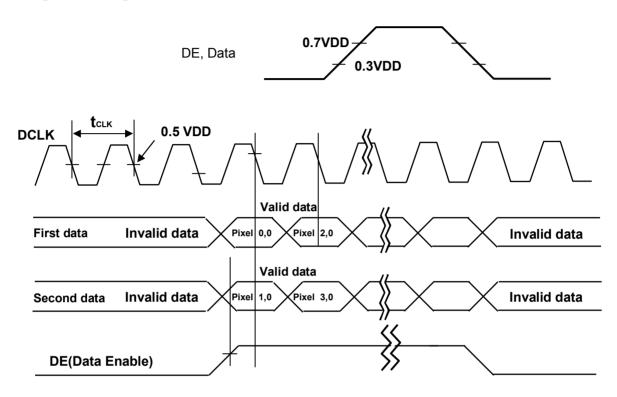
Table 7 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

I	ТЕМ	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	(100)	140	(240)	tclk	
	Total	tHP	(1060)	1100	(1200)	tclk	2200/2
	Display Period	t∨v	-	1080	-	Lines	
Vertical	Blank	tvв	(25)	45	(65)	Lines	
	Total	tvp	(1105)	1125	(1145)	Lines	
I	TEM	Symbol	Min	Тур	Max	Unit	Note
	DCLK	fclk	(60.64)	61.88	(63.13)	MHz	123.75/2
Frequency	Horizontal	fн	(55.25)	56.25	(57.25)	KHz	
	Vertical	f∨	(49)	50	(51)	Hz	

Table 7. TIMING TABLE for PAL (DE Only Mode)

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). <u>The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.</u>

3-4. Signal Timing Waveforms

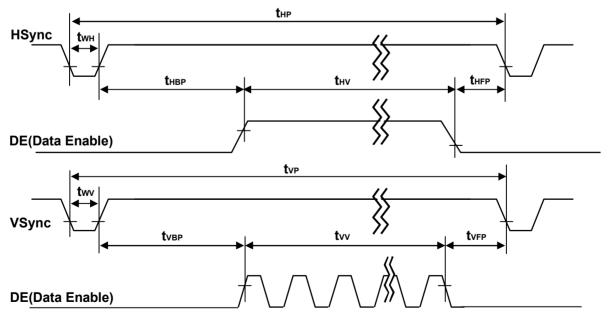


Product Specification

* Reference : Sync. Relation

* tнв = tнгр + twн +tнвр

* $t_{VB} = t_{VFP} + t_{WV} + t_{VBP}$



Ver. 0.0

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

												Inpu	ut Co	olor	Data	a		-							
	Color				RE	ED							GRE	EEN							BL	UE			
			SB							MS								MS							SB
		_		R5																					
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1		0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1		1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0		1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000) Dar	< 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dar	k 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) Dar	< 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Product Specification

3-6. Power Sequence

3-6-1. LCD Driving circuit

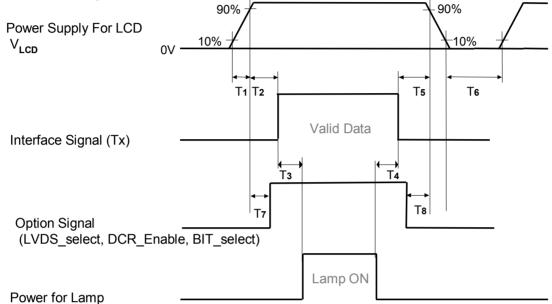


Table 9. POWER SEQUENCE

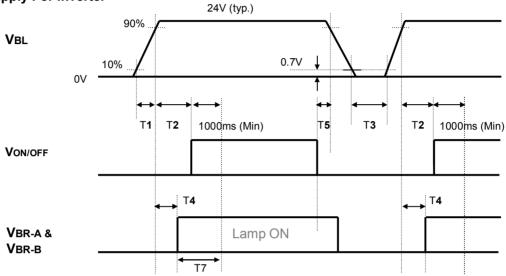
Deremeter		Value		l locit	Natas
Parameter	Min	Тур	Max	Unit	Notes
T1	0.5	-	20	ms	
T2	0.5	-	3 x (1/f _V)	ms	3,5
Т3	200	-	-	ms	4
T4	200	-	-	ms	4
T5	0	-	-	ms	3,5
Т6	2.0	-	-	s	2,6
T7	0	-	T2	ms	5
Т8	0	-	-	ms	5

Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
- 3. The case when the T2/T5 exceed 3x(1/fv), it operates protection pattern (Black pattern) till valid signal inputted. There is no reliability problem. (ex. 60Hz : 3x(1/60Hz) = 50ms)
- 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 5. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V_{LCD}), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
- 6. T6 should be measured after the Module has been fully discharged between power off and on period.

3-6-2. Sequence for Inverter





3-6-3. Deep condition for Inverter

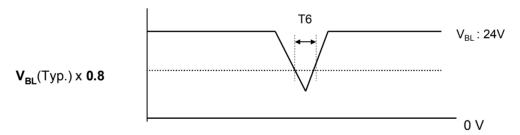


Table 10. Power Sequence for Inverter

Parameter		Values		Units	Remarks		
Parameter	Min	Тур	Max	Units	Remarks		
T1	20	-	-	ms	1		
T2	500	-	-	ms			
Т3	200	-	-	ms			
T4	0		-	ms	2		
T5	10	-	-	ms			
Т6	-	-	10	ms	V_{BL} (Тур) х 0.8		
Τ7	1000	-	-	ms	3		

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

2. T4(max) is less than T2.

3. In T7 section, VBR-B is recommended 3.3V.

4. Optical Specification

Optical characteristics are determined after the unit has been iON_i and stable in a dark environment at 25i 2iC. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 i.

FIG. 1 shows additional information concerning the measurement equipment and method.

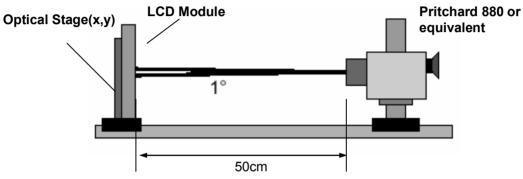


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 11. OPTICAL CHARACTERISTICS

Ta= 25j 2jC, V_{LCD}=12.0V, fv=60Hz, Dclk=148.5MHz VBR_A=1.65V, VBR_B=3.3V

			1 1 - ,	208			-	—
	oromol	for	Symphol		Value		Lipit	Noto
P	aramet	ler	Symbol	Min	Тур	Max	Unit	Note
Contrast Rati	0		CR	700	1000	-		1
Surface Lumi	nance,	white	L _{WH}	400	500	-	cd/m ²	2
Luminance V	ariation		δ _{WHITE} 5P	-	-	1.3		3
Response Tir	ne	Gray-to-Gray	G to G	-	5	8	ms	4,5
		RED	Rx		(0.640)			
			Ry		(0.343)			
		GREEN	Gx		(0.287)			
Color Coordir	nates	GREEN	Gy	Тур	(0.613)	Тур		
[CIE1931]		BLUE	Bx	-0.03	(0.144)	+0.03		
		BLUL	Ву		(0.065)			
		WHITE	Wx		0.279			
		VVI II I L	Wy		0.292			
Viewing Angl	e (CR>′	10)						
	x axis,	right(∳=0¡)	θr	89	-	-		
	x axis, l	left (φ=180¡)	θl	89	-	-		0
	y axis,	up (_{\$=90i})	θu	89	-	-	degree	6
	y axis,	down (_{\$=270} ;)	θd	89	-	-		
Gray Scale				-	-	-		7

LC420WU5

Product Specification

Notes :1. Contrast Ratio(CR) is defined mathematically as :

- CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
 - CRn = Surface Luminance at position n with all white pixels
 - Surface Luminance at position n with all black pixels

n = the Position number (1, 2, 3, 4, 5). For more information, see FIG 2.

- Surface luminance are determined after the unit has been ¡ON¡ and 30min after lighting the backlight in a dark environment at 25¡ 2¡C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5})

Where $L_{_{on1}}$ to $L_{_{on5}}$ are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 12.

Gray Level	Luminance [%] (Typ.)					
LO	(0.19)					
L15	(0.39)					
L31	(1.16)					
L47	(2.61)					
L63	(4.80)					
L79	(7.77)					
L95	(11.6)					
L111	(16.2)					
L127	(21.7)					
L143	(28.2)					
L159	(35.5)					
L175	(43.8)					
L191	(53.0)					
L207	(63.3)					
L223	(74.5)					
L239	(86.7)					
L255	100					

Table 12. GRAY SCALE SPECIFICATION

Measuring point for surface luminance & measuring point for luminance variation.

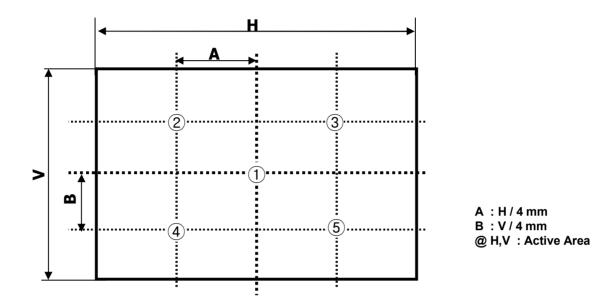


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for $_iGray(N)_i$ and $_iGray(M)_i$.

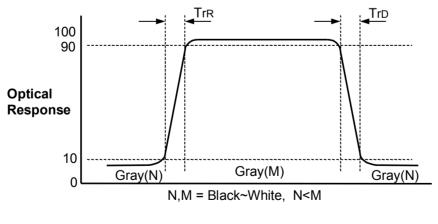


FIG. 3 Response Time

Dimension of viewing angle range

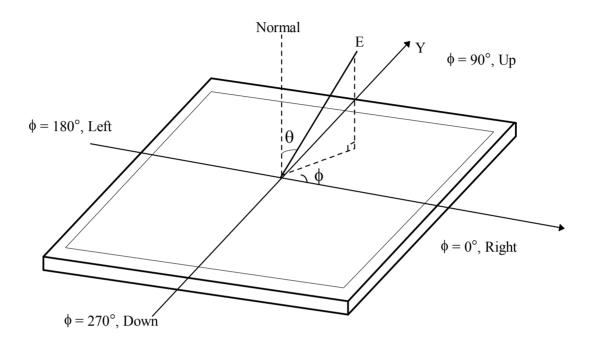


FIG. 4 Viewing Angle

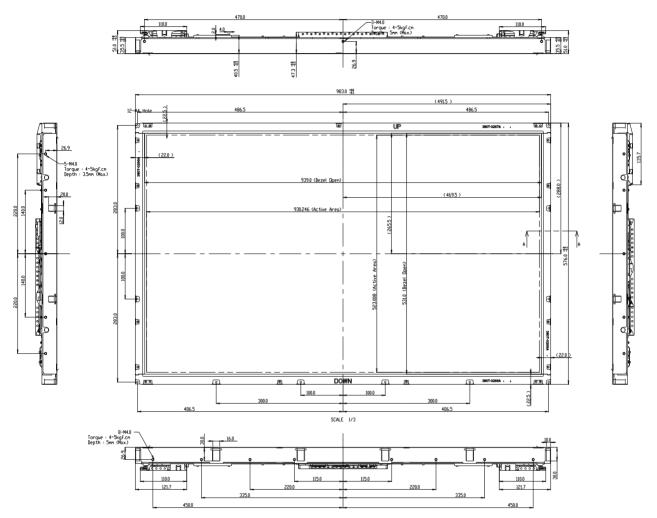
5. Mechanical Characteristics

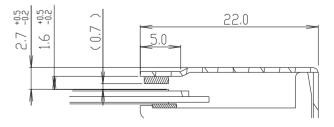
Table 13 provides general mechanical characteristics.

Item	Value		
	Horizontal	983.0 mm	
Outline Dimension	Vertical	576.0 mm	
	Depth	51.0 mm	
Densi Area	Horizontal	939.0 mm	
Bezel Area	Vertical	531.0 mm	
Active Display Area	Horizontal	930.25 mm	
Active Display Area	Vertical	523.01 mm	
Weight	(11.5) Kg (Typ.) , (12.5) Kg (Max.)		

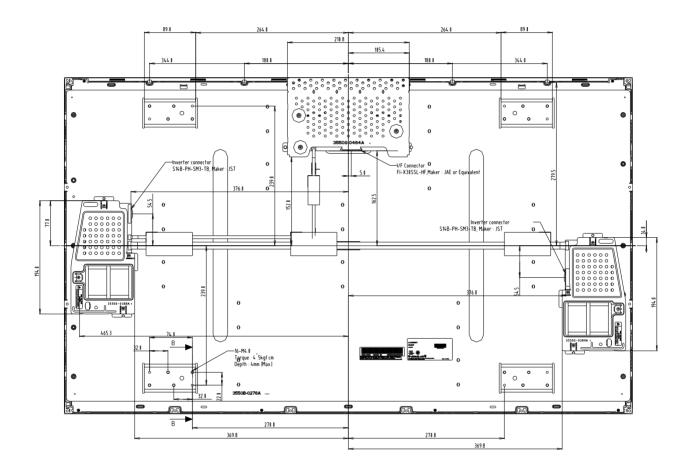
Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

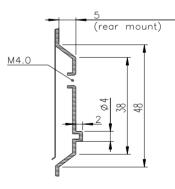
<FRONT VIEW>





<REAR VIEW>





SECTION B-B SCALE 1/1

6. Reliability

Table 14. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60¡C 240h
2	Low temperature storage test	Ta= -20¡C 240h
3	High temperature operation test	Ta= 50¡C 50%RH 240h
4	Low temperature operation test	Ta= 0¡C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : ¡ X, ¡ Y, ¡ Z One time each direction
7	Humidity condition Operation	Ta= 40 ¡C ,90%RH

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
- b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
- c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus..

7-2. EMC

- a) ANSI C63.4 ¡Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. ¡American National Standards Institute(ANSI), 1992
- b) CISPR22 ¡Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.¡ International Special Committee on Radio Interference.
- c) EN 55022 ¡Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.; European Committee for Electro technical Standardization.(CENELEC), 1998 (Including A1: 2000)

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)

D : YEAR

F : PANEL CODE

H : ASSEMBLY CODE

E : MONTH G : FACTORY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR										
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one Pallet : 12 pcs
- b) Pallet Size : 1150 mm X 1020 mm X 815 mm.

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer.
- Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=i 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
- And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM canit be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.

Product Specification

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And dont touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5_iC and 35_iC at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

APPENDIX-I-1

Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter(Pin7=i Li)

Host System 24 Bit	THC63LVD823 or Compatible	FI-R51S-HF	Timing Controller
R10/R20 R11/R21 R12/R22 R13/R23 R14/R24 R15/R25 R16/R26 R17/R27 G10/G20 G11/G21 G12/G22 G13/G23 G14/G24 G15/G25 G16/G26 G17/G27 B10/B20 B11/B21 B12/B22 B13/B23 B14/B24 B15/B25	54/02 TA1-TA1+2 57/83 TA1-TA1+2 58/84 TB1-/TB1+2 59/85 2 60/86 TC1-/TC1+2 51/79 TCLK1-2 52/80 TCLK1-2 63/91 TD1-/TD1+2 64/92 65/93 66/94 TA2-/TA2+2 67/95 68/96 61/89 TC2-/TC2+2 73/99 TCLK2-7	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Rx00- Rx01- Rx01- Rx02- Rx02+ Rx0CLK- Rx03- Rx03- RxE0- RxE1- RxE1- RxE2- RxE3- RxE3- VESA / JEID/
B16/B26 B17/B27 Hsync Vsync Data Enable CLOCK	69/97 70/98 7 8 9 10	Ground LCD	Module

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. j7 means MSB and j0 means LSB at R,G,B pixel data.

APPENDIX-I-2

Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter(Pin7=; H;)

Host System	THC63LVD823	FI-R51S-HF	Timing
24 Bit	or Compatible		Controller
R10/R20 R11/R21 R12/R22 R13/R23 R14/R24 R15/R25 R16/R26 R17/R27 G10/G20 G11/G21 G12/G22 G13/G23 G14/G24 G15/G25 G16/G26 G17/G27 B10/B20 B11/B21 B12/B22 B13/B23 B14/B24 B15/B25 B16/B26 B17/B27 Hsync Vsync Data Enable	51/79 52/80 53/81 TA1-TA1+ 53/81 TA1-TA1+ 54/82 TB1-/TB1+ 57/83 58/84 TC1-/TC1+ 4 59/85 TCLK1- 60/86 TCLK1+ 61/89 TD1-/TD1+ 62/90 TD1-/TD1+ 62/90 TD1-/TD1+ 62/90 TA2-/TA2+ 65/93 TA2-/TA2+ 66/94 TB2-/TB2+ 67/95 68/96 TC2-/TC2+ 69/97 TCLK2- 70/98 TCLK2+ 70/98 TCLK2+	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	RxO0- RxO1- RxO1- RxO1- RxO2- RxO2+ RxO2K- RxO2K- RxO2K- RxO3- RxO3+ RxE0- RxE0- RxE1- RxE1- RxE1+ RxE2- RxE2+ RxECLKIN- RxE2+ RxECLKIN- RxE3- RxE3+ VESA / JEID A DCR Enable

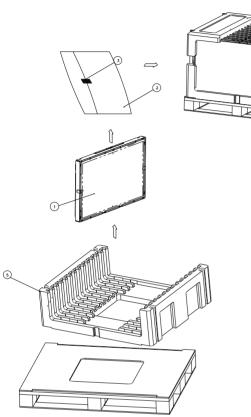
Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. j7 i means MSB and j0 i means LSB at R,G,B pixel data.

Product Specification

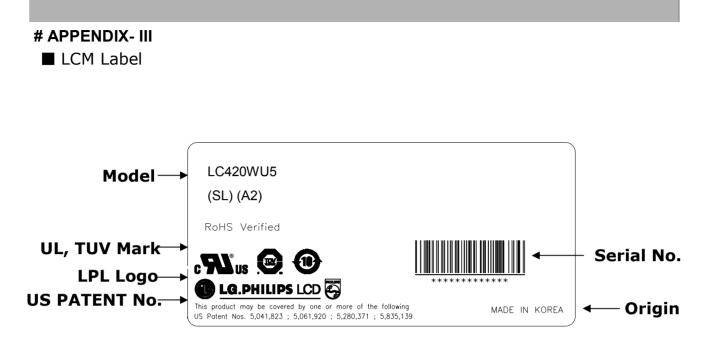
APPENDIX-II

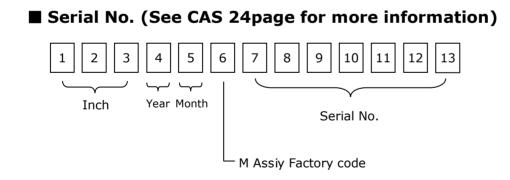
Pallet Assiy



And a state of the	

NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	42INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	PAPER 1140X990X130MM
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,POST	PAPER
8	ANGLE, PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO 80G 100X100





APPENDIX- IV ■ Pallet Label

LC			
	SLA	2	
12 PCS	001/01-01		
MADE I			
XXXX	x xxxxxxx xxx x	XXX XXXX	
-	100.0		>