SPECIFICATION FOR APPROVAL

()	Final	Specif	ficatior
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Title	32.0" WXGA TFT LCD				
BUYER		SUPPLIER	LG.Philips LCD Co., Ltd.		
MODEL		*MODEL	LD320WXN		
		SUFFIX	SAA1		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for your o	confirmation with
your signature and cor	nments.

APPROVED BY	SIGNATURE DATE
REVIEWED BY	
PREPARED BY	
TV Product Developm LG. Philips LCD Co	

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.1	Nov, 15, 2007	-	Preliminary Specification(First Draft)

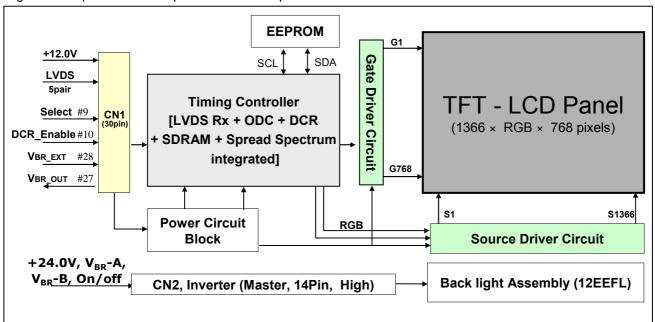
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1. General Description

The LD320WXN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.51 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	31.51 inches(800.4mm) diagonal
Outline Dimension	760.0 mm(H) x 450.0 mm(V) x 48.0 mm(D) (Typ.)
Pixel Pitch	170.25/ [⊥] x 510.75/ [⊥] x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8bit, 16,7 M colors
Luminance, White	500 cd/m² (Center 1 point) (Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total TBD (Typ.) (Logic= TBD W, Lamp= TBD W [VBR-A=1.65V])
Weight	6,150g(Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), anti-glare treatment of the front polarizer (Haze 13%)

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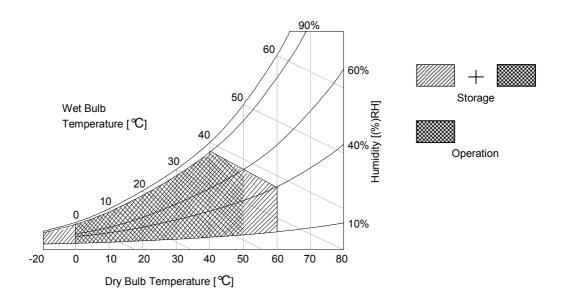
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Value		lue	Lloit	Remark
		Symbol	Min	Max	Unit	Remark
Power Input	LCM	VLCD	-0.3	+14.0	VDC	at 25 ± 2 ℃
Voltage	Backlight inverter	VBL	-0.3	+27.0	VDC	
ON/OFF Conf	ON/OFF Control Voltage		-0.3	+5.5	VDC	
Brightness Co	Brightness Control Voltage		0	+5.0	VDC	
Operating Ter	Operating Temperature		0	+50	$_{\mathbb{C}}$	
Storage Tem	Storage Temperature		-20	+60	℃	Note 4
Operating Ambient Humidity		Нор	10	90	%RH	Note 1
Storage Humidity		Нѕт	10	90	%RH	

Notes: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 ℃ Max. and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit.

The other Is used for the EEFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note		
rarameter	Cymbol	Min	Тур	Max	Offic	14010	
Circuit :							
Power Input Voltage	V _{LCD}	11.4	12.0	12.6	V _{DC}		
Power Input Current		-	TBD	TBD	mA	1	
Power Input Current	I _{LCD}	-	TBD	TBD	mA	2	
Power Consumption	P _{LCD}	-	TBD	TBD	Watt	1	
Rush current	I _{RUSH}	-	-	3.0	Α	3	

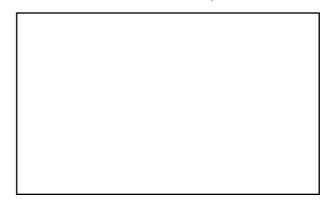
Notes : 1. The specified current and power consumption are under the V_{LCD} =12.0V, 25 \pm 2 °C, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

2. The current is specified at full white pattern.

White: 255 Gray

3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).

Black : 0 Gray



White: 255 Gray

Mosaic Pattern(8 x 6)

Full White pattern

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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Cymhol	Values			Lloit	Notes		
raiailletei			Symbol	Min	Тур	Max	Unit	Notes	
Inverter :									
Power Supply Input	Power Supply Input Voltage		VBL	22.8	24.0	25.2	Vdc	1	
Power Supply Input	t Voltage Rip	ple		-	-	0.5	Vp-p	1	
	After Aging		IDI. A	-	3.5	3.85	Α	V _{BR-A} = 1.65V 1	
Power Supply	After Aging		IBL_A	-	3.6	4.0	Α	V _{BR-A} = 3.3V 1	
Input Current	Roforo Agir	ng.	IBL_B	-	4.0	4.5	Α	V _{BR-A} = 1.65V 2	
	Belore Agii	Before Aging		-	4.5	5.0	Α	V _{BR-A} = 3.3V 2	
Power Supply Input	Power Supply Input Current (In-Rush)		Irush	-	-	TBD	Α	$V_{BL} = 22.8V$ $V_{BR-B} = 3.3V$ $V_{BR-A} = 1.65V$	
Power Consumption	n		PBL	-	TBD	TBD	W	V _{BR-A} = 1.65V 1	
	Brightness	Adjust	VBR-A	0.0	1.65	3.3	Vdc		
Input Voltage for Control System	On/Off	On	V on	2.5	-	5.0	Vdc		
Signals	On/On	Off	V off	-0.3	0.0	0.8	Vdc		
Brightness Adjust		V _{BR-B}	0	-	3.3	V	3		
Lamp:									
Life Time				50,000			Hrs	4	

Notes:

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25± 2 °C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A: 1.65V & VBR-B: 3.3V), it is total power consumption.

The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is 24.0V + 5%.

- 2. Electrical characteristics are determined within 30 minutes at 25± 2 ℃. The specified currents are under the typical supply Input voltage 24V.
- Brightness Control.
 This VBR-B Voltage control brightness.

VBI	R-в Voltage	Function	Vвк-в Voltage	Function	
	0V	Minimum Duty (20%)	3.3V	Maximum Duty (100%)	

- 4. The brightness of the lamp after lighted for 5minutes is defined as 100%.
 T_S is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
 The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
- 5. Specified Values are for a single lamp which is aligned horizontally. The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A: 1.65V & VBR-B: 3.3V), on condition of continuous operating at $25 \pm 2^{\circ}$ C
- 6. The duration of rush current is about 20ms.
- 7. VBR-A can be temporarily used upper than 1.65V for the Al function. Otherwise, VBR-A is recommended 1.65V typically.

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3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and One connectors(14-pin) are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-X30SSL-HF (Manufactured by JAE) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
7	GND	Ground	
9	Select	Select LVDS Data format	1
10	DCR Enable	Dynamic CR Enable ('L ' = Disable , 'H' = Enable)	2
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	VBR_OUT	VBR output form LCD module	
28	VBR_EXT	External VBR input from System to LCD module	
29	Reserved	Low: Normal Operating High: Interlace Free Mode	
30	GND	Ground	3

- Notes: 1. The pin no 9 is an option pin for DISM or LG format. (VESA Format = "GND" / JEIDA Format = "VCC")

 Please refer to Appendix for further details.
 - 2. The pin no 10 is an option pin for DCR Function (Enable = "VCC" / Disable = "GND")
 - 3. The pin no 30 is LCD Test option.
 - LCM operates "AGP" (Auto Generation Pattern) or "NSB" (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 volt power supply. LPL recommends "NSB". (AGP : "VCC" or "OPEN" / NSB : "GND")
 - 4. All GND (ground) pins should be connected together, which should be also connected to the LCD module's metal frame.
 - 5. All VLCD (power input) pins should be connected together.
 - 6. Input Levels of LVDS signals are based on the IEA 664 Standard.

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3-2-2. Backlight Inverter

Master

-Inverter Connector: 20022WR-14B1

(manufactured by Yeon-Ho) or Equivalent

- Mating Connector : PHR-14 or Equivalent

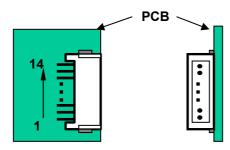
Table 7. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Master	Note
1	VBL	Power Supply +24.0V	VBL	
2	VBL	Power Supply +24.0V	VBL	
3	VBL	Power Supply +24.0V	VBL	
4	VBL	Power Supply +24.0V	VBL	
5	VBL	Power Supply +24.0V	VBL	
6	GND	Backlight Ground	GND	
7	GND	Backlight Ground	GND	
8	GND	Backlight Ground	GND	1
9	GND	Backlight Ground	GND	
10	GND	Backlight Ground	GND	
11	VBR-A	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	VBR-A	2, 3
12	VON/OFF	0.0V ~ 5.0V	On/Off	
13	VBR-B	Burst dimming voltage DC 0.0V ~ 3.3V	VBR-B	3
14	Status	Normal : Upper 3.0V Abnormal : Under 0.7V	Status	4

Notes: 1. GND should be connected to the LCD module's metal frame.

- If Pin #11 is open, VBR-A = 1.65V. When apply over 1.65V(~ 3.3V) continuously, its luminance is increasing however lamp's life time is decreasing.
 It could be usable for boost up luminance when using DCR (=Dynamic contrast ratio) function only.
- 3. Minimum Brightness: VBR-B = 0V Maximum Brightness: VBR-B = 3.3V
- 4. Even though Pin #14 is open, there is no effect on inverter operating, The output terminal of inverter.
- 5. Each impedance of pin #11,12 and 13 is TBD[$M\Omega$], TBD[$M\Omega$], TBD[$M\Omega$]

Rear view of LCM



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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE (DE Only Mode)

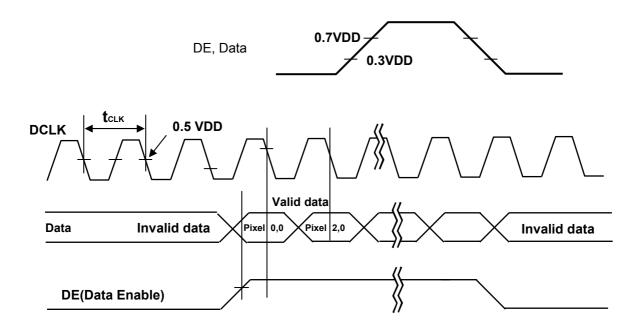
l	TEM	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	f _{CLK}	63	72.3	80	MHz	
	Frequency	f _H	39	47.4	53	KHZ	
Hsync	Display Valid	t _{HV}	1366	1366	1366	Clks	
	Blank	t _{HT-} t _{HV}	80	162	474	Clks	
	Total	t _{HT}	1446	1528	1840	Clks	
	Frequency	f _V	47	60	63	HZ	
	Display Valid	t _{VV}	768	768	768	Lines	PAL :47~53Hz,
Vsync	Blank	t _{VT} -t _{VV}	7	22	295	Lines	NTSC : 57~63Hz
	Total	t _{VT}	775	790	1063	Lines	

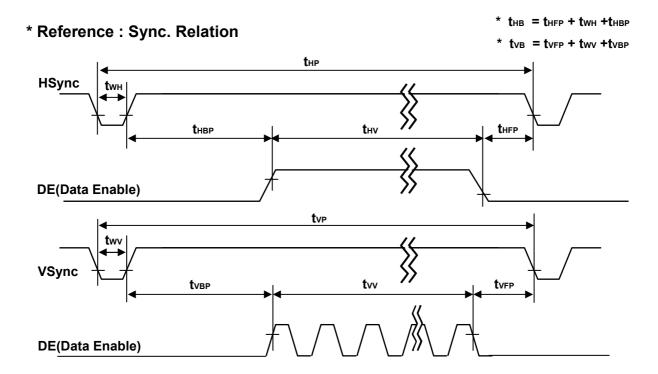
Note: 1. The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).

If you use spread spectrum for EMI, add some additional clock to minimum value for clock margin.

- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency.
- 3. Timing should be set based on clock frequency.

3-4. Signal Timing Waveforms





3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

													lnpι	ıt Co	olor	Data	а									
	Color			_		RE	D			_		_		GRE	EEN	l				_		BL	UE			
			MS							-	MS								MS							SB
	I								R1 F	-				G4										B2		
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
										Î																
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) [Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN										Î																
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) [Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE										Ì																
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

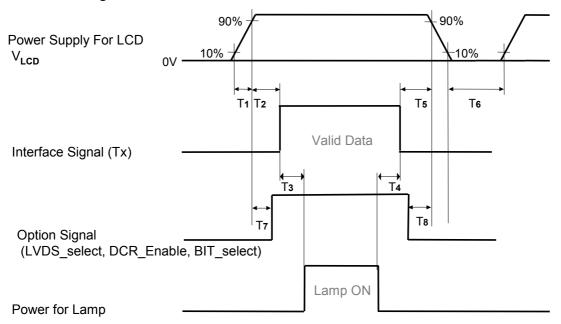


Table 9. POWER SEQUENCE

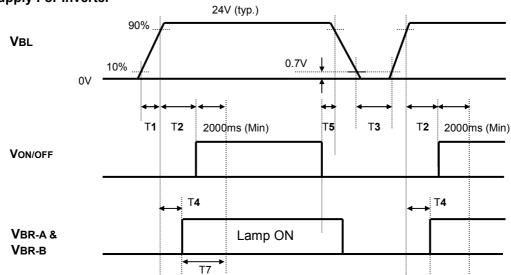
Devementes		l lait	Notos		
Parameter	Min	Тур	Max	Unit	Notes
T1	0.5	-	20	ms	
T2	0.5	-	3 x (1/f _V)	ms	3,5
Т3	200	-	-	ms	4
T4	200	-	-	ms	4
T5	0	-	-	ms	3,5
T6	2.0	-	-	S	2,6
T7	0	-	T2	ms	5
Т8	0	-	T5	ms	5

Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
- 3. The case when the T2/T5 exceed 3x(1/fv), it operates protection pattern (Black pattern) till valid signal inputted. There is no reliability problem. (ex. 60Hz : 3x(1/60Hz) = 50ms)
- 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 5. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V_{LCD}), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
- 6. T6 should be measured after the Module has been fully discharged between power off and on period.

3-6-2. Sequence for Inverter

Power Supply For Inverter



3-6-3. Deep condition for Inverter

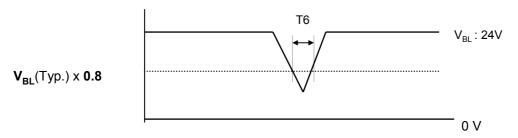


Table 12. Power Sequence for Inverter

Dorameter		Values		Linita	Pomorko	
Parameter	Min	Тур	Max	Units	Remarks	
T1	20	-	-	ms	1	
T2	500	-	-	ms		
Т3	200	-	-	ms		
T4	0		-	ms	2	
T5	10	-	-	ms		
T6	-	-	10	ms	V _{BL} (Typ) x 0.8	
T7	2000	-	-	ms	3	

Note: 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time

- 2. T4(max) is less than T2.
- 3. In T7 section, V_{BR} -B should be max level(3.3V) and V_{BR} -A should be 1.65V.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and for 30 minutes in a dark environment at $25\pm2\,^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

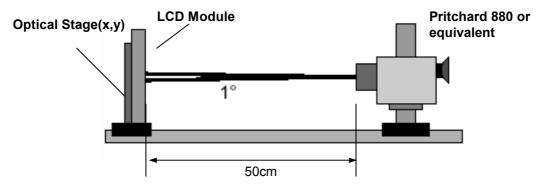


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= $25\pm 2^{\circ}$ C, V_{LCD}=12.0V, fv=60Hz, Dclk=72.4MHz, VBR-A =1.65V, VBR-B =3.3V

		1a- 25± 2 0, V _{[0}	, ,	Value	,	,	
Parar	Parameter		Min	Тур	Max	Unit	Note
Contrast Ratio (TE	ontrast Ratio (TBD)		TBD	900	-		1
Surface Luminano		L _{WH}	400	500		cd/m ²	2
Luminance Variati			-	-	1.3		3
Response Time		δ _{WHITE} 5P G to G	-	9	14	ms	4
		Rx		TBD			TBD
	RED	Ry		TBD			TBD
	ODEEN	Gx		TBD			TBD
Color Coordinates	GREEN	Gy	Тур	TBD	Тур		TBD
[CIE1931]	DLUE	Bx	-0.03	TBD	+0.03		TBD
	BLUE	Ву		TBD			TBD
	WHITE	Wx		TBD			
	VVIIIE	Wy		TBD			
Viewing Angle (Cl	R>10)						
x ax	s, right(φ=0°)	θr	89		-		
x ax	s, left (φ=180°)	θΙ	89		-		_
y ax	s, up (φ=90°)	θи	89		-	degree	5
y ax	s, down (φ=270°)	θd	89		-		
Gray Scale				2.2			6

Notes: 1. Contrast Ratio(CR) is defined mathematically as:

CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)

Surface Luminance at position n with all white pixels

Surface Luminance at position n with all black pixels

n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.

- 2. Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25± 2℃. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : $\delta \, \text{WHITE(5P)} = \text{Maximum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on1}}, \text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on2}}, \, \text{L}_{\text{on3}}, \, \text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on4}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}) \, / \, \text{Minimum}(\text{L}_{\text{on5}}, \, \text{L}_{\text{on5}}, \, \text{L$

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. Gray scale specification
 Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
LO	TBD
L15	TBD
L31	TBD
L47	TBD
L63	TBD
L79	TBD
L95	TBD
L111	TBD
L127	TBD
L143	TBD
L159	TBD
L175	TBD
L191	TBD
L207	TBD
L223	TBD
L239	TBD
L255	TBD

Measuring point for surface luminance & measuring point for luminance variation.

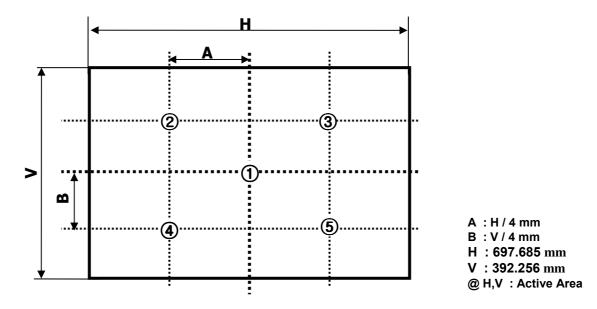


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

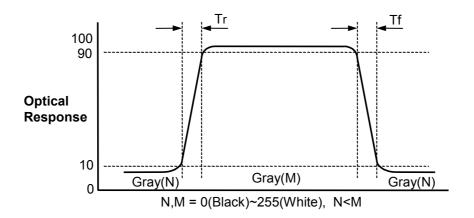


FIG. 3 Response Time

Dimension of viewing angle range

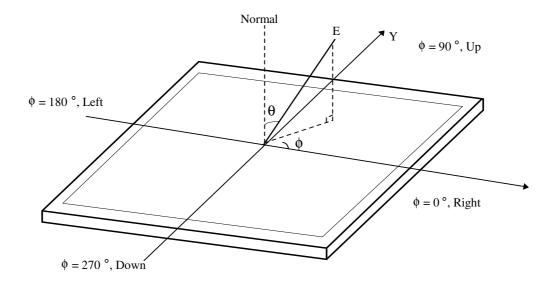


FIG. 4 Viewing Angle

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5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

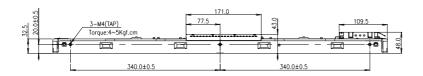
Table 12. MECHANICAL CHARACTERISTICS

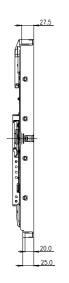
Item		Value			
	Horizontal	760.0mm			
Outline Dimension	Vertical	450.0 mm			
	Depth	48.0 mm			
Down Avon	Horizontal	703.8mm			
Bezel Area	Vertical	398.4mm			
Active Display Area	Horizontal	697.685mm			
Active Display Area	Vertical	392.256mm			
Weight	6,150 g(Typ.), 6,770g(Max)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer(13%)				

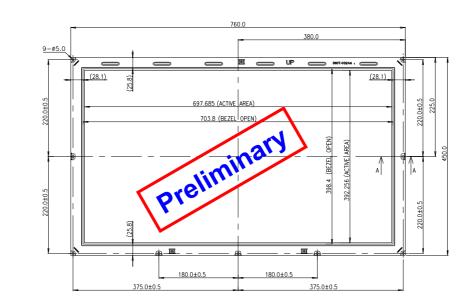
Note: 1.Please refer to a mechanic drawing in terms of tolerance at the next page.

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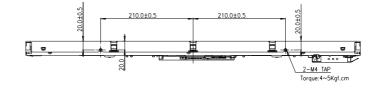
<FRONT VIEW>

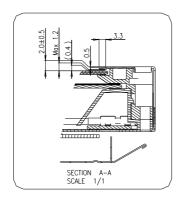




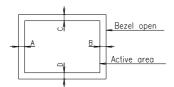






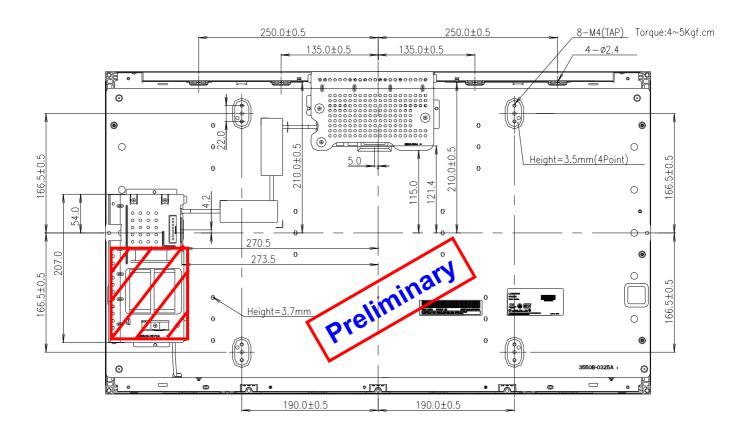


- NOTES
 1. Unspecified tolerances are to be ±1.0mm.
 2. Tilt and partial disposition tolerance of display area are as following.
 (1) X-Direction: IA-BI ≤ 1.5mm
 (2) Y-Direction: IC-DI ≤ 1.5mm



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<REAR VIEW>



Notes: It should be recommended that any exterior materials do not go passing up the red area slanted. (For example, electrical cable, system board, etc.). Otherwise, it could cause that abnormal display happens.

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6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60 ℃ 240h
2	Low temperature storage test	Ta= -20℃ 240h
3	High temperature operation test	Ta= 50℃ 50%RH 240h
4	Low temperature operation test	Ta= 0 ℃ 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : $100G$ Waveform : half sine wave, $2ms$ Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	ESD test	Condition : 150pF, 330 ohm Case , air Evaluation : ± 15kV
8	Humidity condition Operation	Ta= 40 ℃ 90%RH 240h
9	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)

Note: Before and after Reliability test, LCM should be operated with normal function.

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7. International Standards

7-1. Safety

a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc.,

Standard for Audio, Video and Similar Electronic Apparatus.

b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association,

Standard for Audio, Video and Similar Electronic Apparatus.

c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002,

Safety requirements for Audio, Video and Similar Electronic Apparatus..

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
 - CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
 - EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L
--

A,B,C: SIZE(INCH) D:YEAR

E: MONTH F~ M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

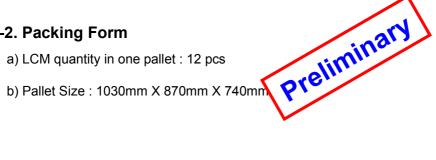
2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form



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9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.

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9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 ℃ and 35 ℃ at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

9-7. Appropriate Condition for Public Display

- Generally large-sized LCD modules are designed for consumer applications (TV).

 Accordingly, a long-term display like in Public Display (PD) application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.
- 1. Normal operating condition
 - Temperature: 0 ~ 40 °C
 - Operating Ambient Humidity: 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)

Note) Long-term static display can cause image sticking.

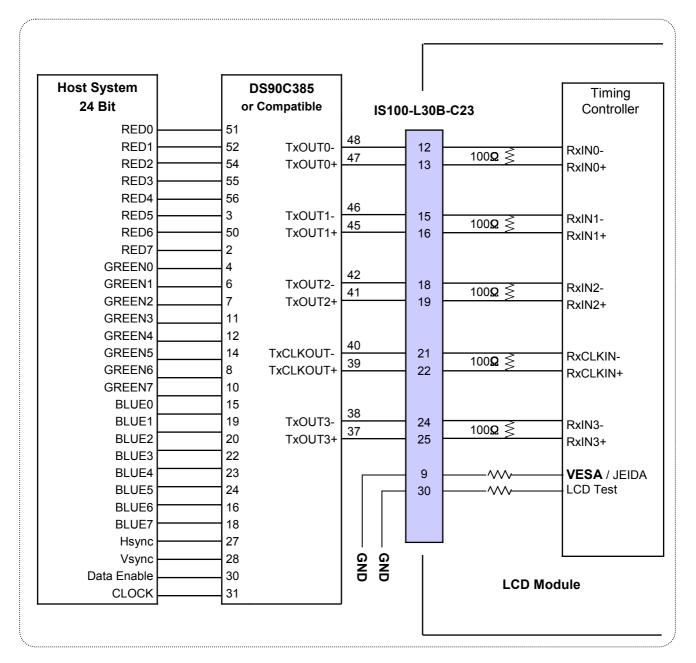
- 2. Operating usages under abnormal condition1
- a. Ambient condition
 - Well-ventilated place is recommended to set up PD system.
- b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.[3.2)]

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- 3. Operating usages to protect against image sticking due to long-term static display
- a. Suitable operating time: under 18 hours a day.
- b. Static information display recommended to use with moving image.
- Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- c. Background and character (image) color change
- Use different colors for background and character, respectively.
- Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when PD is used according to operating usages.

APPENDIX-I-1

■ Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="L")



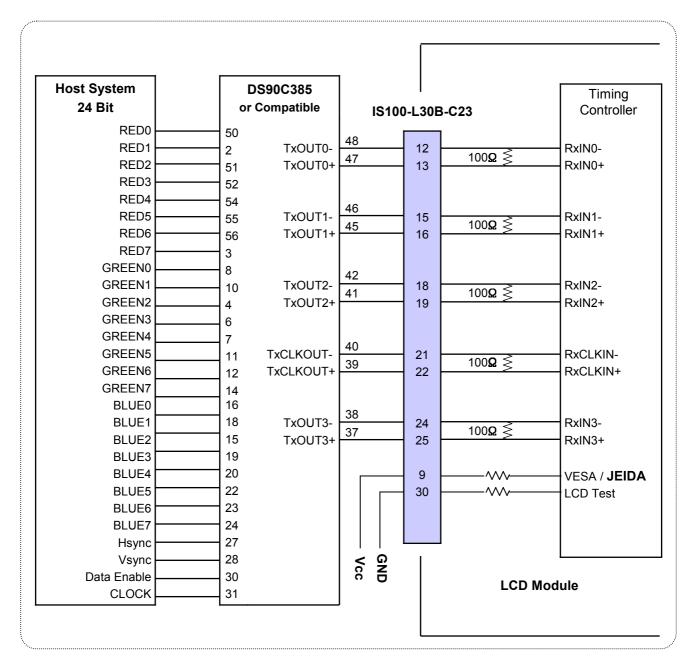
Notes:

- 1. The LCD module uses a 100 Ohm(Ω) resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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APPENDIX-I-2

■ Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="H")

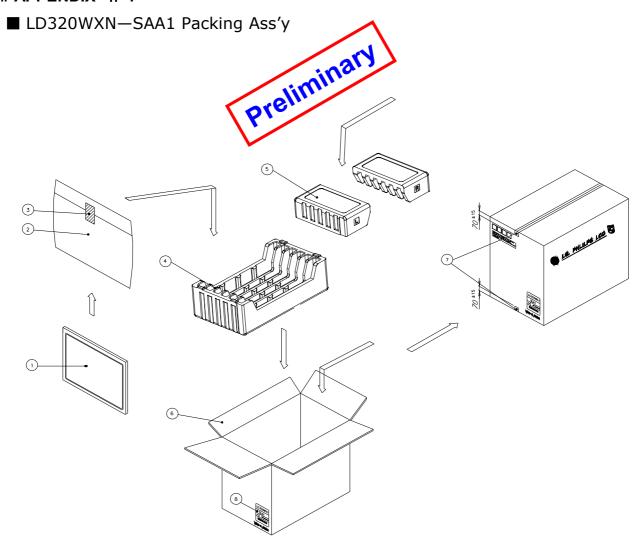


Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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APPENDIX- II-1



NO.	DESCRIPTION	MATERIAL		
1	LCD MODULE			
2	BAG	AL		
3	TAPE	MASKING 20MM X 50M		
4	PACKING, BOTTOM	EPS		
5	PACKING, TOP R_L	EPS		
6	BOX	PAPER_DW3		
7	TAPE	OPP 70MMX300M		
8	LABEL	YUPO PAPER 100X100		

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APPENDIX- II-2

Preliminary

3

1

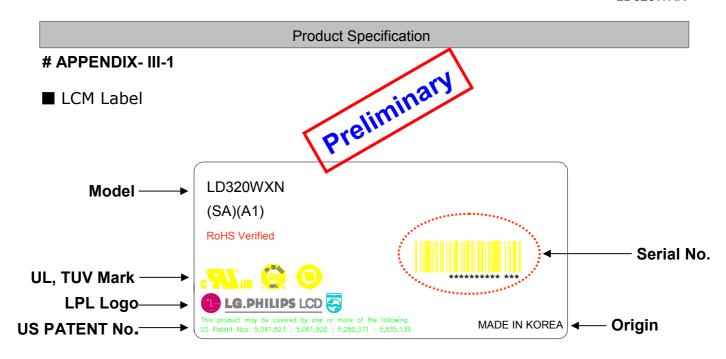
2

Box quantity per pallet: 2ea

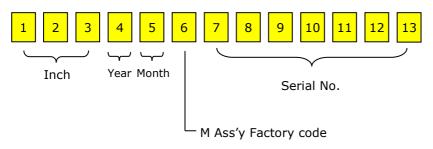
Pallet size: L1030 x W870 x H740

Pallet gross weight: 113.0kg

NO.	DESCRIPTION	MATERIAL		
1	PACKING ASS'Y			
2	PALLET	Plywood		
3	BAND	PP		
4	CLIP, BAND	STEEL		
5	ANGLE, Cover	PAPER (DW3)		
6	LABEL	PAPER		



■ Serial No. (See CAS 26page for more information)



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APPENDIX- III-2

■ Box Label

SAA1 6 PCS 001/01-01 MADE IN KOREA ROHS Verified

■ Pallet Label

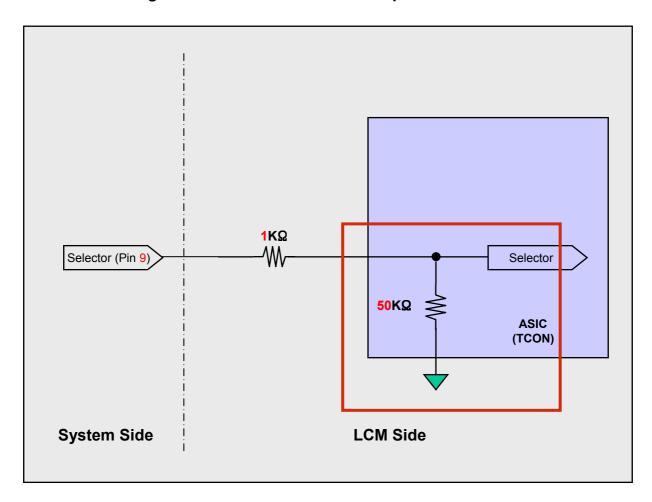


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APPENDIX- IV

■ Option Pin Circuit Block Diagram

Circuit Block Diagram of LVDS Format Selection pin



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