

Product Specification

3-2. Interface Connections

- LCD Connector(CN1) : GT123-20P-H16-LSS(Manufactured by LS Cable) or Equivalent
- Mating Connector : GT123-20S-CS(Manufactured by LS Cable) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No	Symbol	Description	Output Pin # (LVDS Tx)
1	VLCD	Power Supply +3.3V	
2	VLCD	Power Supply +3.3V	
3	GND	Power Ground.	
4	GND	Power Ground	
5	RXIN0-	LVDS Receiver Signal(-)	PIN#48
6	RXIN0+	LVDS Receiver Signal(+)	PIN#47
7	GND	Ground	
8	RXIN1-	LVDS Receiver Signal(-)	PIN#46
9	RXIN1+	LVDS Receiver Signal(+)	PIN#45
10	GND	Ground	
11	RXIN2-	LVDS Receiver Signal(-)	PIN#42
12	RXIN2+	LVDS Receiver Signal(+)	PIN#41
13	GND	Ground	
14	RXCLK IN-	LVDS Receiver Clock Signal(-)	PIN#40
15	RXCLK IN+	LVDS Receiver Clock Signal(+)	PIN#39
16	GND	Ground	
17	RXIN3-	LVDS Receiver Signal(-)	PIN#38
18	RXIN3+	LVDS Receiver Signal(+)	PIN#37
19	GND	Ground	
20	GND	Ground	Note 1

- Note:
1. 20nd Pin should be ground.
 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
 3. All VLCD (power input) pins should be connected together.
 4. Input Level of LVDS signal is based on the IEA 664 Standard.

Rear view of LCM

