SPECIFICATION FOR APPROVAL

(♦) Preliminary Specification
( ) Final Specification

<table>
<thead>
<tr>
<th>Title</th>
<th>19.0” SXGA TFT LCD</th>
</tr>
</thead>
<tbody>
<tr>
<td>BUYER</td>
<td></td>
</tr>
<tr>
<td>MODEL</td>
<td></td>
</tr>
<tr>
<td>SUPPLIER</td>
<td>LG.Philips LCD Co., Ltd.</td>
</tr>
<tr>
<td>*MODEL</td>
<td>LM190E05</td>
</tr>
<tr>
<td>SUFFIX</td>
<td>SL02</td>
</tr>
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</table>

*When you obtain standard approval, please use the above model name without suffix

<table>
<thead>
<tr>
<th>SIGNATURE</th>
<th>DATE</th>
<th>APPROVED BY</th>
<th>DATE</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>G.T. Kim / Manager</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>REVIEWED BY</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>K.J. Kwon / Manager</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>PREPARED BY</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>J.Y.Lee / Engineer</td>
<td></td>
</tr>
</tbody>
</table>

Please return 1 copy for your confirmation with your signature and comments.

Product Engineering Dept.
LG. Philips LCD Co., Ltd

Ver 0.1 Dec. 28, 2004 1 / 29
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### RECORD OF REVISIONS

<table>
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<th>Revision No</th>
<th>Date</th>
<th>Page</th>
<th>Description</th>
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<tr>
<td>Ver 0.0</td>
<td>Sep. 15. 2004</td>
<td>6, 7</td>
<td>Preliminary Specifications</td>
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<td>Ver 0.1</td>
<td>Dec. 27. 2004</td>
<td></td>
<td>*Power Consumption :</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.0 W → 4.3 W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>*Add Gray to Gray Response Time :</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Typ. : 14msec, Max. : 22msec</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>*Add Typical Spec. of Life Time :</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Typ. 52,000 Hrs (Reference value)</td>
</tr>
</tbody>
</table>

*Add Gray to Gray Response Time : Typ. 14msec, Max. 22msec

*Add Typical Spec. of Life Time : Typ. 52,000 Hrs (Reference value)
1. General Description

The LM190E05-SL02 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has a 19.0 inch diagonal measured active display area with SXGA resolution (1024 vertical by 1280 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16.7M colors.

The LM190E05-SL02 has been designed to apply the interface method that enables low power, high speed, low EMI. FPD Link or compatible must be used as a LVDS (Low Voltage Differential Signaling) chip.

The LM190E05-SL02 is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LM190E05-SL02 characteristics provide an excellent flat panel display for office automation products such as monitors.

<table>
<thead>
<tr>
<th>General Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Active screen size</td>
</tr>
<tr>
<td>Outline Dimension</td>
</tr>
<tr>
<td>Pixel Pitch</td>
</tr>
<tr>
<td>Pixel Format</td>
</tr>
<tr>
<td>Display Colors</td>
</tr>
<tr>
<td>Luminance, white</td>
</tr>
<tr>
<td>Viewing Angle (CR&gt;10)</td>
</tr>
<tr>
<td>Power Consumption</td>
</tr>
<tr>
<td>Weight</td>
</tr>
<tr>
<td>Display operating mode</td>
</tr>
<tr>
<td>Surface treatments</td>
</tr>
</tbody>
</table>
2. Absolute maximum ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Values</th>
<th>Units</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Input Voltage</td>
<td>$V_{LCD}$</td>
<td>-0.3</td>
<td>+14.0</td>
<td>$V_{dc}$ At 25°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>$T_{OP}$</td>
<td>0</td>
<td>+50</td>
<td>°C 1</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$T_{ST}$</td>
<td>-20</td>
<td>+60</td>
<td>°C 1</td>
</tr>
<tr>
<td>Operating Ambient Humidity</td>
<td>$H_{OP}$</td>
<td>10</td>
<td>+90</td>
<td>%RH 1</td>
</tr>
<tr>
<td>Storage Humidity</td>
<td>$H_{ST}$</td>
<td>10</td>
<td>+90</td>
<td>%RH 1</td>
</tr>
</tbody>
</table>

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.
3. Electrical specifications

3-1. Electrical characteristics

The LM190E05-SL02 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. Another which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

### Table 2. Electrical Characteristics

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Values</th>
<th>Units</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>MODULE:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Power Supply Input Voltage</td>
<td>( V_{LCD} )</td>
<td>10.8</td>
<td>12.0</td>
<td>13.2</td>
</tr>
<tr>
<td>Power Supply Input Current</td>
<td>( I_{LCD} )</td>
<td>-</td>
<td>0.36</td>
<td>0.38</td>
</tr>
<tr>
<td></td>
<td>( I_{LCD} )</td>
<td>-</td>
<td>0.47</td>
<td>0.50</td>
</tr>
<tr>
<td>Differential Impedance</td>
<td>( Z_m )</td>
<td>90</td>
<td>100</td>
<td>110</td>
</tr>
<tr>
<td>Power Consumption</td>
<td>( P_{LCD} )</td>
<td>-</td>
<td>4.3</td>
<td>4.8</td>
</tr>
<tr>
<td>Rush Current</td>
<td>( I_{RUSH} )</td>
<td>-</td>
<td>2.0</td>
<td>3.0</td>
</tr>
</tbody>
</table>

Notes:

1. The specified current and power consumption are under the \( V_{LCD}=12.0 \text{V}, 25 \pm 2^\circ \text{C}, f_v=60 \text{Hz} \) condition whereas mosaic pattern (8 x 6) is displayed and \( f_v \) is the frame frequency.
2. The current is specified at the maximum current pattern.
3. The duration of rush current is about 5ms and rising time of power Input is 500us ± 20%.

---

White : 255Gray
Black : 0Gray

[ Figure 3 ] Mosaic pattern (8X6) for power consumption measurement
Table 3. Electrical Characteristics

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Values</th>
<th>Units</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min.</td>
<td>Typ.</td>
<td>Max.</td>
</tr>
<tr>
<td>LAMP for each CCFL:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>$V_{BL}$</td>
<td>655</td>
<td>675</td>
<td>790</td>
</tr>
<tr>
<td>Operating Current</td>
<td>$I_{BL}$</td>
<td>3.0</td>
<td>7.0</td>
<td>7.5</td>
</tr>
<tr>
<td>Established Starting Voltage</td>
<td>$V_{BS}$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>at 25 °C</td>
<td></td>
<td>-</td>
<td>-</td>
<td>1100</td>
</tr>
<tr>
<td>at 0 °C</td>
<td></td>
<td>-</td>
<td>-</td>
<td>1400</td>
</tr>
<tr>
<td>Operating Frequency</td>
<td>$f_{BL}$</td>
<td>40</td>
<td>60</td>
<td>70</td>
</tr>
<tr>
<td>Discharge Stabilization Time</td>
<td>$T_{S}$</td>
<td>-</td>
<td>-</td>
<td>3</td>
</tr>
<tr>
<td>Power Consumption</td>
<td>$P_{BL}$</td>
<td>-</td>
<td>28.35</td>
<td>31.19</td>
</tr>
<tr>
<td>Life Time</td>
<td></td>
<td>45,000</td>
<td>(52,000)</td>
<td>-</td>
</tr>
</tbody>
</table>

Notes:

The design of the inverter must have specifications for the lamp in LCD Assembly. The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.

※ Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action.

Because leakage current is occurred between lamp wire and conducting tape.

1. Specified values are for a single lamp.
2. Operating voltage is measured at 25 ± 2°C. The variance of the voltage is ± 10%.
3. The voltage above $V_{S}$ should be applied to the lamps for more than 1 second for start-up.
   (Inverter open voltage must be more than lamp starting voltage.)

Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

4. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
5. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.
   $T_{S}$ is the time required for the brightness of the center of the lamp to be not less than 95%.
6. The lamp power consumption shown above does not include loss of external inverter.
   The used lamp current is the lamp typical current. ($P_{BL} = V_{BL} \times I_{BL} \times N_{Lamp}$)
7. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C.
8. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following. It shall help increase the lamp lifetime and reduce leakage current.

   a. The asymmetry rate of the inverter waveform should be less than 10%.
   b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.

   * Asymmetry rate:
     \[ \frac{|I_p - I_{-p}|}{I_{rms}} \times 100\% \]
   * Distortion rate
     \[ \frac{I_p (or I_{-p})}{I_{rms}} \]

9. The inverter which is combined with this LCM, is highly recommended to connect coupling (ballast) condenser at the high voltage output side. When you use the inverter which has not coupling (ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.

10. In case of edgy type back light with over 4 parallel lamps, input current and voltage wave form should be synchronized.
3-2. Interface Connections

Interface chip must be used LVDS, part No. SN75LVDS83 (Tx, Texas Instrument) or compatible. This LCD employs a interface connection, a 30 pin connector is used for the module electronics interface. Four 2pin connectors are used for the integral backlight system. The electronics interface connector is a model GT101-30S-H16 (LG Cable) or IN-30-BA 10 ( UJU Electronics) which is compatible. And mating connector is FI-X30H (JAE) or compatible. The pin configuration for the connector is shown in the table 4 and the signal mapping with LVDS transmitter is shown in the table 5.

<table>
<thead>
<tr>
<th>Pin No</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>RxO0-</td>
<td>LVDS Signal of Odd Channel 0(-)</td>
</tr>
<tr>
<td>2</td>
<td>RxO0+</td>
<td>LVDS Signal of Odd Channel 0(+)</td>
</tr>
<tr>
<td>3</td>
<td>RxO1-</td>
<td>LVDS Signal of Odd Channel 1(-)</td>
</tr>
<tr>
<td>4</td>
<td>RxO1+</td>
<td>LVDS Signal of Odd Channel 1(+)</td>
</tr>
<tr>
<td>5</td>
<td>RxO2-</td>
<td>LVDS Signal of Odd Channel 2(-)</td>
</tr>
<tr>
<td>6</td>
<td>RxO2+</td>
<td>LVDS Signal of Odd Channel 2(+)</td>
</tr>
<tr>
<td>7</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>8</td>
<td>RxOC-</td>
<td>LVDS Signal of Odd Channel Clock(-)</td>
</tr>
<tr>
<td>9</td>
<td>RxOC+</td>
<td>LVDS Signal of Odd Channel Clock(+)</td>
</tr>
<tr>
<td>10</td>
<td>RxO3-</td>
<td>LVDS Signal of Odd Channel 3(-)</td>
</tr>
<tr>
<td>11</td>
<td>RxO3+</td>
<td>LVDS Signal of Odd Channel 3(+)</td>
</tr>
<tr>
<td>12</td>
<td>RxE0-</td>
<td>LVDS Signal of Even Channel 0(-)</td>
</tr>
<tr>
<td>13</td>
<td>RxE0+</td>
<td>LVDS Signal of Even Channel 0(+)</td>
</tr>
<tr>
<td>14</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>15</td>
<td>RxE1-</td>
<td>LVDS Signal of Even Channel 1(-)</td>
</tr>
<tr>
<td>16</td>
<td>RxE1+</td>
<td>LVDS Signal of Even Channel 1(+)</td>
</tr>
<tr>
<td>17</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>18</td>
<td>RxE2-</td>
<td>LVDS Signal of Even Channel 2(-)</td>
</tr>
<tr>
<td>19</td>
<td>RxE2+</td>
<td>LVDS Signal of Even Channel 2(+)</td>
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<tr>
<td>20</td>
<td>RxEC-</td>
<td>LVDS Signal of Even Channel Clock(-)</td>
</tr>
<tr>
<td>21</td>
<td>RxEC+</td>
<td>LVDS Signal of Even Channel Clock(+)</td>
</tr>
<tr>
<td>22</td>
<td>RxE3-</td>
<td>LVDS Signal of Even Channel 3(-)</td>
</tr>
<tr>
<td>23</td>
<td>RxE3+</td>
<td>LVDS Signal of Even Channel 3(+)</td>
</tr>
<tr>
<td>24</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>25</td>
<td>NC</td>
<td>No connection</td>
</tr>
<tr>
<td>26</td>
<td>NC</td>
<td>No connection</td>
</tr>
<tr>
<td>27</td>
<td>NC</td>
<td>No connection</td>
</tr>
<tr>
<td>28</td>
<td>VCC</td>
<td>Power supply (12.0V Typ.)</td>
</tr>
<tr>
<td>29</td>
<td>VCC</td>
<td>Power supply (12.0V Typ.)</td>
</tr>
<tr>
<td>30</td>
<td>VCC</td>
<td>Power supply (12.0V Typ.)</td>
</tr>
</tbody>
</table>

First Pixel Data

Second Pixel Data
Notes:  
1. All GND (ground) pins should be connected together and should also be connected to the LCD’s metal frame.
2. All V_{LCD} (power input) pins should be connected together.
3. All NC pins should be separated from other signal or power.
### Table 5. Required signal assignment for Flat Link (TI:SN75LVDS83) Transmitter

<table>
<thead>
<tr>
<th>Pin</th>
<th>Pin Name</th>
<th>Require Signal</th>
<th>Pin</th>
<th>Pin Name</th>
<th>Require Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VCC</td>
<td>Power Supply for TTL Input</td>
<td>29</td>
<td>GND</td>
<td>Ground pin for TTL</td>
</tr>
<tr>
<td>2</td>
<td>D5</td>
<td>TTL Input(R7)</td>
<td>30</td>
<td>D26</td>
<td>TTL Input(DE)</td>
</tr>
<tr>
<td>3</td>
<td>D6</td>
<td>TTL Input(R5)</td>
<td>31</td>
<td>TxCLKIN</td>
<td>TTL Level clock Input</td>
</tr>
<tr>
<td>4</td>
<td>D7</td>
<td>TTL Input(G0)</td>
<td>32</td>
<td>PWR DWN</td>
<td>Power Down Input</td>
</tr>
<tr>
<td>5</td>
<td>GND</td>
<td>Ground pin for TTL</td>
<td>33</td>
<td>PLL GND</td>
<td>Ground pin for PLL</td>
</tr>
<tr>
<td>6</td>
<td>D8</td>
<td>TTL Input(G1)</td>
<td>34</td>
<td>PLL VCC</td>
<td>Power Supply for PLL</td>
</tr>
<tr>
<td>7</td>
<td>D9</td>
<td>TTL Input(G2)</td>
<td>35</td>
<td>PLL GND</td>
<td>Ground pin for PLL</td>
</tr>
<tr>
<td>8</td>
<td>D10</td>
<td>TTL Input(G6)</td>
<td>36</td>
<td>LVDS GND</td>
<td>Ground pin for LVDS</td>
</tr>
<tr>
<td>9</td>
<td>VCC</td>
<td>Power Supply for TTL Input</td>
<td>37</td>
<td>TxOUT3+</td>
<td>Positive LVDS differential data output3</td>
</tr>
<tr>
<td>10</td>
<td>D11</td>
<td>TTL Input(G7)</td>
<td>38</td>
<td>TxOUT3-</td>
<td>Negative LVDS differential data output3</td>
</tr>
<tr>
<td>11</td>
<td>D12</td>
<td>TTL Input(G3)</td>
<td>39</td>
<td>TxCLKOUT+</td>
<td>Positive LVDS differential clock output</td>
</tr>
<tr>
<td>12</td>
<td>D13</td>
<td>TTL Input(G4)</td>
<td>40</td>
<td>TxCLKOUT-</td>
<td>Negative LVDS differential clock output</td>
</tr>
<tr>
<td>13</td>
<td>GND</td>
<td>Ground pin for TTL</td>
<td>41</td>
<td>TxOUT2+</td>
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<td>14</td>
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<td>D15</td>
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<td>LVDS GND</td>
<td>Ground pin for TTL</td>
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<td>D20</td>
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<td>D27</td>
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<td>TTL Input(B4)</td>
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<td>D0</td>
<td>TTL Input(R0)</td>
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<td>24</td>
<td>D22</td>
<td>TTL Input(B5)</td>
<td>52</td>
<td>D1</td>
<td>TTL Input(R1)</td>
</tr>
<tr>
<td>25</td>
<td>D23</td>
<td>TTL Input(RSVD)</td>
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<td>GND</td>
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<td>TTL Input(R2)</td>
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<td>27</td>
<td>D24</td>
<td>TTL Input(HSYNC)</td>
<td>55</td>
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<td>TTL Input(R3)</td>
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<td>28</td>
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<td>TTL Input(VSYNC)</td>
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<td>TTL Input(R4)</td>
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**Notes:**
1. Refer to LVDS Transmitter Data Sheet for detail descriptions.
2. 7 means MSB and 0 means LSB at R,G,B pixel data
The backlight interface connector is a model BHR-05VS-1(CN2/CN5) manufactured by JST and 1674817-1 (CN3/CN4) manufactured by AMP. The mating connector part number are SM02B-BHSS-1-TB(2pin), SM04(9-E2)B-BHS-1-TB(5pin) or equivalent. The pin configuration for the connector is shown in the table below.

**Table 6. Backlight connector pin configuration**

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<thead>
<tr>
<th>No</th>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Notes</th>
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<td>Power supply for lamp 1(High voltage side)</td>
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<td>2</td>
<td>HV</td>
<td>Power supply for lamp 2(High voltage side)</td>
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<td>NC</td>
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<td>CN2</td>
<td>4</td>
<td>LV</td>
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<td></td>
</tr>
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<td>CN2</td>
<td>5</td>
<td>LV</td>
<td>Power supply for lamp 2(Low voltage side)</td>
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<tr>
<td>CN3</td>
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<td>HV</td>
<td>Power supply for lamp 3(High voltage side)</td>
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<td>CN3</td>
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<td>LV</td>
<td>Power supply for lamp 3(Low voltage side)</td>
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<td>CN4</td>
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<td>CN4</td>
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<td>LV</td>
<td>Power supply for lamp 4(Low voltage side)</td>
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<td>CN5</td>
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<td>HV</td>
<td>Power supply for lamp 6(High voltage side)</td>
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<td>HV</td>
<td>Power supply for lamp 5(High voltage side)</td>
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[ Figure 5 ] Backlight connector view
3-3. Signal Timing Specifications

This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 7. Timing table

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<th>Parameter</th>
<th>Symbol</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
<th>Notes</th>
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<td>64.0</td>
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<td>76</td>
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<td>-</td>
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</table>

Notes: 1. DE mode operation
2. tHFP + tWH + tHBP < (1/2) tHV
3. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
4. tHFP, tWH and tHBP should be any times of a character number (8).
3-4. Signal Timing Waveforms

[Figure 6] Signal timing waveforms
3-5. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. Color data reference

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Ver 0.1 Dec. 28, 2004 15 / 29
3-6. Power Sequence

Table 9. Power sequence time delay

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<th>Values</th>
<th>Units</th>
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</tr>
</tbody>
</table>

Notes:
1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD Vₜₖₑᵈₜ to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signals are valid.
4. Optical Specifications

Optical characteristics are determined after the unit has been ‘ON’ and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are measured at an approximate distance 50cm from the LCD surface at a viewing angle of $\Phi$ and $\delta$ equal to 0 °.

Figure. 9 presents additional information concerning the measurement equipment and method.

![Optical Stage(x,y)](image)

**Table 10. Optical characteristics** (Ta=25 °C, $V_{L_{\text{LCD}}}$=12.0V, $V_{s}=60$Hz Dclk=54MHz, $I_{BL}=7.0$mArms)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Values</th>
<th>Units</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min.</td>
<td>Typ.</td>
<td>Max.</td>
</tr>
<tr>
<td>Contrast ratio</td>
<td>CR</td>
<td>400</td>
<td>600</td>
<td>-</td>
</tr>
<tr>
<td>Surface luminance, white</td>
<td>$L_{\text{WH}}$</td>
<td>230</td>
<td>280</td>
<td>-</td>
</tr>
<tr>
<td>Luminance uniformity</td>
<td>$\delta_{\text{WHITE}}$</td>
<td>-</td>
<td>-</td>
<td>1.33</td>
</tr>
<tr>
<td>Response time</td>
<td>$T_{R}$</td>
<td>18</td>
<td>30</td>
<td>-</td>
</tr>
<tr>
<td>Rise time</td>
<td>$T_{T_{R}}$</td>
<td>-</td>
<td>7</td>
<td>12</td>
</tr>
<tr>
<td>Decay time</td>
<td>$T_{D}$</td>
<td>-</td>
<td>11</td>
<td>18</td>
</tr>
<tr>
<td>Gray to Gray</td>
<td>$T_{\text{GTG, AVG}}$</td>
<td>-</td>
<td>14</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>$T_{\text{GTG, MAX}}$</td>
<td>22</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>CIE color coordinates</td>
<td>XR</td>
<td>0.610</td>
<td>0.640</td>
<td>0.670</td>
</tr>
<tr>
<td>Red</td>
<td>YR</td>
<td>0.314</td>
<td>0.344</td>
<td>0.374</td>
</tr>
<tr>
<td>Green</td>
<td>XG</td>
<td>0.260</td>
<td>0.290</td>
<td>0.320</td>
</tr>
<tr>
<td></td>
<td>YG</td>
<td>0.576</td>
<td>0.608</td>
<td>0.638</td>
</tr>
<tr>
<td>Blue</td>
<td>XB</td>
<td>0.115</td>
<td>0.145</td>
<td>0.175</td>
</tr>
<tr>
<td></td>
<td>YB</td>
<td>0.044</td>
<td>0.074</td>
<td>0.104</td>
</tr>
<tr>
<td>White</td>
<td>XW</td>
<td>0.283</td>
<td>0.313</td>
<td>0.343</td>
</tr>
<tr>
<td></td>
<td>YW</td>
<td>0.299</td>
<td>0.329</td>
<td>0.359</td>
</tr>
<tr>
<td>Color Shift</td>
<td>$\theta_{\text{CST, H}}$</td>
<td>-</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>Horizontal</td>
<td>$\theta_{\text{CST, V}}$</td>
<td>-</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>Vertical</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Viewing angle</td>
<td>$\theta_{\text{H}}$</td>
<td>170</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>General</td>
<td>$\theta_{\text{V}}$</td>
<td>170</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>Effective</td>
<td>$\theta_{\text{GMA, H}}$</td>
<td>170</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>Horizontal</td>
<td>$\theta_{\text{GMA, V}}$</td>
<td>170</td>
<td>178</td>
<td>-</td>
</tr>
<tr>
<td>Vertical</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Gray scale</td>
<td></td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
Notes:

1. Contrast ratio (CR) is defined mathematically as:
   \[
   \text{Contrast ratio} = \frac{\text{Surface luminance with all white pixels}}{\text{Surface luminance with all black pixels}}
   \]
   It is measured at center point.

2. Average Luminance \(L_{WH}\) is luminance value at location P1 with all pixels displaying white.

3. The variation in surface luminance, \(\delta_{WHITE}\) is defined as:
   \[
   \delta_{WHITE} = \frac{\text{Maximum (P1, P2, P3, ..., P9)}}{\text{Minimum (P1, P2, P3, ..., P9)}}
   \]
   Where P1 to P9 are the luminance with all pixels displaying white at 9 locations.
Notes:

4. **The response time** is defined as the following figure and shall be measured by switching the input signal for “black” and “white”. Response time is the time required for the display to transition from black to white (Rise Time, $T_{RR}$) and from white to black (Decay Time, $T_{RD}$).

![Response Time Diagram](image)

5. **The Gray to Gray response time** is defined as the following figure and shall be measured by switching the input signal for “Gray To Gray”.
   - Gray step: 5 Step
   - $T_{GTG_{AVR}}$ is the total average time at rising time and falling time for “Gray To Gray”.
   - $T_{GTG_{MAX}}$ is the max time at rising time or falling time for "Gray To Gray".

<table>
<thead>
<tr>
<th>Gray to Gray</th>
<th>Rising Time</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>G255</td>
</tr>
<tr>
<td>Falling Time</td>
<td>G255</td>
</tr>
<tr>
<td></td>
<td>G191</td>
</tr>
<tr>
<td></td>
<td>G127</td>
</tr>
<tr>
<td></td>
<td>G63</td>
</tr>
<tr>
<td></td>
<td>G0</td>
</tr>
</tbody>
</table>
6. **Color shift** is the angle at which the color difference is lower than 0.04.

- Color difference($\Delta u'v'$)

\[
u' = \frac{4x}{-2x + 12y + 3} \\
v' = \frac{9y}{-2x + 12y + 3}
\]

\[
\Delta u'v' = \sqrt{(u'_1-u'_2)^2+(v'_1-v'_2)^2}
\]

- Pattern size : 25% Box size
- Viewing angle direction of color shift : Horizontal, Vertical

| AVERAGE RGB VALUES IN BRUCE RGB FOR MACBETH CHART |
|---------------------------------|-------------------|-------------------|-------------------|-------------------|-------------------|
| dark skin | light skin | blue sky | foliage | blue flower | bluish green |
| R | G | B | R | G | B | R | G | B | R | G | B |
| 88 | 206 | 85 | 77 | 128 | 114 |
| 56 | 142 | 112 | 102 | 118 | 199 |
| 45 | 123 | 161 | 48 | 185 | 176 |
| orange | purple blue | moderate red | purple | yellow green | orange yellow |
| R | G | B | R | G | B | R | G | B | R | G | B |
| 219 | 90 | 211 | 76 | 180 | 230 |
| 104 | 89 | 67 | 39 | 183 | 182 |
| 24 | 174 | 87 | 86 | 58 | 29 |
| blue | green | red | yellow | magenta | cyan |
| R | G | B | R | G | B | R | G | B | R | G | B |
| 20 | 72 | 197 | 241 | 207 | 35 |
| 32 | 148 | 27 | 212 | 62 | 128 |
| 145 | 85 | 37 | 36 | 151 | 172 |
| white | neutral 8 | neutral 6,5 | neutral 5 | neutral 3,5 | black |
| R | G | B | R | G | B | R | G | B | R | G | B |
| 240 | 206 | 155 | 110 | 63 | 22 |
| 240 | 206 | 155 | 110 | 63 | 22 |
| 240 | 206 | 155 | 110 | 63 | 22 |

( Test Pattern : Macbeth Chart )
7. **Viewing angle (general)** is the angle at which the contrast ratio is greater than 10.

8. **Effective viewing angle** is the angle at which the gamma shift of gray scale is lower than 0.3.

\[ L = aV^\gamma + L_0 \]
\[ \log(L - L_0) = r \log(V) + \log(a) \]

Here the Parameter \( \alpha \) and \( \gamma \) relate the signal level \( V \) to the luminance \( L \).

The GAMMA we calculate from the log-log representation (Fig. 2)

<table>
<thead>
<tr>
<th>Gray Level</th>
<th>Relative Luminance [%] (Typ.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0.3</td>
</tr>
<tr>
<td>31</td>
<td>1.41</td>
</tr>
<tr>
<td>63</td>
<td>5.59</td>
</tr>
<tr>
<td>95</td>
<td>14.2</td>
</tr>
<tr>
<td>127</td>
<td>24.7</td>
</tr>
<tr>
<td>159</td>
<td>38.4</td>
</tr>
<tr>
<td>191</td>
<td>55.3</td>
</tr>
<tr>
<td>223</td>
<td>78.9</td>
</tr>
<tr>
<td>255</td>
<td>100</td>
</tr>
</tbody>
</table>
5. Mechanical Characteristics
Table 11. provides general mechanical characteristics for the model LM190E05-SL02. Please refer to Figure 15,16 regarding the detailed mechanical drawing of the LCD.

<table>
<thead>
<tr>
<th>Table 12. Mechanical characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Outside dimensions</strong></td>
</tr>
<tr>
<td>Horizontal</td>
</tr>
<tr>
<td>Vertical</td>
</tr>
<tr>
<td>Depth</td>
</tr>
<tr>
<td><strong>Bezel area</strong></td>
</tr>
<tr>
<td>Horizontal</td>
</tr>
<tr>
<td>Vertical</td>
</tr>
<tr>
<td><strong>Active display area</strong></td>
</tr>
<tr>
<td>Horizontal</td>
</tr>
<tr>
<td>Vertical</td>
</tr>
<tr>
<td><strong>Weight (approximate)</strong></td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td><strong>Surface Treatment</strong></td>
</tr>
<tr>
<td>Hard coating(3H)</td>
</tr>
<tr>
<td>Anti-glare treatment of the front polarizer</td>
</tr>
</tbody>
</table>
Figure 15. Front view
Figure 15. Rear view

Notes:
1. Backlight: 6 Cold Cathode Fluorescent Lamps
2. V-Connector Specification: 07181-3054-1892/03-PA 6(LLL)
3. Lamp Connector Specification
   - 9P-3055-12(1ST) or Compatible
   - 1074.97=18(1ST) or Compatible
4. Torque of screw hole: 0.5x6.9867 cm
5. Tilt and position deviation tolerance of display area as follows:
   (a) Y-Direction: 0 (max) <= 0.5
   (b) X-Direction: 0 (max) <= 1.0

6. Lamp(CDF.) No. is marked at backlight connector

7. Do not apply conductive tape around the backlight wires.
8. Unprotected area to be 3.0x3.0mm.
6. Reliability

Table 12. Environment test condition

<table>
<thead>
<tr>
<th>No.</th>
<th>Test item</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>High temperature storage test</td>
<td>Ta= 60°C 240h</td>
</tr>
<tr>
<td>2</td>
<td>Low temperature storage test</td>
<td>Ta= -20°C 240h</td>
</tr>
<tr>
<td>3</td>
<td>High temperature operation test</td>
<td>Ta= 50°C 50%RH 240h</td>
</tr>
<tr>
<td>4</td>
<td>Low temperature operation test</td>
<td>Ta= 0°C 240h</td>
</tr>
<tr>
<td>5</td>
<td>Vibration test (non-operating)</td>
<td>Wave form: random</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Vibration level: 1.0G RMS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Bandwidth: 10-500Hz</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Duration: X,Y,Z, 20 min.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>One time each direction</td>
</tr>
<tr>
<td>6</td>
<td>Shock test (non-operating)</td>
<td>Shock level: 100G</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Waveform: half sine wave, 2ms</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Direction: ±X, ±Y, ±Z</td>
</tr>
<tr>
<td></td>
<td></td>
<td>One time each direction</td>
</tr>
<tr>
<td>7</td>
<td>Altitude storage / shipment operation</td>
<td>0 - 40,000 feet (12,192m)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0 - 10,000 feet (3,048m)</td>
</tr>
</tbody>
</table>

{ Result evaluation criteria }
There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.
7. International Standards

7-1. Safety

c) EN 60950 : 2000, Third Edition
   IEC 60950 : 1999, Third Edition
   European Committee for Electrotechnical Standardization(CENELEC)
   EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz.” American National Standards Institute(ANSI), 1992
8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
<th>F</th>
<th>G</th>
<th>H</th>
<th>I</th>
<th>J</th>
<th>K</th>
<th>L</th>
<th>M</th>
</tr>
</thead>
</table>

Note

1. **YEAR**

<table>
<thead>
<tr>
<th>Year</th>
<th>2001</th>
<th>2002</th>
<th>2003</th>
<th>2004</th>
<th>2005</th>
<th>2006</th>
<th>2007</th>
<th>2008</th>
<th>2009</th>
<th>2010</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mark</td>
<td>1</td>
<td>2</td>
<td>3</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>7</td>
<td>8</td>
<td>9</td>
<td>0</td>
</tr>
</tbody>
</table>

2. **MONTH**

<table>
<thead>
<tr>
<th>Month</th>
<th>Jan</th>
<th>Feb</th>
<th>Mar</th>
<th>Apr</th>
<th>May</th>
<th>Jun</th>
<th>Jul</th>
<th>Aug</th>
<th>Sep</th>
<th>Oct</th>
<th>Nov</th>
<th>Dec</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mark</td>
<td>1</td>
<td>2</td>
<td>3</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>7</td>
<td>8</td>
<td>9</td>
<td>A</td>
<td>B</td>
<td>C</td>
</tr>
</tbody>
</table>

3. **FACTORY CODE**

<table>
<thead>
<tr>
<th>Factory Code</th>
<th>LPL Gumi</th>
<th>LPL Nanjing</th>
<th>HEESUNG</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mark</td>
<td>K</td>
<td>C</td>
<td>D</td>
</tr>
</tbody>
</table>

4. **SERIAL NO.**

| Mark | 100001~199999, 200001~299999, 300001~399999, ..., A00001~A99999, ..., Z00001~Z99999 |

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 6 pcs

b) Box size: 533mm X 310mm X 459mm.
9. Precautions

Please pay attention to the following when you use this TFT LCD module.

9-1. Mounting Precautions

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach a transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength to resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not described because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dusty clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are determined to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: $V=\pm 200mV$ (Over and under shoot voltage)
(2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
(7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
(8) A screw which is fastened up the steels should be a machine screw (if not, it causes metal foreign material and deal LCM a fatal blow)
(9) Please do not set LCD on its edge.
9-3. Electrostatic Discharge Control
Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don’t touch interface pin directly.

9-4. Precautions for Strong Light Exposure
Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage
When storing modules as spares for a long time, the following precautions are necessary.
(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
   It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film
(1) The protection film is attached to the bezel with a small masking tape.
   When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the Bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.